

Evaluating the **ADPA7007** 20 GHz to 44 GHz, GaAs, pHEMT, 31.5 dBm (>1 W), MMIC Power Amplifier

FEATURES

2-layer Rogers 4350B evaluation board with heat sink
 End launch 2.9 mm RF connectors
 Through calibration path

EVALUATION KIT CONTENTS

2-layer Rogers 4350B evaluation board with heat sink

EQUIPMENT NEEDED

RF signal generator
 RF spectrum analyzer
 RF network analyzer
 5 V, 2.5 A power supply
 Externally biased mode, -1.5 V, 100 mA power supply

GENERAL DESCRIPTION

The ADPA7007-EVALZ consists of a 2-layer printed circuit board (PCB) fabricated from 10 mil thick Rogers 4350B copper clad mounted to an aluminum heat sink. The ADPA7007-EVALZ uses the same PCB as the [ADPA7005-EVALZ](#) but with the ADPA7007 populated on the board. The heat sink assists in providing thermal relief to the [ADPA7007](#) as well as mechanical support to the PCB. Mounting holes on the heat sink allow it to be attached to larger heat sinks for improved thermal management. The RFIN and RFOUT ports are populated by 2.9 mm female coaxial connectors and their respective RF traces are of 50 Ω characteristic impedance. The board is populated with components suitable for use over the entire operating temperature range of the device.

RF traces are 50 Ω grounded coplanar waveguide. Package ground leads and the exposed pad connect directly to the ground plane. Multiple vias are used to connect the top and bottom ground planes, with particular focus on the area directly beneath the ground paddle to provide adequate electrical conduction and thermal conduction to the heat sink.

The power supply decoupling capacitors on the evaluation board represent the configuration that was used to characterize and qualify the device. The number of capacitors can be reduced but this scope varies from system to system. The general guidance is to first remove or combine the largest capacitors that are farthest from the device.

Consult the ADPA7007 data sheet in conjunction with this user guide when working with the ADPA7007-EVALZ board.

ADPA7007-EVALZ EVALUATION BOARD PHOTOGRAPHS

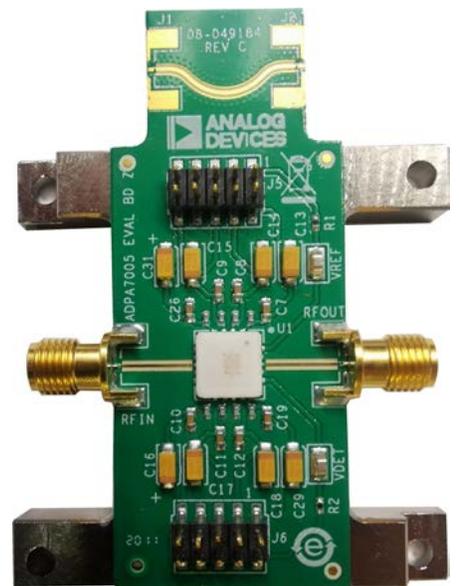


Figure 1. Primary Side

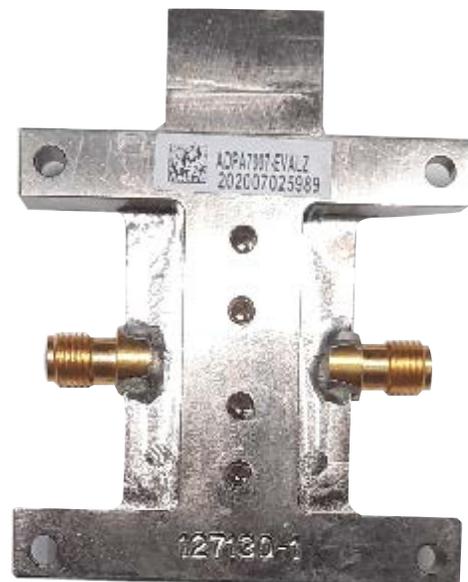


Figure 2. Secondary Side

TABLE OF CONTENTS

Features.....	1	Operating the ADPA7007-EVALZ.....	3
Evaluation Kit Contents	1	Through Calibration Path.....	3
Equipment Needed.....	1	Evaluation Board Schematic and Artwork	4
General Description.....	1	Ordering Information.....	5
ADPA7007-EVALZ Evaluation Board Photographs.....	1	Bill of Materials	5
Revision History	2		

REVISION HISTORY

10/2020—Revision 0: Initial Version

OPERATING THE ADPA7007-EVALZ

The ADPA7007 operates in externally biased mode. For the externally biased configuration, adjust V_{GG1} within -1.5 V to 0 V through J5 and V_{GG2} within -1.5 V to 0 V through the J6 header to set the target drain current.

See the ADPA7007 data sheet for bias sequencing information.

Ground and gate control voltages are accessed through two 10-pin headers (see Table 1 for the header pinout).

Table 1. J5 and J6 Header Connections to the ADPA7007

Header Pin No.	ADPA7007 Pin
J5	
1	VREF
2, 4, 6	$V_{DD1}, V_{DD3}, V_{DD5}$, respectively
10	V_{GG1}
3, 5, 7, 8, 9 ¹	GND ¹
J6	
2	VDET
1, 3, 5	$V_{DD2}, V_{DD4}, V_{DD6}$, respectively
9	V_{GG2}
4, 6, 7, 8, 10 ¹	GND ¹

¹ These header pins are connected to all GND pins and the exposed pad of the ADPA7007 on the evaluation board.

THROUGH CALIBRATION PATH

To calibrate out board trace losses, a through (thru) calibration path is provided between J1 and J2. J1 and J2 must be populated with RF connectors to use the through calibration path.

Figure 3 shows the plot of the data in Table 2 of the through calibration path (J1 to J2). See Figure 4 for the evaluation board schematic.

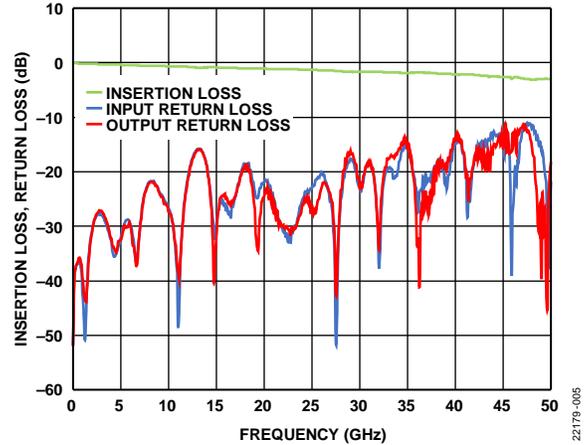


Figure 3. Insertion Loss and Return Loss of Through Calibration Path

Table 2. Insertion Loss and Return Loss of Through Calibration Path

Frequency (GHz)	Insertion Loss (dB)
0.01	-0.01
1	-0.14
5	-0.35
10	-0.60
15	-0.84
20	-1.06
25	-1.29
30	-1.58
35	-1.82
40	-2.1
45	-2.59
50	-3.03

EVALUATION BOARD SCHEMATIC AND ARTWORK

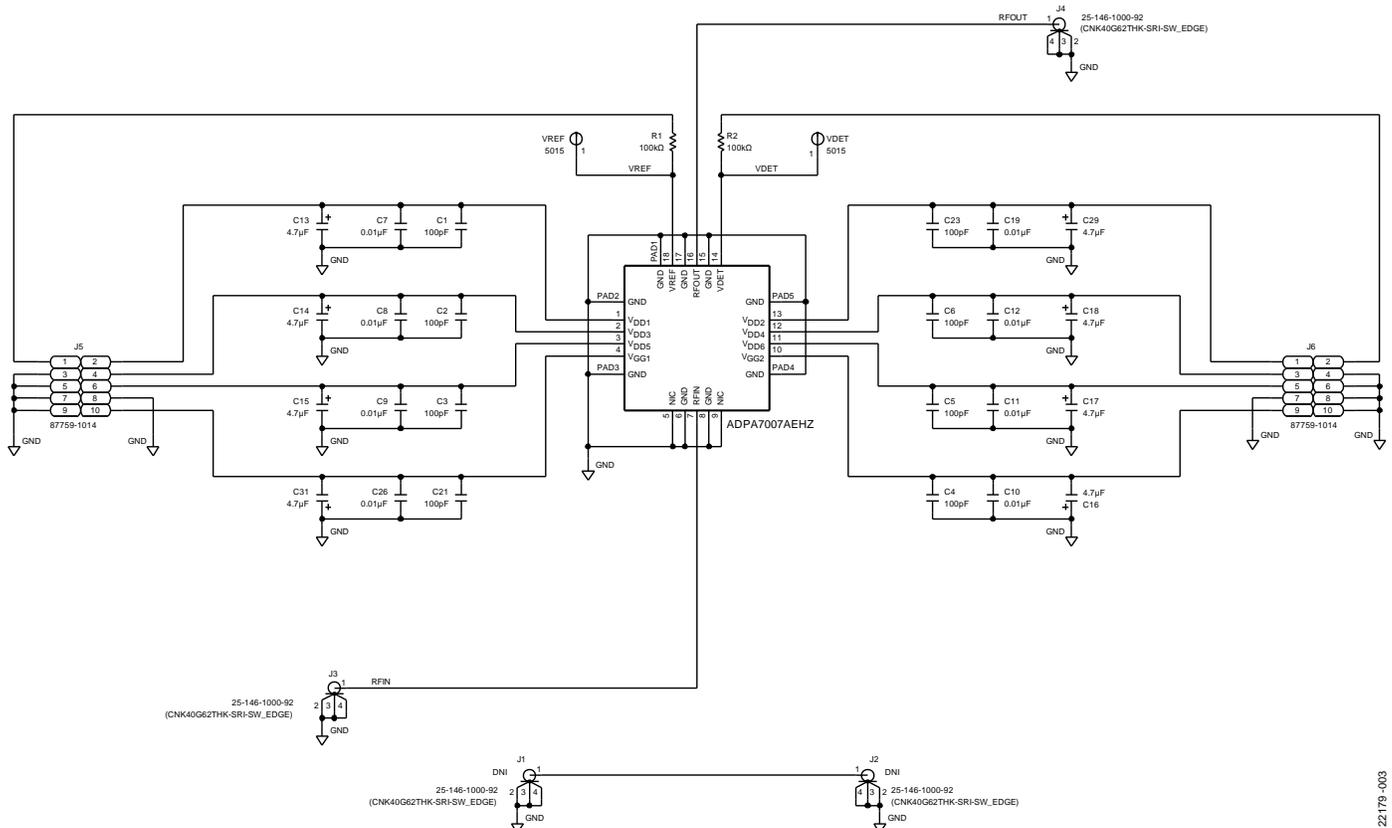


Figure 4. ADPA7007-EVALZ Evaluation Board Schematic

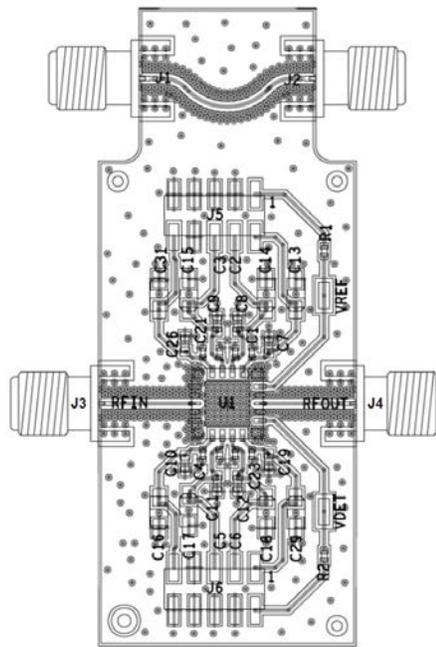


Figure 5. ADPA7007-EVALZ Assembly Drawing (Note That J1 and J2 Are Not Installed)

ORDERING INFORMATION

BILL OF MATERIALS

Table 3.

Reference Designator	Description	Manufacturer	Part Number
C1, C2, C3, C4, C5, C6, C21, C23	Ceramic capacitor C0G, 100 pF	Kemet	C0402C101J5GACTU
C7, C8, C9, C10, C11, C12, C19, C26	Ceramic capacitor X7R, 0.01 μ F	Yageo	CC0603KRX7R9BB103
C13, C14, C15, C16, C17, C18, C29, C31	Tantalum capacitor, 4.7 μ F	AVX	TAJA475K020RNJ
J1, J2	Connectors, 2.9 mm, K jack edge, not installed	SRI Connector Gage Co	25-146-1000-92
J3, J4	Connectors, 2.9 mm, K jack edge	SRI Connector Gage Co	25-146-1000-92
J5, J6	Connector, PCB header, vertical, dual row, 10-position, 2 mm pitch	Molex	87759-1014
R1, R2	Resistor, precision, thick film chip, 100 k Ω	Panasonic	ERJ-2RKF1003X
U1	Gallium arsenide (GaAs), pseudomorphic high electron mobility transfer (pHEMT), monolithic microwave integrated circuit (MMIC), >1 W, 20 GHz to 44 GHz power amplifier	Analog Devices, Inc.	ADPA7007AEHZ
Not applicable	Aluminum heat sink, 02.51 in. \times 1.9 in.	Not applicable	Not applicable



ESD Caution

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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