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USING GMSL SERDES DEVICES IN A DUAL AUTOMOTIVE ELECTRONIC CONTROL UNIT (ECU) APPLICATION

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Abstract: Maxim's gigabit multimedia serial link (GMSL) solution serializes digital video and audio data across a single twisted-pair cable. In addition, an integrated bidirectional control channel enables a single microcontroller (μ C) to program the serializer, deserializer, and all connected peripherals. For typical applications, this eliminates the remote-side microcontroller and its support components, such as a clock source/crystal and a low-voltage power supply. This not only simplifies the design of the remote device, but also reduces the cost, size, and power consumption as well. There are times, however, when a μ C already resides on both sides of the link, due to other design requirements not related to the GMSL. This application note describes how to connect two μ Cs to control the GMSL.

Basic Dual µC Use

Usually when using a single μ C, both the control-direction selection (CDS) pins on the serializer/deserializer are set low for a serializer-side μ C, or high for a deserializer-side μ C. However, if CDS on the serializer is set low and CDS on the deserializer is high, each GMSL chip can connect to their respective μ Cs simultaneously (**Figure 1**).

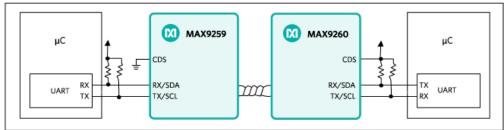


Figure 1. Simplified dual µC application schematic with CDS settings shown.

Internal Operation

When using two μ Cs, both the serializer's and deserializer's l²C masters are disabled, and RX/SDA and TX/SDL are configured as a UART interface with their corresponding μ Cs. Since each device is operating as a local side device, they cannot enter sleep mode. Use the respective active-low PWDN pins to put each device into a low-power state. Keep in mind that all of the device settings reset to their power-on value when returning from the power-down state.

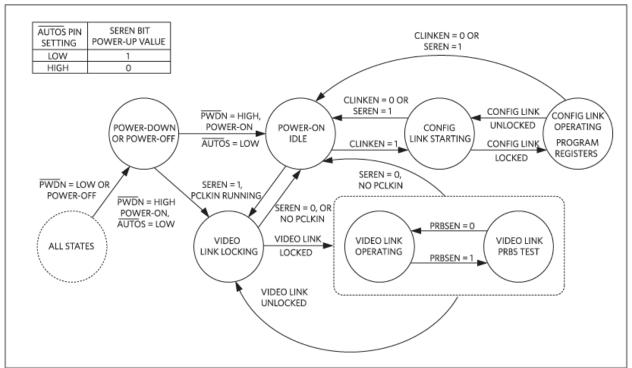


Figure 2. Serializer state diagram (CDS = low).

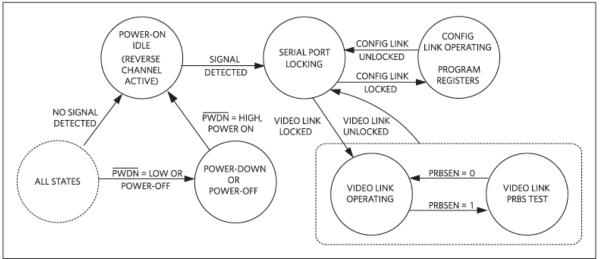


Figure 3. Deserializer state diagram (CDS = high).

Dual µC Contention Issues

In the configuration shown in Figure 1 above, each µC can communicate with the MAX9259 serializer, MAX9260 deserializer, or the other µC using the GMSL UART protocol. The GMSL does not provide a solution for contention avoidance, and the user will need to provide his/her own contention-handling scheme.

Separate Networks

The simplest solution for avoiding contention is to have each µC set its attached serializer/deserializer FWDCCEN and REVCCEN bits to 0

(0x04 D[1:0]). This disables the forward and reverse control-channel receivers and transmitters, and effectively breaks the control network into two independent networks (**Figure 4**). Any future control communication across the serial link first requires each side's µC to reenable communication on their respective ends of the link. This configuration is mostly useful in always-on applications, where critical link-specific register settings will not change from an initial setting.

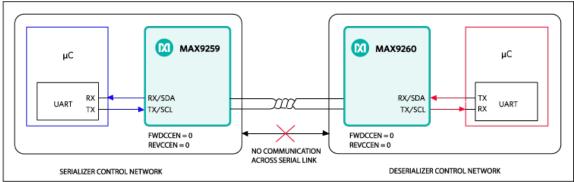


Figure 4. Separated control networks eliminate the possibility of contention.

Software Contention Handling

In applications where there must be communication between both sides of the serial link, users can always implement a higher layer protocol to avoid contention (**Figure 5**). In the example below, each μ C waits for the acknowledge frame to determine if its commands were successful. In the event of contention, the serializer/deserializer does not send an acknowledge frame. After failing to receive the acknowledge frame, the μ Cs wait for a period of time that depends on their device address before resending their commands. Since the microcontrollers in this design have different device addresses, contention will not occur during the retry communication.

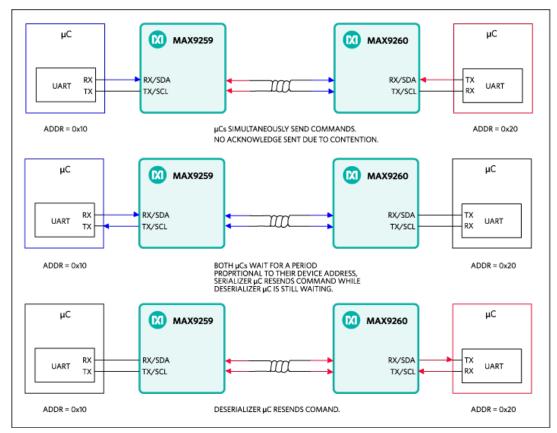


Figure 5. An example of software-based contention handling.

Single/Dual µC Use

Certain applications do not require use of both μ Cs at all times. During operation, if one of the CDS inputs on either side changes state, that device resumes operation in the corresponding state described in the *Link Startup Procedure* section of the MAX9259 data sheet. Switching between single μ C and dual μ C operation enables the GMSL to use fewer resources when needed. The unused μ C can be shut down to reduce power consumption and extend battery life.

Remote Display Example (Deserializer)

In the following application, the deserializer side of the link is a display panel that is configured for remote power-on/off. The board's shutdown inputs and the single/dual μ C control are all connected to the output of the MAX9260 GPIO0 (**Figure 6**). Upon power-up, the GPIO output is high, which keeps the remote-side devices shut and the deserializer configured as a remote-side device due to the added inverter. Since MS is connected to GPIO, The MAX9260 powers up in sleep mode, leaving all of the devices in a low-power state.

To start the remote panel, the serializer wakes up the MAX9260 and establishes the serial link. The serializer-side μ C then sets GPIO0 low to make MS low and the inverter output high. The inverter sets the MAX9260 as a local-side device and wakes up the rest of the remote display panel. MS must be set low to keep the MAX9260 UART interface in base mode.

To shut down the remote panel, the serializer sets GPIO0 high to shut down the remote-side devices and set the MAX9260 as a remote device. Then, set SLEEP = 1 in the MAX9260 to put the device to sleep.

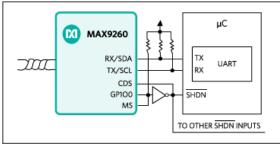


Figure 6. Dual/single μ C remote display example.

Remote Camera Example (Serializer)

Similar to the previous example, the serializer side of the link is a camera module that is configured for remote power-on/off. The MAX9259's INT output controls the board's shutdown inputs and the single/dual μ C switch (**Figure 7**). For this application, INT functions as a GPO, with the output set by either SETINT (D7 of 0x0D in the MAX9259) or the deserializer's INT input. Upon power-up, The INT output is low, which keeps the remote-side devices shut down. The inverter output, connected to CDS, configures the serializer as a remote-side device. Since active-low AUTOS is set high, the MAX9259 powers up in sleep mode.

To start the remote panel, the deserializer wakes up the MAX9259 using GMSL UART commands. The deserializer then sets the MAX9259's INT output high to power-up all remote devices. The inverter output sets the MAX9259 as a local-side device and can now accept UART commands from the local μ C.

To shut down the remote panel, the deserializer sets the MAX9259 INT output low to shut down the remote-side devices and set the MAX9259 as a remote device. The deserializer then sets SLEEP = 1 in the MAX9259 to put the device to sleep.

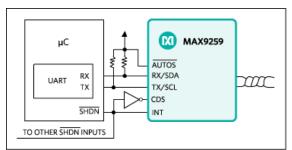


Figure 7. Dual/single μ C remote camera example.

Additional Applications

Dual μ C use is not limited to the applications shown above. The symmetric and bidirectional control channel, along with on-the-fly CDS and bypass settings (though MS), enable a multitude of serializer/deserializer and μ C configurations. The designer is now afforded a higher degree of control to increase the capabilities of their system, minimize power consumption, and maximize the use of available resources.

Related Parts		
MAX9259	Gigabit Multimedia Serial Link with Spread Spectrum and Full-Duplex Control Channel	Free Samples
MAX9260	Gigabit Multimedia Serial Link with Spread Spectrum and Full-Duplex Control Channel	Free Samples
MAX9263	HDCP Gigabit Multimedia Serial Link Serializer/Deserializer	Free Samples
MAX9264	HDCP Gigabit Multimedia Serial Link Serializer/Deserializer	Free Samples
MAX9265	HDCP Gigabit Multimedia Serial Link Serializer with LVDS System Interface	Free Samples
MAX9266	HDCP Gigabit Multimedia Serial Link Deserializer with LVDS System Interface	Free Samples

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