Simple, High Efficiency, Multi-Output, Isolated Flyback Supply with Excellent Regulation by Ryan Huff

Introduction

Simplicity, tight regulation, and high efficiency are no longer optional features in isolated power supplies, but achieving all three is traditionally difficult. High efficiency often requires the use of advanced topologies and home-brewed secondary synchronous rectification schemes. Tight regulation for a multi-output supply is often accomplished with inefficient, linear post regulators or efficient (but relatively expensive) switch-mode buck regulator ICs. All of these solutions fail the simplicity test in parts count and design complexity.

Fortunately, a breakthrough IC makes it possible to achieve high efficiency and tight regulation while maintaining the simplicity typically associated with a flyback supply. The LT3825 simplifies and improves the performance of low voltage, multioutput flyback supplies by providing precise synchronous rectifier timing and eliminating the need for optocoupler feedback while maintaining excellent regulation and superior loop response.



Figure 1. Simple, high efficiency, $36V_{IN}$ - $72V_{IN}$ to $2.5V_{OUT}$ at 3A, $3.3V_{OUT}$ at 3A, and $5.0V_{OUT}$ at 2A synchronous flyback

48V Input to Triple Output: 5V at 2A, 3.3V at 3A and 2.5V at 3A

The circuit in Figure 1 shows an isolated, no-optoisolator, synchronous flyback, 48V to 5.0V at 2A, 3.3V at 3A, and 2.5V at 3A power supply. Figure 2 shows its efficiency. The converter's efficiency of over 87% at the nominal input voltage of 48V and full, rated output current on each output approaches that of a higher parts count forward converter. This is primarily the result of a simple, well-controlled implementation of synchronous rectification. As a result of this high efficiency, the greatest temperature rise of any component is only 35°C above the ambient temperature with a paltry 100LFM of airflow.

The feedback winding is used to regulate the output voltage instead of an optocoupler and secondary-side reference, with good results. The regulation curve shown in Figure 3 shows that $\pm 1.6\%$ is easily attainable when loading outputs proportionately. Even when the outputs are loaded in every possible 10% to 100% load current combination, the cross-regulation between all outputs is within $\pm 3.6\%$. Figure 4 shows the supply's transient response for a 1.5A-to-3A load step on the 2.5V output at $5A/\mu s$ slew rate and 36V input. With this 50% load step, all output voltages remain within $\pm 2\%$ of their set points.

This circuit also has the advantage of having extremely low ripple on the output voltages; exhibiting less than $10mV_{P-P}$ on all outputs at a switching



Figure 2. Efficiency of circuit in Figure 1

frequency of 200kHz. This performance is attributable to the small, second stage, inductor/capacitor filter on each output.

LT3825 Operation

The synchronous rectifier output (SG pin) of the LT3825 makes driving the synchronous rectifier MOSFETs (Q2–Q4) simple while maintaining a low parts count. Setting the dead-time of these synchronous rectifiers relative to Q1 only requires one resistor to program. Avoiding traditional, more complicated, discrete timing circuits allows the designer to set optimum dead-times since this timing is well controlled within the LT3825. The LT3825 also precludes the need for a secondary-side synchronous controller IC and its associated circuitry.

The easy-to-implement synchronous rectification also has another advantage: it tightens the output cross-regulation. An alternative to synchronous rectification is



Figure 4. 1.5A to 3A to 1.5A load current step (top trace) on 2.5V output and output voltage responses (bottom traces) of circuit in Figure 1



using Schottky diodes, which can vary by more than 0.25V over temperature and load. Under the same conditions, the voltage drop across the MOSFETs in Figure 1 vary only by 60mV, a factor of four better. The MOSFET-based topology tightly couples each output, thereby reducing voltage differences during extreme temperature and cross-loading conditions.

Instead of using a parts-intensive, secondary-side voltage reference and error amplifier to drive an optocoupler, the LT3825 uses the primary bias winding on the flyback transformer, T1 (see Figure 1). The voltage on this winding during the flyback pulse is the average of all output voltages as reflected to the primary. The LT3825 feedback (FB) pin reads this voltage, which is then used to modulate the on-time of Q1 to regulate the output voltages. Cross-regulation performance is enhanced since the average of all outputs is presented to the controller as opposed to just one output voltage's information as with an optocoupler. Another important benefit of this technique is that the output voltage information arrives at the controller immediately after the switching cycle is terminated. In a conventional optocoupler-based design, delays of tens to hundreds of microseconds occur in the optocoupler alone, severely limiting the converter's transient response.

Other Features

An optional, resistor programmable, input undervoltage lockout is available. An optional soft-start capacitor

DESIGN IDEAS

controls the slew rate of the output voltage during start-up, which limits the inrush current of the input power supply. Since the LT3825 incorporates current-mode control, both shortcircuit behavior and ease of loop compensation are improved over voltage-mode controllers. The switching frequency can be set anywhere from 50kHz to 250kHz, making it possible to find the right balance of solution size and efficiency for a specific application. The switching frequency can be synchronized to an external system clock for further flexibility.

It Is Possible to Reduce the Parts Count Even More

For lower input voltages (5V to 20V) and simpler designs, the LT3837 complements the LT3825. The LT3837 starts up and runs from the lower

input voltage connected directly to the V_{CC} pin, so several components are not needed to generate a bias supply, including D1, C6, R1, and R2.

Conclusion

The LT3825 allows a designer to improve the performance of multioutput isolated flyback circuits while lowering parts count and simplifying implementation.

LTC4089, continued from page 22 High Voltage Buck Output Capacitor Selection

All the ceramic capacitors used in the circuit are recommended to be X5R or better (X7R). However, be cautious about the claimed initial capacitance value (e.g., some 0805 size 22μ F/6.3V X5R caps measure only 11µF at no bias) and derating with bias and temperature (some X5R caps derate to less than 20% of their initial values with full 6.3V voltage bias). It is critical to use a $22\mu F/16V$ X5R or better cap at the output of the LTC4089 buck regulator (connected to HVOUT), as low capacitance causes duty-jitter in certain conditions. The LTC4089-5 can operate with a 22μ F/6.3V ceramic cap at the output.

High Voltage Buck Current Limit

As shown in Figure 7, the buck output current capability is a function of inductance and the input voltage. For most of the input range, the output current limit is 1A for a 10µH inductor



Figure 7. The high voltage switching regulator's maximum output current for two different value inductors

and 1.1A for a 33μ H inductor. When powered from the high voltage source, if the sum of the system load current at the OUT terminal and charge current (set by R_{PROG}) exceeds the buck output current limit, the buck output voltage collapses to the battery voltage.

Accept USB and 5V Adaptor with Different Current Limits

Like all other LTC PowerPath controllers, the LTC4089/LTC4089-5 can be configured to accept 5V adaptor/USB input in the same USB connector or different connectors with different current limits by changing the resistance connected to CLPROG pin. Figure 8 shows the schematic diagrams.

Conclusion

The LTC4089 and LTC4089-5 combine a monolithic high voltage switching buck regulator, a full featured Li-ion battery charger, and a PowerPath controller in a tiny 3mm × 6mm DFN package. They solve many battery charging and power path problems and easily fits into handheld applications, such as portable GPS navigators and MP3 players, where a high voltage source and small PCB space are required.



Figure 8. IN pin accepting USB and 5V Adaptor with different current limits