High Efficiency PolyPhase Power Supply Delivers 30A per Phase with Ultralow Inductor DCR Sensing and Fast Transient Response

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The LTC3875 is a feature-rich dual-output synchronous buck controller that meets the power density demands of modern high speed, high capacity data processing systems, telecom systems, industrial equipment and DC power distribution systems.

The LTC3875's features include:

LTC3875

- 4.5V to 38V input range and 0.6V to 3.5V output range
- Proprietary current mode architecture enhances the signal-to-noise ratio of the current sensing signal, allowing the use of ultralow DCR power inductors to maximize efficiency and reduce switching jitter.
- Fast transient response, facilitating high density design with less output capacitance.
- Remote output voltage sensing and ±0.5% reference (0.6V) window for accurate regulation.
- On-chip drivers in a 6mm × 6mm QFN package to satisfy demanding space requirements.
- Easy parallel multiphase operation for high current applications.

DUAL-OUTPUT CONVERTER (1.0V AT 30A AND 1.5V AT 30A)

Figure 1 shows a typical 4.5V~14V input, dual-output solution. The LTC3875's two channels run relative to each other with a 180° phase shift, reducing the input RMS current ripple and capacitor size. Each phase has one top MOSFET and one bottom MOSFET to provide up to 30A of output current.

Similar to LTC3866, the LTC3875 employs a unique current sensing architecture to enhance its signal-to-noise ratio, enabling current mode control via a small sense signal of a very low inductor DCR, $1m\Omega$ or less. As a result, the efficiency is greatly



SNSA1⁺, SNSA2⁺: Positive AC Current Sense Comparator Inputs SNSD1⁺, SNSD2⁺: Positive DC Current Sense Comparator Inputs SNS1⁻, SNS2⁻: Negative AC and DC Current Sense Comparator Inputs V_{OSNS1}⁺, V_{OSNS2}⁺: Positive Inputs of Remote Sensing Differential Amplifiers V_{OSNS1}⁻, V_{OSNS2}⁻: Negative Inputs of Remote Sensing Differential Amplifiers TK/SS1, TK/SS2: Voltage Tracking and Soft Start Inputs

The LTC3875 can sense a DCR value as low as $0.2m\Omega$ with careful PCB layout. Moreover, an additional temperature compensation circuit can be used to guarantee the accurate current limit over a wide temperature range.

improved and the jitter is reduced. The current mode control yields fast cycleby-cycle current limit, current sharing and simplified feedback compensation.

The LTC3875 can sense a DCR value as low as 0.2m Ω with careful PCB layout. The LTC3875 uses two positive sense pins SNSD⁺ and SNSA⁺ to acquire signals. The filter time constant of the SNSD⁺ should match the L/DCR of the output inductor, while the filter at SNSA⁺ should have a bandwidth five times larger than that of SNSD⁺. Moreover, an additional temperature compensation circuit can be used to guarantee the accurate current limit over a wide temperature range.

Efficiency can be optimized with ultralow DCR inductor. As shown in Figure 2, the total solution efficiency in forced continuous mode (CCM) is 87.3% at 1.0V/30A output, and 89.8% at 1.5V/30A. The hot spot (bottom MOSFET) temperature rise is 57°C without any airflow as shown in Figure 3, where the ambient temperature is about 23°C.

The LTC3875 features fast transient response and minimizes undershoot through a proprietary solution. Peak current mode control is widely adopted in switching converters due to its cycleby-cycle peak current limit and easy compensation. However, the inherent switching cycle delay of peak current mode control results in large undershoot of the output voltage when there is a load step-up. The LTC3875 overcomes undershoot by using a dynamic switching frequency adjustment scheme. The internal



Figure 2. Efficiency comparison of the two channels

transient detector can detect a large voltage undershoot, leading the LTC3875 to run the power stage at twice the preset switching frequency for about 20 cycles.

Figure 4 shows that switching cycle delay is reduced from 2.18µs to 1.2µs and voltage undershoot is reduced from 95mV to 67.5mV (29% reduction) at 15A load up



 V_{IN} = 12V, V_{OUT1} = 1.0V/30A, V_{OUT2} = 1.5V/30A, NO AIRFLOW Figure 3. Thermal test results

after the fast transient is enabled. In other words, with fast transient enabled, the LTC3875 can achieve the same transient performance as without, but with 20% less output capacitance, increasing power density and reducing total cost. Compared to other nonlinear control methods, the response scheme used by the LTC3875 is linear, simplifying overall design.

Figure 4. Transient comparison



(b) Fast transient enabled





SELECT PIN ABBREVIATIONS

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SINGLE OUTPUT, DUAL PHASE, HIGH CURRENT CONVERTER (12V TO 1V AT 6A)

The LTC3875 can be easily configured as a dual-phase single-output converter for higher current solutions. Figure 5 shows a buck converter that produces a 1V, 60A output from a 12V input. Multiple ICs can be paralleled and phase-interleaved for even higher current if required.

The DC current sharing between the two channels is shown in Figure 6. The difference at full load is around 1.6A with 0.32mΩ DCR inductor. Thanks to the peak current mode control architecture,

Figure 6. DC current sharing of 60A solution shown in Figure 5



Figure 7. Dynamic current sharing of 60A solution shown in Figure 5



the dynamic current sharing is also very good, as shown in Figure 7.

CONCLUSION

The LTC3875 delivers high efficiency with reliable current mode control, ultralow DCR sensing and strong integrated drivers in a 6mm × 6mm 40-pin QFN. It supports temperature-compensated DCR sensing for high reliability. Its fast transient response can help improve the transient response with minimum output capacitance. Tracking, multichip operation, and external sync capability fill out its menu of features. The LTC3875 is ideal for high current applications, such as telecom and datacom systems, industrial and computer systems applications.