RELIABILITY REPORT

FOR

MAX9986AETP+

PLASTIC ENCAPSULATED DEVICES

February 13, 2009

MAXIM INTEGRATED PRODUCTS

120 SAN GABRIEL DR.

SUNNYVALE, CA 94086

Approved by
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Quality Assurance
Director, Reliability Engineering

Conclusion

The MAX9986AETP+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim"s continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim"s quality and reliability standards.

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I. Device Description

A. General

The MAX9986A high-linearity downconversion mixer provides 8.2dB gain, +25dBm IIP3, and 10dB NF for 815MHz to 1000MHz base-station receiver applications. With a 960MHz to 1180MHz LO frequency range, this particular mixer is ideal for high-side LO injection receiver architectures. Low-side LO injection is supported by the MAX9984, which is pin-for-pin and functionally compatible with the MAX9986A. In addition to offering excellent linearity and noise performance, the MAX9986A also yields a high level of component integration. This device includes a double-balanced passive mixer core, an IF amplifier, a dual-input LO selectable switch, and an LO buffer. On-chip baluns are also integrated to allow for single-ended RF and LO inputs. The MAX9986A requires a nominal LO drive of 0dBm, and supply current is guaranteed to be below 250mA. The MAX9986A is a derivative version of the MAX9986 with improved large-signal blocking performance. The MAX9984/MAX9986/MAX9986A are pin compatible with the MAX9994/MAX9996 1700MHz to 3000MHz mixers, making this entire family of downconverters ideal for applications where a common PC board layout is used for both frequency bands. The MAX9986A is also functionally compatible with the MAX9993. The MAX9986A is available in a compact, 20-pin, thin QFN package (5mm x 5mm) with an exposed paddle. Electrical performance is guaranteed over the extended -40°C to +85°C temperature range.

II. Manufacturing Information

A. Description/Function:	SiGe High-Linearity, 815MHz to 1000MHz Downconversion Mixer with LO Buffer/Switch
B. Process:	G4
C. Number of Device Transistors:	
D. Fabrication Location:	Oregon
E. Assembly Location:	ASAT China, UTL Thailand
F. Date of Initial Production:	December 22, 2005

III. Packaging Information

A. Package Type:	20-pin TQFN 5x5
B. Lead Frame:	Copper
C. Lead Finish:	100% matte Tin
D. Die Attach:	Conductive Epoxy
E. Bondwire:	Gold (1 mil dia.)
F. Mold Material:	Epoxy with silica filler
G. Assembly Diagram:	#05-9000-1318
H. Flammability Rating:	Class UL94-V0
I. Classification of Moisture Sensitivity per JEDEC standard J-STD-020-C	Level 1
J. Single Layer Theta Ja:	48°C/W
K. Single Layer Theta Jc:	2.1°C/W
L. Multi Layer Theta Ja:	32°C/W
M. Multi Layer Theta Jc:	2.7°C/W

IV. Die Information

A. Dimensions:	97 X 94 mils
B. Passivation:	Si_3N_4
C. Interconnect:	Au
D. Backside Metallization:	None
E. Minimum Metal Width:	1.2 microns (as drawn) Metal 1, 2 & 3 5.6 microns (as drawn) Metal 4
F. Minimum Metal Spacing:	1.6 microns (as drawn) Metal 1, 2 & 3, 4.2 microns (as drawn) Metal 4
G. Bondpad Dimensions:	5 mil. Sq.
H. Isolation Dielectric:	SiO_2
I. Die Separation Method:	Wafer Saw

V. Quality Assurance Information

A. Quality Assurance Contacts:	Ken Wendel (Director, Reliability Engineering) Bryan Preeshl (Managing Director of QA)
B. Outgoing Inspection Level:	0.1% for all electrical parameters guaranteed by the Datasheet. 0.1% For all Visual Defects.
C. Observed Outgoing Defect Rate:	< 50 ppm
D. Sampling Plan:	Mil-Std-105D

VI. Reliability Evaluation

A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate (λ) is calculated as follows:

$$\lambda = \frac{1}{MTTF} = \frac{1.83}{192 \times 4340 \times 96 \times 2}$$ (Chi square value for MTTF upper limit)

(where 4340 = Temperature Acceleration factor assuming an activation energy of 0.8eV)

$\lambda = 11.2 \times 10^{-9}$

$\lambda = 11.2$ F.I.T. (60% confidence level @ 25°C)

The following failure rate represents data collected from Maxim's reliability monitor program. Maxim performs quarterly 1000 hour life test monitors on its processes. This data is published in the Product Reliability Report found at http://www.maxim-ic.com/. Current monitor data for the G4 Process results in a FIT Rate of 0.2 @ 25C and 3.6 @ 55C (0.8 eV, 60% UCL)

B. Moisture Resistance Tests

The industry standard 85°C/85%RH or HAST testing is monitored per device process once a quarter.

C. E.S.D. and Latch-Up Testing

The CR28-5 die type has been found to have all pins able to withstand a HBM transient pulse of +/-1500 V per JEDEC JESD22-A114-D. Latch-Up testing has shown that this device withstands a current of +/-250 mA.

Table 1
Reliability Evaluation Test Results

MAX9986AETP+

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES
Static Life Test (Note 1)				
	Ta = 135°C Biased Time = 192 hrs.	DC Parameters & functionality	96	0
Moisture Testing (Note 2)				
85/85	Ta = 85°C RH = 85% Biased Time = 1000hrs.	DC Parameters & functionality	77	0
Mechanical Stress (Note 2)				
Temperature Cycle	-65°C/150°C 1000 Cycles Method 1010	DC Parameters & functionality	77	0

Note 1: Life Test Data may represent plastic DIP qualification lots.
Note 2: Generic Package/Process data