

RELIABILITY REPORT FOR MAX8569AEUT+

PLASTIC ENCAPSULATED DEVICES

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# **MAXIM INTEGRATED**

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#### Conclusion

The MAX8569AEUT+ successfully meets the quality and reliability standards required of all Maxim Integrated products. In addition, Maxim Integrated's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim Integrated's quality and reliability standards.

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# I. Device Description

A. General

The MAX8569A/MAX8569B low-quiescent-current boost regulators provide up to 200mA at output voltages up to 5.5V from a 1.5V to 5.5V input source. The built-in synchronous rectifier allows for over 90% efficiency while achieving small size and low cost by eliminating the need for an external Schottky diode. The MAX8569A provides an adjustable output while the MAX8569B is fixed at 3.0V or 3.3V. The MAX8569B features a power-on reset output (active-low RST) to signal that the output has reached regulation. All devices connect the battery input to the output during shutdown, allowing the input battery to be used as a backup or real-time clock supply when the converter is off.



## II. Manufacturing Information

- A. Description/Function:
   200mA Step-Up Converters in 6-Pin SOT23 and TDFN

   B. Process:
   B8

   C. Number of Device Transistors:
   Step-Up Converters in 6-Pin SOT23 and TDFN
- D. Fabrication Location:California or TexasE. Assembly Location:Japan, ThailandF. Date of Initial Production:April 23, 2005

#### III. Packaging Information

A. Package Type:	6-pin SOT23			
B. Lead Frame:	Copper			
C. Lead Finish:	100% matte Tin			
D. Die Attach:	Conductive			
E. Bondwire:	N/A			
F. Mold Material:	Epoxy with silica filler			
G. Assembly Diagram:	#05-9000-1796			
H. Flammability Rating:	Class UL94-V0			
I. Classification of Moisture Sensitivity per Level 1 JEDEC standard J-STD-020-C				
J. Single Layer Theta Ja:	185.5°C/W			
K. Single Layer Theta Jc:	75°C/W			
L. Multi Layer Theta Ja:	134.4°C/W			
M. Multi Layer Theta Jc:	38.7°C/W			

#### IV. Die Information

Α.	Dimensions:	85 X 45 mils
В.	Passivation:	$Si_3N_4/SiO_2$ (Silicon nitride/ Silicon dioxide)
C.	Interconnect:	Al/0.5%Cu with Ti/TiN Barrier
D.	Backside Metallization:	None
E.	Minimum Metal Width:	0.8 microns (as drawn)
F.	Minimum Metal Spacing:	0.8 microns (as drawn)
G.	Bondpad Dimensions:	
Н.	Isolation Dielectric:	SiO <sub>2</sub>
Ι.	Die Separation Method:	Wafer Saw



#### V. Quality Assurance Information

A.	Quality Assurance Contacts:	Richard Aburano (Manager, Reliability Engineering) Don Lipps (Manager, Reliability Engineering) Bryan Preeshl (Vice President of QA)
В.	Outgoing Inspection Level:	<ul><li>0.1% for all electrical parameters guaranteed by the Datasheet.</li><li>0.1% For all Visual Defects.</li></ul>
C.	Observed Outgoing Defect Rate:	< 50 ppm
D.	Sampling Plan:	Mil-Std-105D

### VI. Reliability Evaluation

A. Accelerated Life Test

The results of the 135C biased (static) life test are shown in Table 1. Using these results, the Failure Rate ( $\lambda$ ) is calculated as follows:

$$\lambda = \underbrace{1}_{\text{MTTF}} = \underbrace{1.83}_{\text{192 x 4340 x 80 x 2}} \text{ (Chi square value for MTTF upper limit)}$$

$$\lambda = 13.7 \times 10^{-9}$$

x = 13.7 F.I.T. (60% confidence level @ 25°C)

The following failure rate represents data collected from Maxim Integrated's reliability monitor program. Maxim Integrated performs quarterly life test monitors on its processes. This data is published in the Reliability Report found at http://www.maximintegrated.com/qa/reliability/monitor. Cumulative monitor data for the B8 Process results in a FIT Rate of 0.04 @ 25C and 0.73 @ 55C (0.8 eV, 60% UCL).

#### B. E.S.D. and Latch-Up Testing (lot TQHDAQ001G, D/C 0512)

The PP29 die type has been found to have all pins able to withstand a HBM transient pulse of +/-2000V per JEDEC JESD22-A114. Latch-Up testing has shown that this device withstands a current of +/-250mA.



# Table 1 Reliability Evaluation Test Results

# MAX8569AEUT+

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES	COMMENTS
Static Life Test (Note	e 1) Ta = 135°C Biased Time = 192 hrs.	DC Parameters & functionality	80	0	TQHAAQ001I, D/C 0511

Note 1: Life Test Data may represent plastic DIP qualification lots.