

RELIABILITY REPORT

FOR

MAX6344ZUT+ (MAX6342-MAX6345)

PLASTIC ENCAPSULATED DEVICES

April 9, 2009

MAXIM INTEGRATED PRODUCTS

120 SAN GABRIEL DR. SUNNYVALE, CA 94086

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Conclusion

The MAX6344ZUT+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim's quality and reliability standards.

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I. Device Description

A. General

The MAX6342-MAX6345 family of microprocessor (µP) supervisory circuits monitors power supplies in digital systems. These devices significantly improve system reliability and accuracy compared to separate ICs or discrete components. The MAX6342-MAX6345 provide factory-trimmed VCC reset threshold voltages from 2.33V to 4.63V and operate with supply voltages between +1V and +5.5V. A +1.25V threshold detector allows for a power-fail warning, for low-battery detection, or for monitoring another power supply. The MAX6342 contains an active-low MR input and an active-low push-pull reset. The MAX6343 and MAX6344 are identical to the MAX6342 except they provide an active-low, open-drain reset and an active-high, push-pull reset, respectively. The MAX6345 provides a second reset output in place of the active-low MR input to give it an active-high push-pull reset and an active-low push-pull reset. All of the devices are packaged in a miniature 6-pin SOT23.



II. Manufacturing Information

A. Description/Function: 6-Pin µP Reset Circuit with Power-Fail Comparator

B. Process: S3

C. Number of Device Transistors:

D. Fabrication Location: Oregon

E. Assembly Location: Carsem Malaysia, UTL Thailand

F. Date of Initial Production: October 23, 1999

III. Packaging Information

A. Package Type: 6-pin SOT23
B. Lead Frame: Copper

C. Lead Finish: 100% matte Tin

D. Die Attach: Non-conductive Epoxy
E. Bondwire: Gold (1 mil dia.)
F. Mold Material: Epoxy with silica filler

G. Assembly Diagram: #05-1601-0088H. Flammability Rating: Class UL94-V0

I. Classification of Moisture Sensitivity per Level 1

JEDEC standard J-STD-020-C

J. Single Layer Theta Jb: 115*°C/WK. Single Layer Theta Jc: 80°C/W

IV. Die Information

A. Dimensions: 57 X 35 mils

B. Passivation: Si₃N₄/SiO₂ (Silicon nitride/ Silicon dioxide

C. Interconnect: Aluminum/Si (Si = 1%)

D. Backside Metallization: None

E. Minimum Metal Width: 3.0 microns (as drawn)F. Minimum Metal Spacing: 3.0 microns (as drawn)

G. Bondpad Dimensions: 5 mil. Sq.
 H. Isolation Dielectric: SiO₂
 I. Die Separation Method: Wafer Saw



V. Quality Assurance Information

A. Quality Assurance Contacts: Ken Wendel (Director, Reliability Engineering)

Bryan Preeshl (Managing Director of QA)

B. Outgoing Inspection Level: 0.1% for all electrical parameters guaranteed by the Datasheet.

0.1% For all Visual Defects.

C. Observed Outgoing Defect Rate: < 50 ppmD. Sampling Plan: Mil-Std-105D

VI. Reliability Evaluation

A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate (λ) is calculated as follows:

$$\lambda = 1 \over \text{MTTF}$$
 = $\frac{1.83}{192 \times 4340 \times 80 \times 2}$ (Chi square value for MTTF upper limit) (Chi square value for MTTF upper limit) (where 4340 = Temperature Acceleration factor assuming an activation energy of 0.8eV)

$$\lambda = 13.4 \times 10^{-9}$$

 $\lambda = 13.4 \text{ F.I.T. (60\% confidence level @ 25°C)}$

The following failure rate represents data collected from Maxim's reliability monitor program. Maxim performs quarterly 1000 hour life test monitors on its processes. This data is published in the Product Reliability Report found at http://www.maxim-ic.com/. Current monitor data for the S3 Process results in a FIT Rate of 3.6 @ 25C and 66.0 @ 55C (0.8 eV, 60% UCL)

B. Moisture Resistance Tests

The industry standard 85°C/85%RH or HAST testing is monitored per device process once a quarter.

C. E.S.D. and Latch-Up Testing

The MS25-5 die type has been found to have all pins able to withstand a HBM transient pulse of +/-2500 V per Mil-Std 883 Method 3015.7. Latch-Up testing has shown that this device withstands a current of +/-250 mA.



Table 1Reliability Evaluation Test Results

MAX6344ZUT+

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES	
Static Life Test (Note 1)				
,	Ta = 135°C	DC Parameters	80	0	
	Biased	& functionality			
	Time = 192 hrs.				
Moisture Testing	(Note 2)				
85/85	Ta = 85°C	DC Parameters	77	0	
	RH = 85%	& functionality			
	Biased				
	Time = 1000hrs.				
Mechanical Stres	s (Note 2)				
Temperature	-65°C/150°C	DC Parameters	77	0	
Cycle	1000 Cycles	& functionality			
	Method 1010				

Note 1: Life Test Data may represent plastic DIP qualification lots.

Note 2: Generic Package/Process data