

RELIABILITY REPORT FOR MAX6225BESA+ PLASTIC ENCAPSULATED DEVICES

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MAXIM INTEGRATED PRODUCTS

120 SAN GABRIEL DR. SUNNYVALE, CA 94086

Approved by
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Quality Assurance
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Conclusion

The MAX6225BESA+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim's quality and reliability standards.

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- I. Device Description
 - A. General

The MAX6225/MAX6241/MAX6250 are low-noise, precision voltage references with extremely low 1ppm/°C temperature coefficients and excellent ±0.02% initial accuracy. These devices feature buried-zener technology for lowest noise performance. Load-regulation specifications are guaranteed for source and sink currents up to 15mA. Excellent line and load regulation and low output impedance at high frequency make them ideal for high-resolution data-conversion systems up to 16 bits. The MAX6225 is set for 2.500V output, the MAX6241 is set for 4.096V output, and the MAX6250 is set for 5.000V output. All three provide for the option of external trimming and noise reduction.



II. Manufacturing Information

Α.	Description/Function:	Low-Noise, Precision, +2.5V/+4.096V/+5V Voltage References
В.	Process:	S3
C.	Number of Device Transistors:	

D. Fabrication Location:	Oregon
E. Assembly Location:	Thailand
F. Date of Initial Production:	Pre 1997

III. Packaging Information

A. Package Type:	8L SOIC (N)
B. Lead Frame:	Copper
C. Lead Finish:	100% matte Tin
D. Die Attach:	Conductive
E. Bondwire:	Au (1 mil dia.)
F. Mold Material:	Epoxy with silica filler
G. Assembly Diagram:	#05-0901-0148 / A
H. Flammability Rating:	Class UL94-V0
I. Classification of Moisture Sensitivity per JEDEC standard J-STD-020-C	1
J. Single Layer Theta Ja:	170°C/W
K. Single Layer Theta Jc:	40°C/W
L. Multi Layer Theta Ja:	128.4 °C/W
M. Multi Layer Theta Jc:	36°C/W

IV. Die Information

Dimensions:	85 X 145 mils
Passivation:	Si_3N_4/SiO_2 (Silicon nitride/ Silicon dioxide)
Interconnect:	Al/0.5%Cu with Ti/TiN Barrier
Backside Metallization:	None
Minimum Metal Width:	3.0 microns (as drawn)
Minimum Metal Spacing:	3.0 microns (as drawn)
Bondpad Dimensions:	
Isolation Dielectric: Die Separation Method:	SiO ₂ Wafer Saw
	Dimensions: Passivation: Interconnect: Backside Metallization: Minimum Metal Width: Minimum Metal Spacing: Bondpad Dimensions: Isolation Dielectric: Die Separation Method:



V.	Quality	Assurance	Information
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Α.	Quality Assurance Contacts:	Richard Aburano (Manager, Reliability Engineering)		
		Bryan Braashi (Vice President of OA)		
		Bryan Freeshi (vice Fresident of QA)		
В.	Outgoing Inspection Level:	0.1% for all electrical parameters guaranteed by the Datasheet.		
		0.1% For all Visual Defects.		
C.	Observed Outgoing Defect Rate:	< 50 ppm		
D.	Sampling Plan:	Mil-Std-105D		

VI. Reliability Evaluation

A. Accelerated Life Test

The results of the biased (static) life test are shown in Table 1. Using these results, the Failure Rate (λ) is calculated as follows:

 $\lambda = \underbrace{1}_{\text{MTTF}} = \underbrace{1.83}_{1000 \text{ x } 4340 \text{ x } 231 \text{ x } 2} \text{ (Chi square value for MTTF upper limit)}$ $\lambda = 0.9 \text{ x } 10^{-9}$ $\lambda = 0.9 \text{ F.I.T. (60\% confidence level @ 25°C)}$

The following failure rate represents data collected from Maxim's reliability monitor program. Maxim performs quarterly life test monitors on its processes. This data is published in the Reliability Report found at http://www.maxim-ic.com/qa/reliability/monitor. Cumulative monitor data for the S3 Process results in a FIT Rate of 0.04 @ 25C and 0.69 @ 55C (0.8 eV, 60% UCL)

B. E.S.D. and Latch-Up Testing (ESD lot XLYCDX001A D/C 9806, Latch-Up lot NLYCGA008D D/C 0013)

The RF20-2 die type has been found to have all pins able to withstand a HBM transient pulse of +/-2000V per Mil-Std 883 Method 3015.7. Latch-Up testing has shown that this device withstands a current of +/-250mA.



Table 1 Reliability Evaluation Test Results

MAX6225BESA+

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES	COMMENTS	
Static Life Test (Note 1)						
	Ta = 135°C	DC Parameters	77	0	NLYAG4105A, D/C 0245	
	Biased	& functionality	77	0	NLYAG4106A, D/C 0305	
	Time = 1000 hrs.		77	0	NLYAG4115B, D/C 0245	

Note 1: Life Test Data may represent plastic DIP qualification lots.