

RELIABILITY REPORT FOR MAX31855ESA+ PLASTIC ENCAPSULATED DEVICES

June 15, 2011

MAXIM INTEGRATED PRODUCTS

120 SAN GABRIEL DR.

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Approved by		
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Quality Assurance		
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Conclusion

The MAX31855ESA+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim's quality and reliability standards.

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I. Device Description

A. General

The MAX31855 performs cold-junction compensation and digitizes the signal from a K, J, N, T, or E type thermocouple. (Contact the factory for S and R type thermocouples.) The data is output in a signed 14-bit, SPITM- compatible, read-only format. This converter resolves temperatures to 0.25NC, allows readings as high as +1800NC and as low as -270NC, and exhibits thermocouple accuracy of \pm 2NC for temperatures ranging from -200NC to +700NC for K-type thermocouples. For full range accuracies and other thermocouple types, see the Thermal Characteristics specifications.



A. Description/Function:	Cold-Junction Compensated Thermocouple-to-Digital Converter
B. Process:	S4
C. Number of Device Transistors:	25614
D. Fabrication Location:	Texas
E. Assembly Location:	Malaysia
F. Date of Initial Production:	March 25, 2011

F. Date of Initial Production:

III. Packaging Information

A. Package Type:	8-pin SOIC (N)
B. Lead Frame:	Copper
C. Lead Finish:	100% matte Tin
D. Die Attach:	Conductive
E. Bondwire:	Au (1 mil dia.)
F. Mold Material:	Epoxy with silica filler
G. Assembly Diagram:	#
H. Flammability Rating:	Class UL94-V0
I. Classification of Moisture Sensitivity per JEDEC standard J-STD-020-C	Level 1
J. Single Layer Theta Ja:	170°C/W
K. Single Layer Theta Jc:	40°C/W
L. Multi Layer Theta Ja:	132°C/W

IV. Die Information

A. Dimensions:	76 X 118 mils
B. Passivation:	Si ₃ N ₄ /SiO ₂ (Silicon nitride/ Silicon dioxide)
C. Interconnect:	Al with Ti/TiN Barrier
D. Backside Metallization:	None
E. Minimum Metal Width:	Metal1 = 0.5 / Metal2 = 0.6 / Metal3 = 0.6 microns (as drawn)
F. Minimum Metal Spacing:	Metal1 = 0.45 / Metal2 = 0.5 / Metal3 = 0.6 microns (as drawn)
G. Bondpad Dimensions:	5 mil. Sq.
H. Isolation Dielectric:	SiO ₂
I. Die Separation Method:	Wafer Saw



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A. Quality Assurance Contacts:	Richard Aburano (Manager, Reliability Engineering)
	Don Lipps (Manager, Reliability Engineering)
	Bryan Preeshl (Vice President of QA)
B. Outgoing Inspection Level:	0.1% for all electrical parameters guaranteed by the Datasheet.
	0.1% For all Visual Defects.
C. Observed Outgoing Defect Rate:	< 50 ppm
D. Sampling Plan:	Mil-Std-105D

VI. Reliability Evaluation

A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate (λ) is calculated as follows:

 $\lambda = \underbrace{1}_{\text{MTTF}} = \underbrace{1.83}_{\text{192 x 4340 x 77 x 2}} \text{ (Chi square value for MTTF upper limit)}$ $\lambda = 14.3 \times 10^{-9}$ $\lambda = 14.3 \text{ F.I.T. (60\% confidence level @ 25°C)}$

The following failure rate represents data collected from Maxim's reliability monitor program. Maxim performs quarterly life test monitors on its processes. This data is published in the Reliability Report found at http://www.maxim-ic.com/qa/reliability/monitor. Cumulative monitor data for the S4 Process results in a FIT Rate of 0.05 @ 25C and 0.83 @ 55C (0.8 eV, 60% UCL)

B. E.S.D. and Latch-Up Testing (lot ZM163058B D/C 1111)

The DT03 die type has been found to have all pins able to withstand a HBM transient pulse of +/-2500V per JEDEC JESD22-A114. Latch-Up testing has shown that this device withstands a current of +/-250mA and overvoltage per JEDEC JESD78.



Table 1 Reliability Evaluation Test Results

MAX31855ESA+

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES	COMMENTS
Static Life Test (N	lote 1) Ta = 135°C Biased Time = 192 hrs.	DC Parameters & functionality	77	0	ZM163058B, D/C 1111

Note 1: Life Test Data may represent plastic DIP qualification lots.