

RELIABILITY REPORT
FOR
MAX294CPA+

PLASTIC ENCAPSULATED DEVICES

December 23, 2008

# **MAXIM INTEGRATED PRODUCTS**

120 SAN GABRIEL DR. SUNNYVALE, CA 94086

Approved by	
Ken Wendel	
Quality Assurance	
Director, Reliability Engineering	



## Conclusion

The MAX294CPA+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim"s continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim"s quality and reliability standards.

## **Table of Contents**

IDevice Description	VQuality Assurance Information
IIManufacturing Information	VIReliability Evaluation
IIIPackaging Information	IVDie Information
Attachments	

### I. Device Description

A. General

See Data Sheet



## II. Manufacturing Information

A. Description/Function: 8th-Order, Lowpass, Elliptic, Switched-Capacitor Filters

B. Process: S3

C. Number of Device Transistors:

D. Fabrication Location: Oregon

E. Assembly Location: Hana Thailand, Carsem Malaysia, UTL Thailand

F. Date of Initial Production: Pre 1997

### III. Packaging Information

A. Package Type: 8-pin PDIP
B. Lead Frame: Copper

C. Lead Finish:

D. Die Attach:

Conductive Epoxy

E. Bondwire:

Gold (1.3 mil dia.)

F. Mold Material:

G. Assembly Diagram:

H. Flammability Rating:

H. Flammability Rating:

Conductive Epoxy

Epoxy with silica filler

#05-0201-0111

Class UL94-V0

I. Classification of Moisture Sensitivity per Level 1

JEDEC standard J-STD-020-C

J. Single Layer Theta Ja: 110°C/WK. Single Layer Theta Jc: 40°C/W

### IV. Die Information

A. Dimensions: 89 X 95 mils

B. Passivation: Si<sub>3</sub>N<sub>4</sub>/SiO<sub>2</sub> (Silicon nitride/ Silicon dioxide

C. Interconnect: Aluminum/Si (Si = 1%)

D. Backside Metallization: None

E. Minimum Metal Width: 3.0 microns (as drawn)F. Minimum Metal Spacing: 3.0 microns (as drawn)

G. Bondpad Dimensions: 5 mil. Sq.
 H. Isolation Dielectric: SiO<sub>2</sub>
 I. Die Separation Method: Wafer Saw



## V. Quality Assurance Information

A. Quality Assurance Contacts: Ken Wendel (Director, Reliability Engineering)

Bryan Preeshl (Managing Director of QA)

B. Outgoing Inspection Level: 0.1% for all electrical parameters guaranteed by the Datasheet.

0.1% For all Visual Defects.

C. Observed Outgoing Defect Rate: < 50 ppm</li>D. Sampling Plan: Mil-Std-105D

### VI. Reliability Evaluation

#### A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate ( $\lambda$ ) is calculated as follows:

$$\lambda = 1 \over MTTF$$
 = 1.83 (Chi square value for MTTF upper limit)  
192 x 4340 x 80 x 2 (where 4340 - Temperature Acceleration factor assuming an activation of

$$x = 13.4 \times 10^{-9}$$

% = 13.4 F.I.T. (60% confidence level @ 25°C)

The following failure rate represents data collected from Maxim's reliability monitor program. Maxim performs quarterly 1000 hour life test monitors on its processes. This data is published in the Product Reliability Report found at http://www.maxim-ic.com/. Current monitor data for the S3 Process results in a FIT Rate of 3.6 @ 25C and 66.0 @ 55C (0.8 eV, 60% UCL)

#### B. Moisture Resistance Tests

The industry standard 85°C/85%RH or HAST testing is monitored per device process once a quarter.

## C. E.S.D. and Latch-Up Testing

The AF11-6 die type has been found to have all pins able to withstand a HBM transient pulse of +/-1000 V per Mil-Std 883 Method 3015.7. Latch-Up testing has shown that this device withstands a current of +/-100 mA.



# **Table 1**Reliability Evaluation Test Results

# MAX294CPA+

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES	
Static Life Test (	(Note 1)				
·	Ta = 135°C Biased Time = 192 hrs.	DC Parameters & functionality	80	0	
Moisture Testing	(Note 2)				
85/85	Ta = 85°C RH = 85% Biased Time = 1000hrs.	DC Parameters & functionality	77	0	
Mechanical Stres	ss (Note 2)				
Temperature	-65°C/150°C	DC Parameters	77	0	
Cycle	1000 Cycles Method 1010	& functionality			

Note 1: Life Test Data may represent plastic DIP qualification lots.

Note 2: Generic Package/Process data