

RELIABILITY REPORT

FOR

MAX225CWI+ (MAX220-MAX249)

PLASTIC ENCAPSULATED DEVICES

April 9, 2009

MAXIM INTEGRATED PRODUCTS

120 SAN GABRIEL DR. SUNNYVALE, CA 94086

| Approved by | |
|-----------------------------------|--|
| Ken Wendel | |
| Quality Assurance | |
| Director, Reliability Engineering | |



Conclusion

The MAX225CWI+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim's quality and reliability standards.

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I. Device Description

A. General

The MAX220-MAX249 family of line drivers/receivers is intended for all EIA/TIA-232E and V.28/V.24 communications interfaces, particularly applications where $\pm 12V$ is not available. These parts are especially useful in battery-powered systems, since their low-power shutdown mode reduces power dissipation to less than 5µW. The MAX225, MAX233, MAX235, and MAX245/MAX246/MAX247 use no external components and are recommended for applications where printed circuit board space is critical.



II. Manufacturing Information

| ۸ | Description/Function: | |
|----|-----------------------|--|
| А. | Description/Function: | |

- B. Process:
- C. Number of Device Transistors:
- D. Fabrication Location:
- E. Assembly Location:
- F. Date of Initial Production:

III. Packaging Information

| A. Package Type: | 28-pin SOIC (W) |
|---|--------------------------|
| B. Lead Frame: | Copper |
| C. Lead Finish: | 100% matte Tin |
| D. Die Attach: | Conductive Epoxy |
| E. Bondwire: | Gold (1.3 mil dia.) |
| F. Mold Material: | Epoxy with silica filler |
| G. Assembly Diagram: | #05-1901-0002 |
| H. Flammability Rating: | Class UL94-V0 |
| I. Classification of Moisture Sensitivity per JEDEC standard J-STD-020-C | Level 1 |
| J. Single Layer Theta Ja: | 80°C/W |
| K. Single Layer Theta Jc: | 18°C/W |
| L. Multi Layer Theta Ja: | 59°C/W |
| M. Multi Layer Theta Jc: | 18°C/W |

IV. Die Information

| A. Dimensions: | 140 X 155 mils |
|----------------------------|--|
| B. Passivation: | Si_3N_4/SiO_2 (Silicon nitride/ Silicon dioxide |
| C. Interconnect: | Aluminum/Cu (Cu = 0.5%) |
| D. Backside Metallization: | None |
| E. Minimum Metal Width: | Metal1 = 0.5 / Metal2 = 0.6 / Metal3 = 0.6 microns (as drawn) |
| F. Minimum Metal Spacing: | Metal1 = 0.45 / Metal2 = 0.5 / Metal3 = 0.6 microns (as drawn) |
| G. Bondpad Dimensions: | 5 mil. Sq. |
| H. Isolation Dielectric: | SiO ₂ |
| I. Die Separation Method: | Wafer Saw |
| | |

+5V-Powered, Multichannel RS-232 Drivers/Receivers

ATP Philippines, Carsem Malaysia

M6

Oregon

Pre 1997



V. Quality Assurance Information

| A. Quality Assurance Contacts: | Ken Wendel (Director, Reliability Engineering) Bryan Preeshl (Managing Director of QA) |
|-----------------------------------|--|
| B. Outgoing Inspection Level: | 0.1% for all electrical parameters guaranteed by the Datasheet.0.1% For all Visual Defects. |
| C. Observed Outgoing Defect Rate: | < 50 ppm |
| D. Sampling Plan: | Mil-Std-105D |

VI. Reliability Evaluation

A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate (λ) is calculated as follows:

 $\lambda = \underbrace{1}_{\text{MTTF}} = \underbrace{1.83}_{192 \text{ x } 4340 \text{ x } 180 \text{ x } 2} \text{ (Chi square value for MTTF upper limit)} \\ \text{(where } 4340 = \text{Temperature Acceleration factor assuming an activation energy of 0.8eV)} \\ \lambda = 6.0 \text{ x } 10^{-9}$

𝔅 = 6.0 F.I.T. (60% confidence level @ 25°C)

The following failure rate represents data collected from Maxim's reliability monitor program. Maxim performs quarterly 1000 hour life test monitors on its processes. This data is published in the Product Reliability Report found at http://www.maxim-ic.com/. Current monitor data for the M6 Process results in a FIT Rate of 2.10 @ 25C and 25.46 @ 55C (0.8 eV, 60% UCL)

B. Moisture Resistance Tests

The industry standard 85°C/85%RH or HAST testing is monitored per device process once a quarter.

C. E.S.D. and Latch-Up Testing

The RS04 die type has been found to have all pins able to withstand a HBM transient pulse of +/-1500 V per Mil-Std 883 Method 3015.7. Latch-Up testing has shown that this device withstands a current of +/-100 mA.



MAX225CWI+ TEST ITEM **TEST CONDITION** FAILURE SAMPLE SIZE NUMBER OF **IDENTIFICATION** FAILURES Static Life Test (Note 1) Ta = 135°C **DC** Parameters 180 0 Biased & functionality Time = 192 hrs. Moisture Testing (Note 2) 85/85 Ta = 85°C **DC** Parameters 77 0 RH = 85% & functionality Biased Time = 1000hrs. Mechanical Stress (Note 2) 0 Temperature -65°C/150°C **DC** Parameters 77 Cycle 1000 Cycles & functionality Method 1010

Note 1: Life Test Data may represent plastic DIP qualification lots.

Note 2: Generic Package/Process data

Table 1 Reliability Evaluation Test Results