

MAX20751 Qualification Report

Report approved:
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Quality and Reliability
Maxim Integrated
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Process Technology: 0.18 μ m, 1 Poly, 4 Metal, 1.8/3.3V Package Type: QFN-36, SAC105 (MSL Level = 3)

Summary of Results (Product Related Stress Test)

Test(s)	Spec. Ref.	Conditions	Read Points	Pass Criteria	Lot 1	Lot 2	Lot 3	Comments
High	JESD22-A108	$T_A = 125$ °C	168 Hours	0/77	0/77	0/77	0/77	Pass
Temperature	JLSD22-A100	$V_{DD} = 2.1 V$	500 Hours	0/77	0/77	0/77	0/77	1 433
Operational Life		$V_{DD3.3} = 3.63V$	1000 Hours	0/77	0/77	0/77	0/77	
(HTOL)		V _{DD3.3} — 3.03 V	1000 Hours	0///	0/77	0/77	0///	
Electro Static	JS-001-2012	HBM	Post-ESD					Pass
Discharge	Formerly		500 Volts	0/24	0/24	0/24	0/24	HBM ESD Rating > 2000 Volts
Human Body	JESD22-A114		1000 Volts	0/24	0/24	0/24	0/24	
Model			1500 Volts	0/24	0/24	0/24	0/24	
(HBM)			2000 Volts	0/24	0/24	0/24	0/24	
ESD	JESD22-C101	CDM	Post-ESD					Pass
Characterization			100 Volts	0/3	0/3	0/3	0/3	CDM ESD Rating > 1000 Volts
Charge Device			200 Volts	0/3	0/3	0/3	0/3	
Model			500 Volts	0/3	0/3	0/3	0/3	
			1000 Volts	0/3	0/3	0/3	0/3	
			1500 Volts	0/3	0/3	1/3	0/3	
			2000 Volts	0/3	0/3	2/3	1/3	
Latch-Up	JESD78	T _A =125°C at	Post-LU					Pass
(LU)		$V_{DD} = Max.$	Pass ATE	0/30	0/30	0/30	0/30	Class 2
		$Max clamp = \pm 100 mA$						



Silicon Foundry: TSMC
Assembly Sub-Contractor: Carsem

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Summary of Results (Package Related Stress Test)

Test(s)	Spec. Ref.	Conditions	Read Points	Pass Criteria	Lot 1	Lot 2	Lot 3	Comments QFN-64, Carsem, Au Wire
Temperature Cycling (TC) *	JESD22-A104	Condition B -55°C to 125°C	500 Cycles 1000 Cycles	0/77 0/77	0/77 0/77	0/77 0/77	0/77 0/77	Pass
High Temperature Storage (HTS) *	JESD22-A103	Condition B 150°C	500 Hours 1000 Hours	0/77 0/77	0/77 0/77	0/77 0/77	0/77 0/77	Pass
Highly Accelerated Stress Test (HAST) *	JESD22-A110	130°C, 2 ATM, 85% RH, V _{DD} = 1.8 V	96 Hours	0/77	0/77	0/77	0/77	Pass

^{*} Pre-conditioning prior to TC, HTS and HAST stress tests (per JESD22-A113 and J-STD-020) Level 3 at 260°C.

