

RELIABILITY REPORT

FOR

MAX1983EUT+ (MAX1982/MAX1983)

PLASTIC ENCAPSULATED DEVICES

February 8, 2009

MAXIM INTEGRATED PRODUCTS

120 SAN GABRIEL DR. SUNNYVALE, CA 94086

| Approved by |
|-----------------------------------|
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| Quality Assurance |
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Conclusion

The MAX1983EUT+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim's quality and reliability standards.

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I. Device Description

A. General

The MAX1982/MAX1983 are low-voltage, low-dropout linear regulators with an external bias supply input. The 5V bias supply drives the gate of the internal N-channel pass transistor, making these devices ideal for applications that require low-voltage outputs from low-voltage inputs. The MAX1982 delivers 1.2V (±3%) at 300mA from an input voltage of 1.25V to 5.5V. The MAX1983 delivers an adjustable output voltage from 0.8V to 2V. The MAX1982/MAX1983 include a current-limit and thermal shutdown that protects the regulator in the event of a fault condition. Both devices are offered in a 6-pin SOT23 package and are specified over the extended (-40°C to +85°C) temperature range.



II. Manufacturing Information

A. Description/Function: Low-Voltage, Low-Dropout Linear Regulators with External Bias Supply

B. Process: B8

C. Number of Device Transistors:

D. Fabrication Location: Texas

E. Assembly Location: Carsem Malaysia, UTL Thailand, Unisem Malaysia

F. Date of Initial Production: July 26, 2002

III. Packaging Information

A. Package Type: 6-pin SOT23
B. Lead Frame: Copper

C. Lead Finish:

D. Die Attach:

Conductive Epoxy

E. Bondwire:

Gold (1 mil dia.)

F. Mold Material:

Epoxy with silica filler

G. Assembly Diagram:

#05-3801-0014

H. Flammability Rating:

Class UL94-V0

I. Classification of Moisture Sensitivity per Level 1

JEDEC standard J-STD-020-C

J. Single Layer Theta Jb: 115*°C/WK. Single Layer Theta Jc: 80°C/W

IV. Die Information

A. Dimensions: 57 X 35 mils

B. Passivation: Si₃N₄/SiO₂ (Silicon nitride/ Silicon dioxide

C. Interconnect: Aluminum/Si (Si = 1%)

D. Backside Metallization: None

E. Minimum Metal Width: 0.8 microns (as drawn)F. Minimum Metal Spacing: 0.8 microns (as drawn)

G. Bondpad Dimensions: 5 mil. Sq.
 H. Isolation Dielectric: SiO₂
 I. Die Separation Method: Wafer Saw



V. Quality Assurance Information

A. Quality Assurance Contacts: Ken Wendel (Director, Reliability Engineering)

Bryan Preeshl (Managing Director of QA)

B. Outgoing Inspection Level: 0.1% for all electrical parameters guaranteed by the Datasheet.

0.1% For all Visual Defects.

C. Observed Outgoing Defect Rate: < 50 ppmD. Sampling Plan: Mil-Std-105D

VI. Reliability Evaluation

A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate (λ) is calculated as follows:

$$\lambda = \frac{1}{\text{MTTF}} = \frac{1.83}{192 \times 4340 \times 140 \times 2}$$
(Chi square value for MTTF upper limit)

(where 4340 = Temperature Acceleration factor assuming an activation energy of 0.8eV)

$$x = 7.7 \times 10^{-9}$$

3 = 7.7 F.I.T. (60% confidence level @ 25°C)

The following failure rate represents data collected from Maxim's reliability monitor program. Maxim performs quarterly 1000 hour life test monitors on its processes. This data is published in the Product Reliability Report found at http://www.maxim-ic.com/. Current monitor data for the B8 Process results in a FIT Rate of 2.71 @ 25C and 17.30 @ 55C (0.8 eV, 60% UCL)

B. Moisture Resistance Tests

The industry standard 85°C/85%RH or HAST testing is monitored per device process once a quarter.

C. E.S.D. and Latch-Up Testing

The PD19-1 die type has been found to have all pins able to withstand a HBM transient pulse of +/-1000 V per Mil-Std 883 Method 3015.7. Latch-Up testing has shown that this device withstands a current of +/-250 mA.



Table 1Reliability Evaluation Test Results

MAX1983EUT+

| TEST ITEM | TEST CONDITION | FAILURE IDENTIFICATION | SAMPLE SIZE | NUMBER OF FAILURES | |
|------------------|-----------------|---------------------------|-------------|-----------------------|--|
| Static Life Test | (Note 1) | | | | |
| | Ta = 135°C | DC Parameters | 140 | 0 | |
| | Biased | & functionality | | | |
| | Time = 192 hrs. | ŕ | | | |
| Moisture Testing | g (Note 2) | | | | |
| 85/85 | Ta = 85°C | DC Parameters | 77 | 0 | |
| | RH = 85% | & functionality | | | |
| | Biased | • | | | |
| | Time = 1000hrs. | | | | |
| Mechanical Stre | ss (Note 2) | | | | |
| Temperature | -65°C/150°C | DC Parameters | 77 | 0 | |
| Cycle | 1000 Cycles | & functionality | | | |
| | Method 1010 | • | | | |

Note 1: Life Test Data may represent plastic DIP qualification lots.

Note 2: Generic Package/Process data