

RELIABILITY REPORT FOR MAX1782ETM+ PLASTIC ENCAPSULATED DEVICES

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MAXIM INTEGRATED PRODUCTS

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Approved by
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Conclusion

The MAX1782ETM+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim"s quality and reliability standards.

Table of Contents

- I.Device Description
- II.Manufacturing Information
- III.Packaging Information
-Attachments

VI.Reliability Evaluation

V.Quality Assurance Information

IV.Die Information

I. Device Description

A. General

The MAX1782 smart battery-pack controller integrates a user-programmable microcontroller core, a coulomb-counting fuel gauge, a multi-channel data-acquisition unit, and an SMBus(tm) v1.1 compliant master/slave SMBus interface. The 8-bit, RISC microcontroller core has an integrated 8k bytes of user programmable EEPROM, which provides battery-pack designers with complete flexibility in developing fuel gauging and control algorithms. The MAX1782 is equipped with full ICE (in-circuit emulator) capability for efficient firmware development and debugging. The 16-bit data-acquisition unit measures individual cell voltages, cell stack voltage, chip internal/external temperature, and two general purpose analog inputs. Individual cell voltage measurements with 0.85% accuracy and over-current protection allow the MAX1782 to eliminate a separate 1st level protection IC. Adjustable over-current thresholds and delay timers provide a flexible solution. The MAX1782 provides an additional input which allows the protection MOSFETs to be forced off by external hardware. The integrating fuel gauge module provides a typical input offset of less than 1µV, and gain accuracy of better than 1.5% with no trimming required during pack manufacture. The MAX1782 has a wide 4V to 28V operating voltage range. The IC is available in a 7mm x 7mm 48-TQFN package with a maximum thickness of 0.8mm.



A. Description/Function:	Advanced Smart Battery-Pack Controller
B. Process:	EB8
C. Number of Device Transistors:	

California or Texas

China, Thailand

March 12, 2004

- D. Fabrication Location:
- E. Assembly Location:
- F. Date of Initial Production:

III. Packaging Information

A. Package Type:	48-pin TQFN 7x7
B. Lead Frame:	Copper
C. Lead Finish:	100% matte Tin
D. Die Attach:	Conductive Epoxy
E. Bondwire:	Gold (1 mil dia.)
F. Mold Material:	Epoxy with silica filler
G. Assembly Diagram:	#05-9000-0098
H. Flammability Rating:	Class UL94-V0
I. Classification of Moisture Sensitivity per JEDEC standard J-STD-020-C	Level 1
J. Single Layer Theta Ja:	36°C/W
K. Single Layer Theta Jc:	0.8°C/W
L. Multi Layer Theta Ja:	25°C/W
M. Multi Layer Theta Jc:	0.8°C/W

IV. Die Information

A. Dimensions:	220 X 220 mils
B. Passivation:	Si_3N_4/SiO_2 (Silicon nitride/ Silicon dioxide)
C. Interconnect:	AI/0.5%Cu with Ti/TiN Barrier
D. Backside Metallization:	None
E. Minimum Metal Width:	3.0 microns (as drawn)
F. Minimum Metal Spacing:	3.0 microns (as drawn)
G. Bondpad Dimensions:	5 mil. Sq.
H. Isolation Dielectric:	SiO ₂
I. Die Separation Method:	Wafer Saw



V. Quality Assurance Information

A. Quality Assurance Contacts:	Ken Wendel (Director, Reliability Engineering) Bryan Preeshl (Managing Director of QA)
B. Outgoing Inspection Level:	0.1% for all electrical parameters guaranteed by the Datasheet. 0.1% For all Visual Defects.
C. Observed Outgoing Defect Rate:	< 50 ppm
D. Sampling Plan:	Mil-Std-105D

VI. Reliability Evaluation

A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate (λ) is calculated as follows:

 $\lambda = \underbrace{1}_{MTTF} = \underbrace{1.83}_{192 \times 4340 \times 73 \times 2}$ (Chi square value for MTTF upper limit) $\lambda = 14.7 \times 10^{-9}$ $\lambda = 14.7 \text{ F.I.T. (60\% confidence level @ 25°C)}$

The following failure rate represents data collected from Maxim's reliability monitor program. Maxim performs quarterly 1000 hour life test monitors on its processes. This data is published in the Product Reliability Report found at http://www.maxim-ic.com/. Current monitor data for the EB8 Process results in a FIT Rate of 0.28 @ 25C and 17.30 @ 55C (0.8 eV, 60% UCL)

B. Moisture Resistance Tests

The industry standard 85°C/85%RH or HAST testing is monitored per device process once a quarter.

C. E.S.D. and Latch-Up Testing

The UC03-6 die type has been found to have all pins able to withstand a HBM transient pulse of +/-1000 V per JEDEC JESD22-A114. Latch-Up testing has shown that this device withstands a current of +/-250 mA.



Table 1 Reliability Evaluation Test Results

MAX1782ETM+

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	SAMPLE SIZE	NUMBER OF FAILURES	
Static Life Test	(Note 1)				
	Ta = 135°C	DC Parameters	73	0	
	Biased	& functionality			
	Time = 192 hrs.				
Moisture Testing	(Note 2)				
85/85	Ta = 85°C	DC Parameters	77	0	
	RH = 85%	& functionality			
	Biased				
	Time = 1000hrs.				
Mechanical Stre	ss (Note 2)				
Temperature	-65°C/150°C	DC Parameters	77	0	
Cycle	1000 Cycles	& functionality			
	Method 1010				

Note 1: Life Test Data may represent plastic DIP qualification lots.

Note 2: Generic Package/Process data