

7/2/2007

RELIABILITY REPORT FOR

## DS32B35, Rev A3

# **Dallas Semiconductor**

4401 South Beltwood Parkway Dallas, TX 75244-3292

Prepared by:

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### **Conclusion:**

The following qualification successfully meets the quality and reliability standards required of all Dallas Semiconductor products and processes:

DS32B35, Rev A3

#### **Device Description:**

A description of the device used in this qualification can be found in the product data sheet. You can find the product data sheet at http://dbserv.maxim-ic.com/l\_datasheet3.cfm.

#### **Reliability Derating:**

The Arrhenius model will be used to determine the acceleration factor for failure mechanisms that are temperature accelerated.

AfT = exp((Ea/k)\*(1/Tu - 1/Ts)) = tu/ts AfT = Acceleration factor due to Temperature tu = Time at use temperature (e.g. 55°C) ts = Time at stress temperature (e.g. 125°C) k = Boltzmann's Constant (8.617 x 10-5 eV/°K) Tu = Temperature at Use (°K) Ts = Temperature at Stress (°K) Ea = Activation Energy (e.g. 0.7 ev)

The activation energy of the failure mechanism is derived from either internal studies or industry accepted standards, or activation energy of 0.7ev will be used whenever actual failure mechanisms or their activation energies are unknown. All deratings will be done from the stress ambient temperature to the use ambient temperature.

An exponential model will be used to determine the acceleration factor for failure mechanisms, which are voltage accelerated.

AfV = exp(B\*(Vs - Vu)) AfV = Acceleration factor due to Voltage Vs = Stress Voltage (e.g. 7.0 volts) Vu = Maximum Operating Voltage (e.g. 5.5 volts) B = Constant related to failure mechanism type (e.g. 1.0, 2.4, 2.7, etc.)

The Constant, B, related to the failure mechanism is derived from either internal studies or industry accepted standards, or a B of 1.0 will be used whenever actual failure mechanisms or their B are unknown. All deratings will be done from the stress voltage to the maximum operating voltage. Failure rate data from the operating life test is reported using a Chi-Squared statistical model at the 60% or 90% confidence level (Cf).

The failure rate, Fr, is related to the acceleration during life test by:

Fr = X/(ts \* AfV \* AfT \* N \* 2)X = Chi-Sq statistical upper limit N = Life test sample size Failure Rates are reported in FITs (Failures in Time) or MTTF (Mean Time To Failure). The FIT rate is related to MTTF by:

MTTF = 1/Fr

NOTE: MTTF is frequently used interchangeably with MTBF.

The calculated failure rate for this device/process/assembly is:

FAILURE RATE:	MTTF (YRS):	23595	FITS:	4.8
	<b>DEVICE HOURS:</b>	200800	FAILS:	0

Only data from Operating Life or similar stresses are used for this calculation.

The parameters used to calculate this failure rate are as follows:

Cf: 60%	Ea: 0.7	B: 0	Tu: 25 °C	Vu: 5.5 Volts
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The reliability data follows. At the start of this data is the device information. This is a description of the device for this report. Following this is the assembly information. This section includes a description of the assembly vehicle used to generate this reliability data for both qualifications and monitors. The next section is the detailed reliability data for each stress found in the qualification / monitor. If there are additional assemblies used as part of this report, a description of each will follow which includes the respective reliability data for that assembly. The reliability data section includes the latest data available.

Device Information:	
Device: Process: Passivation: Die Size: Number of Transistors: Interconnect: Gate Oxide Thickness:	DS32B35 E6E-2P2M,HPVt,EPROM,LV-NRDSD,PF ALOCOS:GOI Passivation w/Nov TEOS Oxide-OxyNitride 89 x 141 0 Aluminum / 0.5% Copper 150 Å
Assembly Information:	
Qualification Vehicle: Assembly Site: Pin Count: Package Type: Body Size: Mold Compound: Lead Frame: Lead Frame: Lead Finsh: Die Attach: Bond Wire / Size: Theta JA: Theta JC:	DS32B35 CIRTEK 20 SOIC 2 Die & Welded Crystal (RoHS) 300x2.3 Sumitomo G600 Etched Copper CDA194 with welded crystal Sn Plate 100% Matte (With Anneal Bake) 84-1 LMISR4 Epoxy Silverfilled Ablebond Au / 1.0 mil
Flammability: Moisture Sensitivity (JEDEC J-STD20A)	UL 94-V0 Level 1
Date Code Range:	0636 to 0636
DATE CODE: 0636 LOT	NUMBER: QH639098AB-NPI

#### **OPERATING LIFE**

HIGH VOLTAGE LIFE	0636	125C, 3.6 VOLTS			168	HRS	100	0	
HIGH VOLTAGE LIFE	0636	125C, 3.6 VOLTS			168	HRS	100	0	
HIGH VOLTAGE LIFE	0636	125C, 3.6 VOLTS			168	HRS	100	0	
						Total:		0	
Device Information	:								
Device: Process: Passivation: Die Size: Number of Transistors: Interconnect: Gate Oxide Thickness:		DS32C35 E6E-2P2M,HPVt,EPROM,LV-NRDSD,PF ALOCOS:GOI Passivation w/Nov TEOS Oxide-OxyNitride 89 x 141 0 Aluminum / 0.5% Copper 150 Å							
Assembly Informat	ion:								
Assembly mormation:Qualification Vehicle:DS32C35Assembly Site:CIRTEKPin Count:20Package Type:SOIC 2 Die & Welded Crystal (RoHS)Body Size:300x2.3Mold Compound:Sumitomo G600Lead Frame:Etched Copper CDA194 with welded crystalLead Finsh:Sn Plate 100% Matte (With Anneal Bake)Die Attach:84-1 LMISR4 Epoxy Silverfilled AblebondBond Wire / Size:Au / 1.0 milTheta JA:Theta JC:Flammability:UL 94-V0Moisture SensitivityLevel 1(JEDEC J-STD20A)0636 to 0636DATE CODE:0636LOT NUMBER:Qualification ComponentQualification (Qualification (Qu									
OPERATING LIFE									
DESCRIPTION		DE CONDITION				DPOINT		FAILS	FA#
HIGH TEMP OP LIFE	0636	125C, 3.6 VOLTS			1000	HRS	100	0	
HIGH VOLTAGE LIFE	0636	125C, 3.6 VOLTS			168	HRS	100	0	
HIGH VOLTAGE LIFE	0636	125C, 3.6 VOLTS			168	HRS	100	0	
HIGH VOLTAGE LIFE	0636	125C, 3.6 VOLTS			168	HRS	100	0	
FAILURE RATE:	N	ITTF (YRS):	23595	FITS:		Total: 4.8		0	
		CE HOURS:	200800	FAILS:		4.0 0			
			200000	I ALO.		U			