

Programmable, Triple Axis Gyro & **Accelerometer Evaluation Board**

Preliminary Technical Data

ADIS1635x/PCB

GENERAL DESCRIPTION

The ADIS1635x/PCB includes the ADIS1635xAMLZ part and an evaluation board that provides convenient access to the ADIS1635x using standard 2mm, 2x6, connectors, which can be accessed using a variety of simple cabling options. The ADIS1635x/PCB enables quick integration into an existing digital platform (MCU, DSP, FPGA, PLD, etc). Four mounting holes (sized for 2-56 or 2mm screws) have been provided to secure the board during evaluation.

The ADIS1635x has dedicated data/control registers which are used to control all input/output activity. These registers can be accessed using the 4-wire serial port interface (SPI) signals on J1: SCLK, CS, DOUT and DIN. For specific information on using the ADIS1635x's SPI interface, refer to the ADIS16350 datasheet. Auxiliary functions, such as the 12-bit ADC input, 12-bit DAC output and digital I/O functions, can be accessed using J2. C1 and C2 are not installed but the pads are offered for additional filtering of power supply inputs.

Table 1 – ADIS1635x/PCB Parts List

Ref Des.	Part Description
	ADIS1635xAMLZ
	x – matches part ordered (ADIS1635x/PCBZ)
J1,J2	Hirose P/N A3-12PA-2SV(71)
	Mating connector: 3M P/N 152212-0100-GB
J3	Samtec P/N CLM- CLM-112-02-L-D-A
C1, C2	Not installed

SPECIAL NOTES ON HANDLING

Note that the ADIS1635x/PCB is not reverse polarity protected. Reversing the power supply or applying inappropriate voltages to any pin (outside the Absolute Maximum Ratings in the ADIS16350 data sheet) may damage the ADIS1635x.

ORDERING GUIDE

Model	Package Description
ADIS16350/PCBZ	Evaluation Board, RoHS Compliant
ADIS16354/PCBZ	Evaluation Board, RoHS Compliant
ADIS16355/PCBZ	Evaluation Board, RoHS Compliant

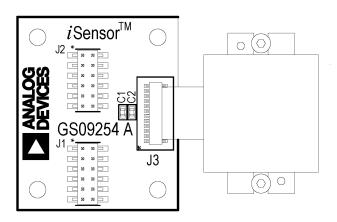


Figure 1 – Basic ADIS 1635x/PCB Assembly View

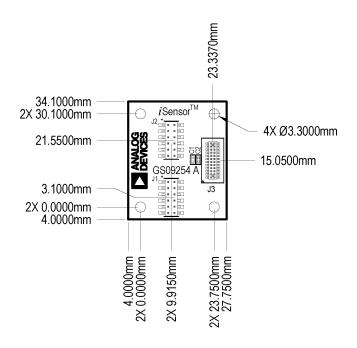


Figure 2 – Package drawing

J1 CONNECTIONS J3 CONNECTIONS RST - 1 1-DNC 2-SCLK DNC-2 CS - 3 DOUT-4 3 - SCLK 4 - DOUT CS-6 DNC - 5 6 - DIN 5 – DIN GND - 7 RST - 8 7 – DIO1 8 - GND GND - 9 10 - VCC VCC - 10 9 - DIO2 VCC - 11 12 - VCC VCC - 12 11 - VCC GND - 14 13 - GND DNC - 16 15 - GND J2 CONNECTIONS DNC - 18 17 - DNC DAC - 20 19 - DNC ADC - 1 2 - GND 21 - ADC DNC - 22 DAC - 3 4 - DNC DNC - 24 23 - DNC GND - 5 6 - DNC DNC - 7 8 - DNC VCC DNC - 9 10 - DNC DIO2 - 11 12 - DIO1 C2 GND

Figure 3 - Schematic

