

8

7

6

5

4

3

2

1

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RELAY CONTROL CHART

CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR

JUMPER TABLE		
JP#	ON	OFF
1		
2		
3		
4		
5		

* SEE ASSEMBLY INSTRUCTIONS

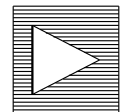
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	RELEASED	8/28/18	A.L.
B	CHANGES PER ENGINEER	1/18/19	A.L.
C	CHANGES PER ENGINEER	5/28/19	A.L.
D	CHANGES PER ENGINEER	18/7/19	A.L.
E	CHANGES PER ECR 093995	03/16/20	A.L.
G	CHANGES PER ECR 097949	06/01/20	A.L.

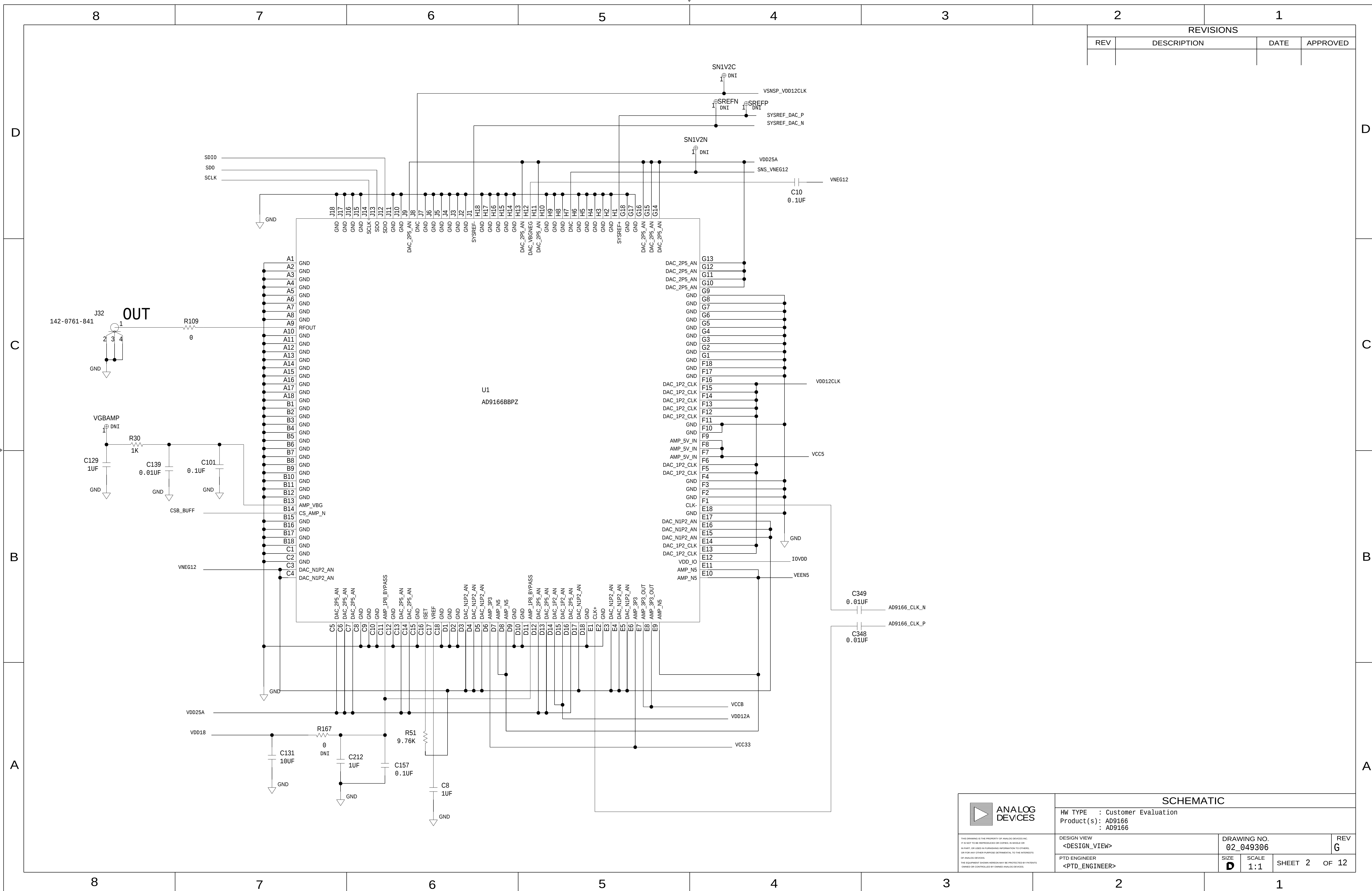
C

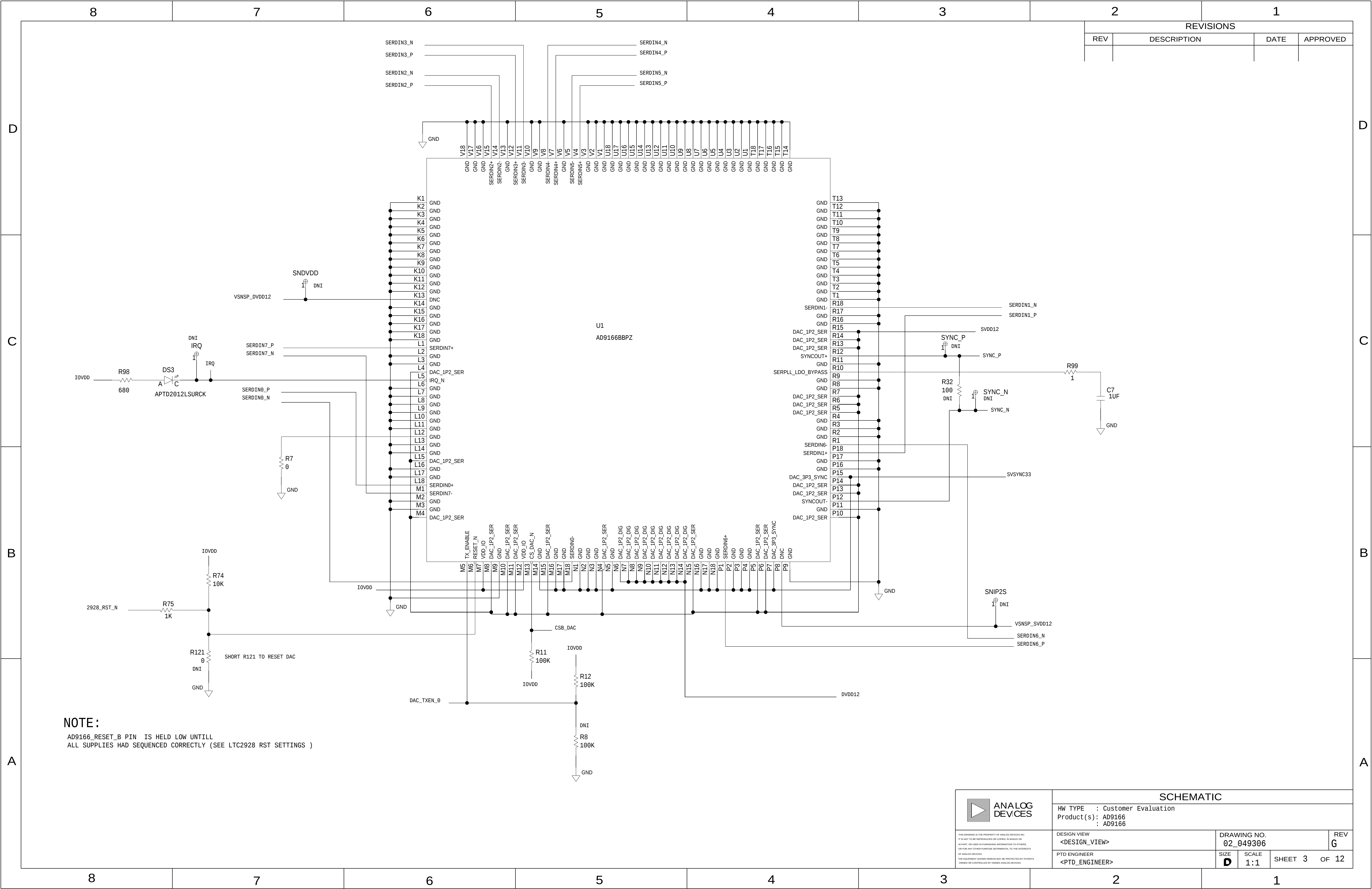
B

A

AUG 17 2020

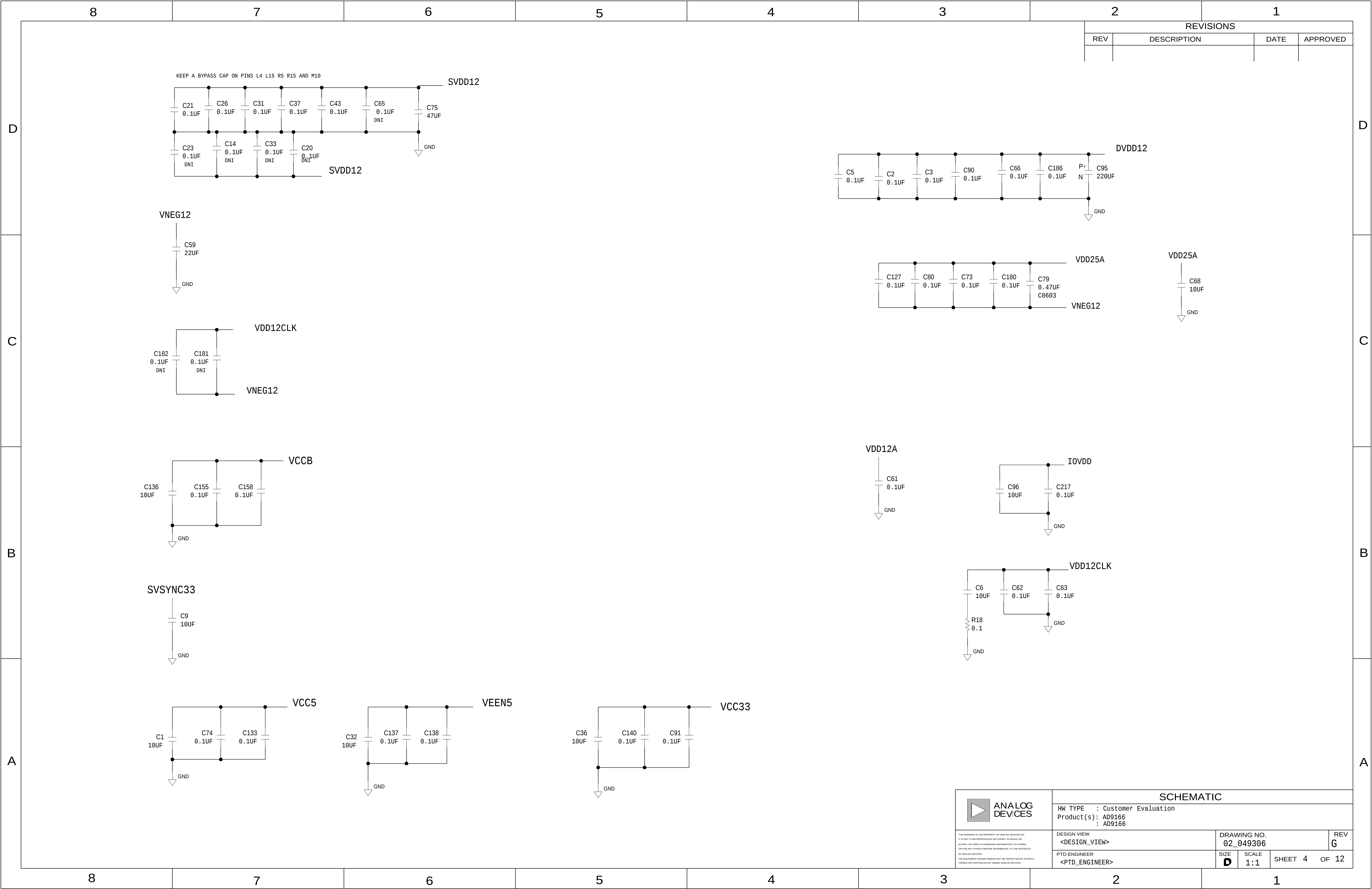
TEMPLATE ENGINEER	DATE	SCHEMATIC	 ANALOG DEVICES					
-								
HARDWARE SERVICES		HW TYPE : Customer Evaluation Product(s): AD9166 : AD9166 PACKAGE : 0-lead N/A N/A-family : Pitch-pitch StyleVendor Style <User Define> <User Define> <User Define>						
-								
HARDWARE SYSTEMS								
-								
TEST ENGINEER								
-		DRAWING NO. 02_049306						
COMPONENT ENGINEER								
-								
TEST PROCESS		REV. G						
-								
HARDWARE RELEASE		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES						
-								
DESIGNER		MASTER PROJECT TEMPLATE	TESTER TEMPLATE	DRAWING NO. 02_049306	REV. G			
-		TBD	no_template					
PTD ENGINEER		TOLERANCES						
<PTD_ENGINEER>								
CHECKER		DECIMALS	FRACTIONS	ANGLES	SIZE	SCALE	CODE ID NO.	SHEET 1 OF 12
-		X.XX +-0.010	+1/32	+2	D	1:1	CodeID	





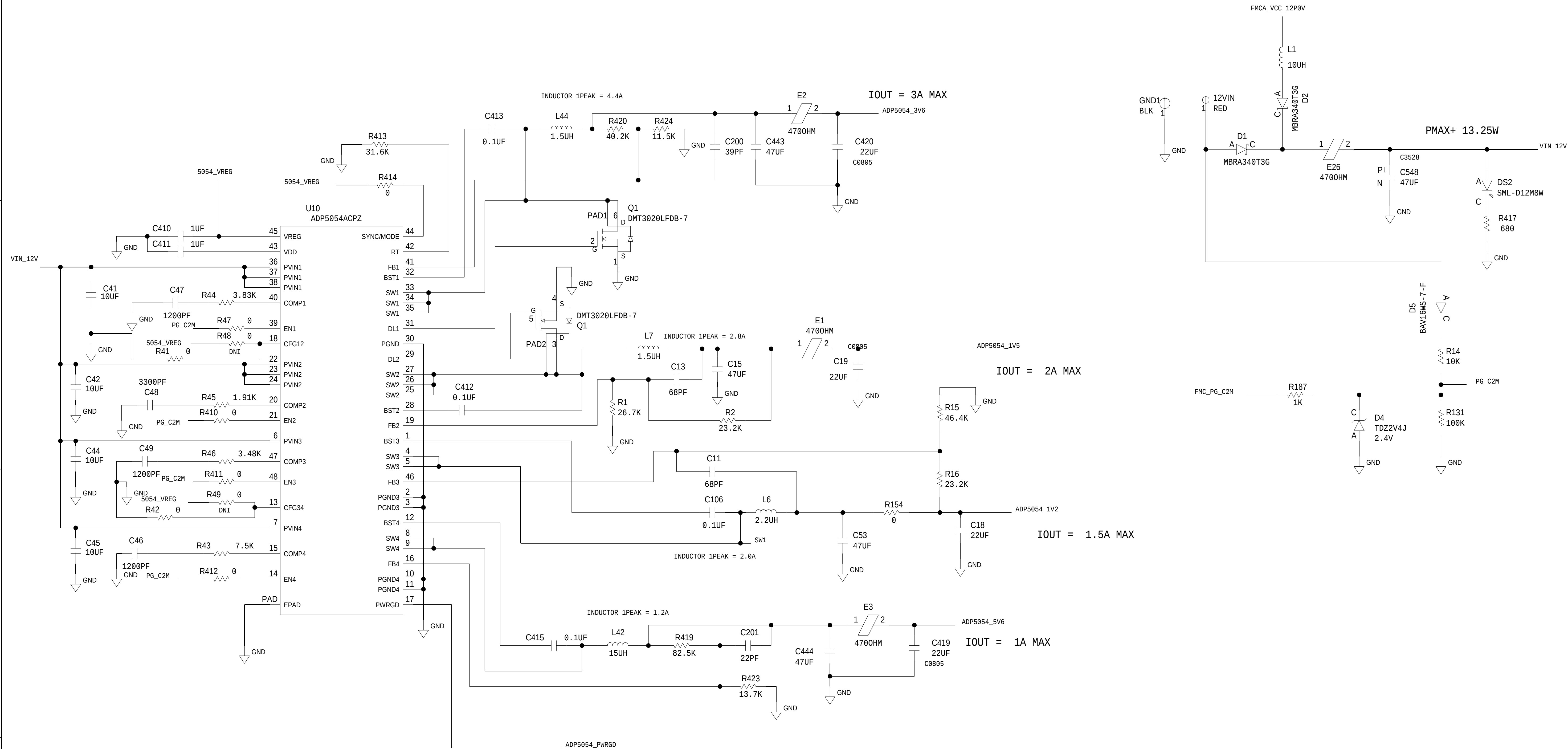
NOTE :
AD9166_RESET_B PIN IS HELD LOW UNTILL
ALL SUPPLIES HAD SEQUENCED CORRECTLY (SEE LTC2928 RST SETTINGS)

	SCHEMATIC			
	HW TYPE : Customer Evaluation Product(s): AD9166 : AD9166			
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_049306		REV G
	PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 3 OF 12

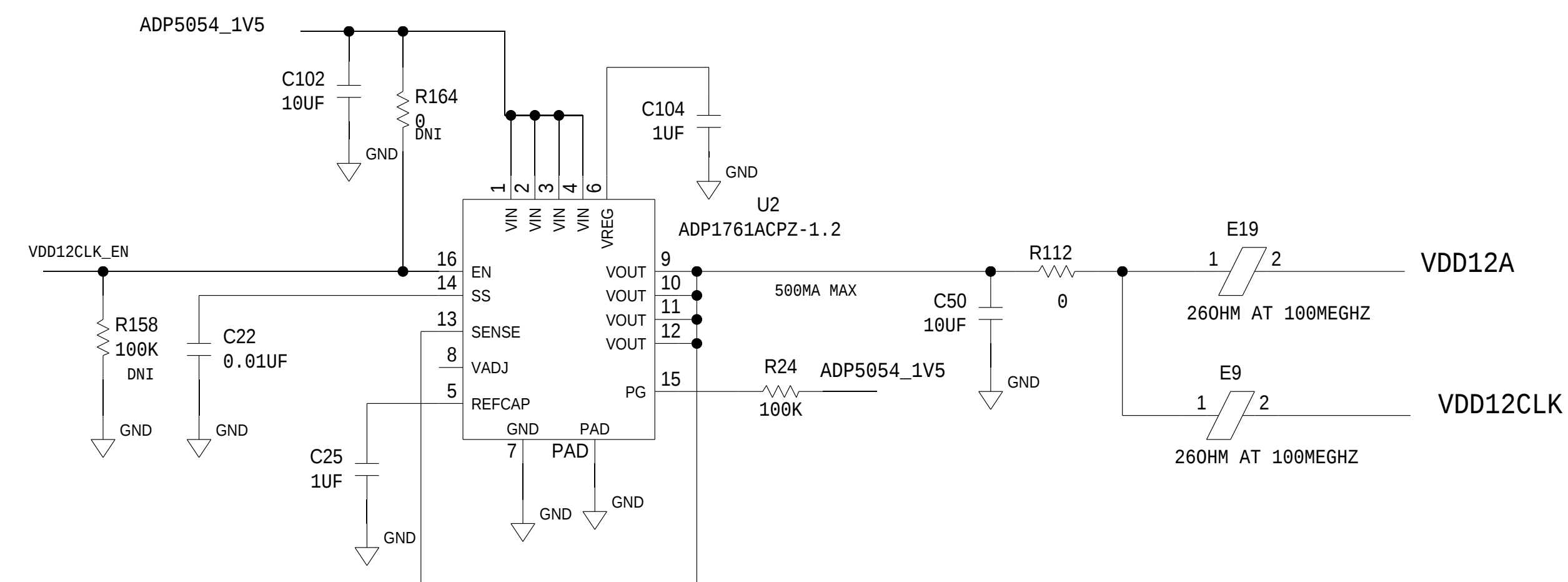


MAIN POWER REGULATOR

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

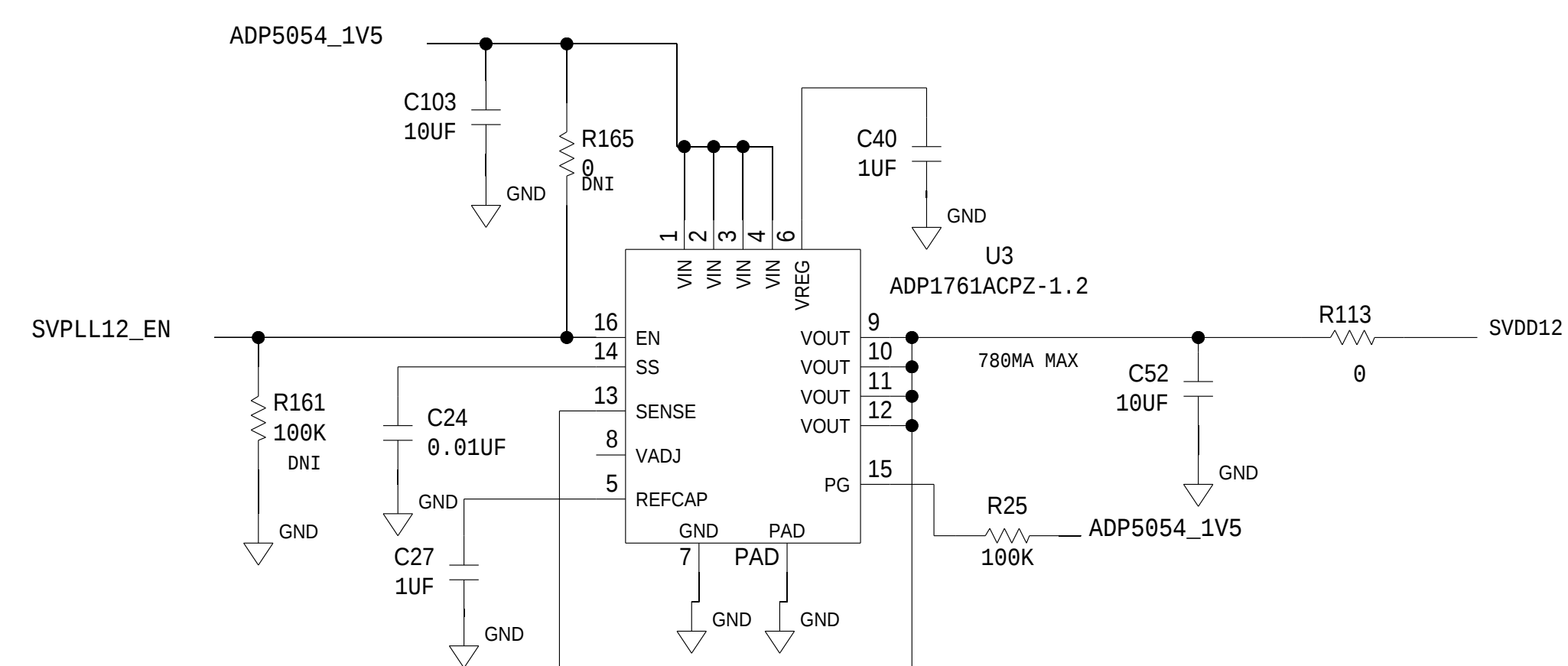


SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : AD9166 : AD9166			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_049306	REV G	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 5 OF 12

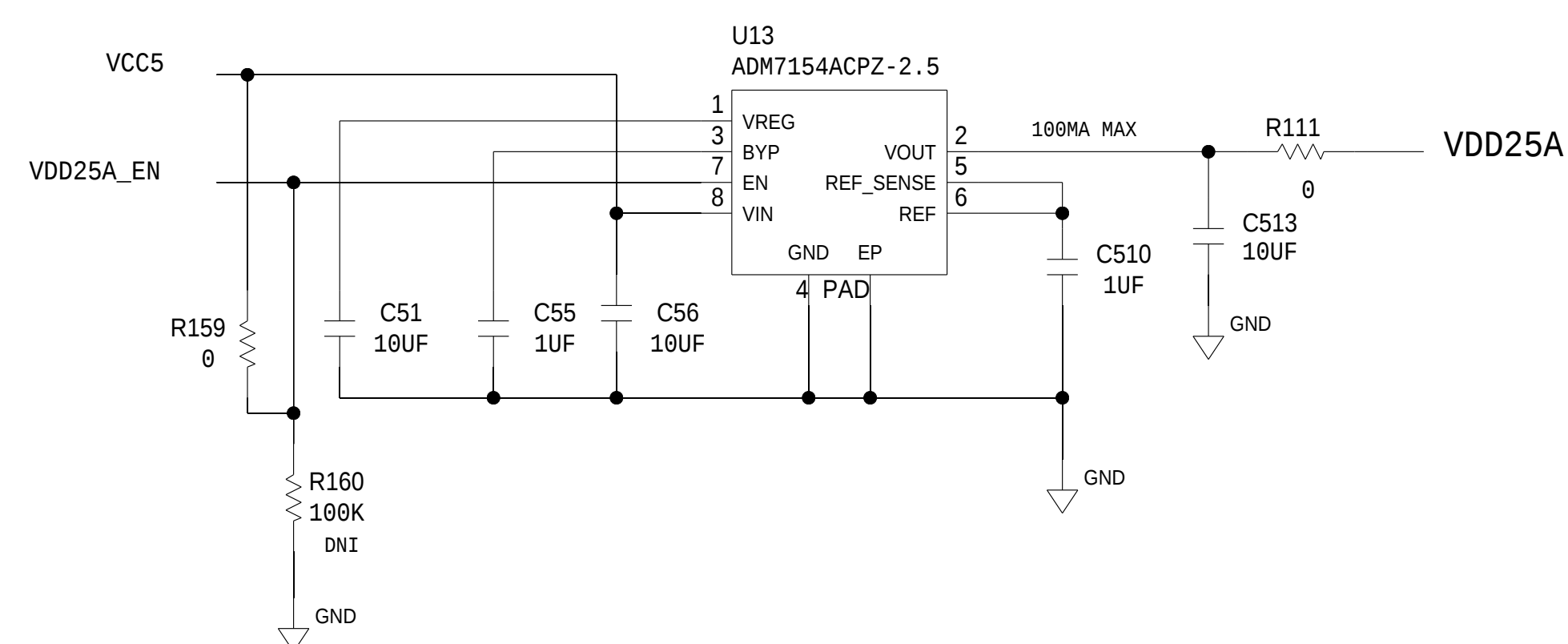


DAC
ANALOG 1.2V

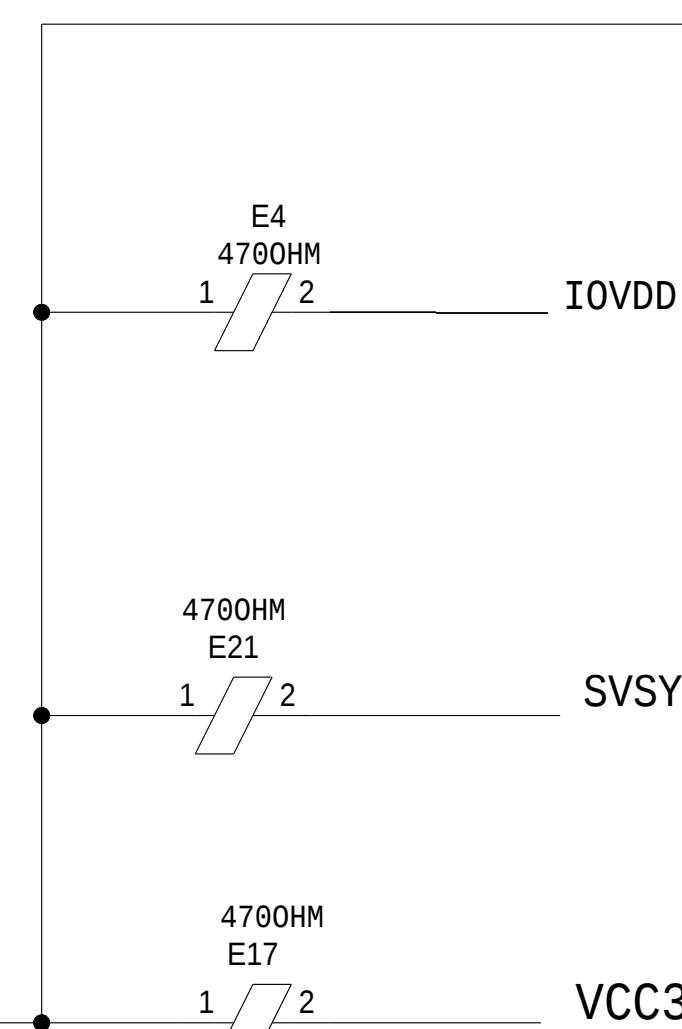
DAC
CLOCK SUPPLY



DAC
SERDES SUPPLY

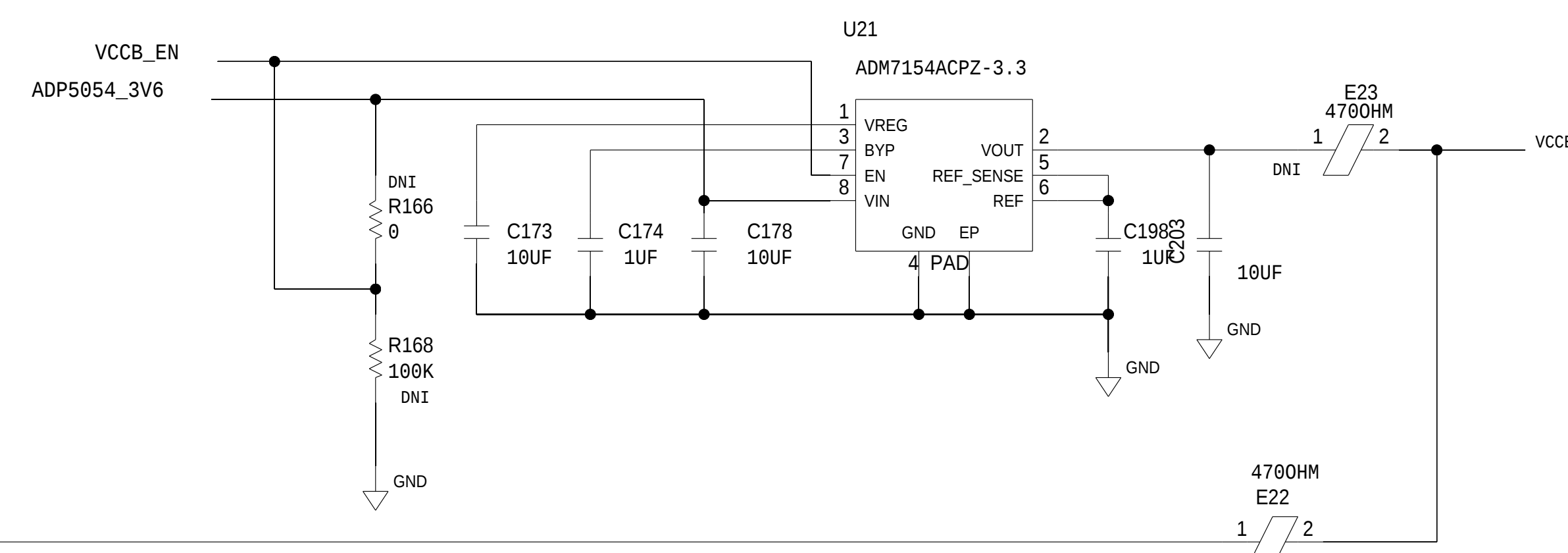
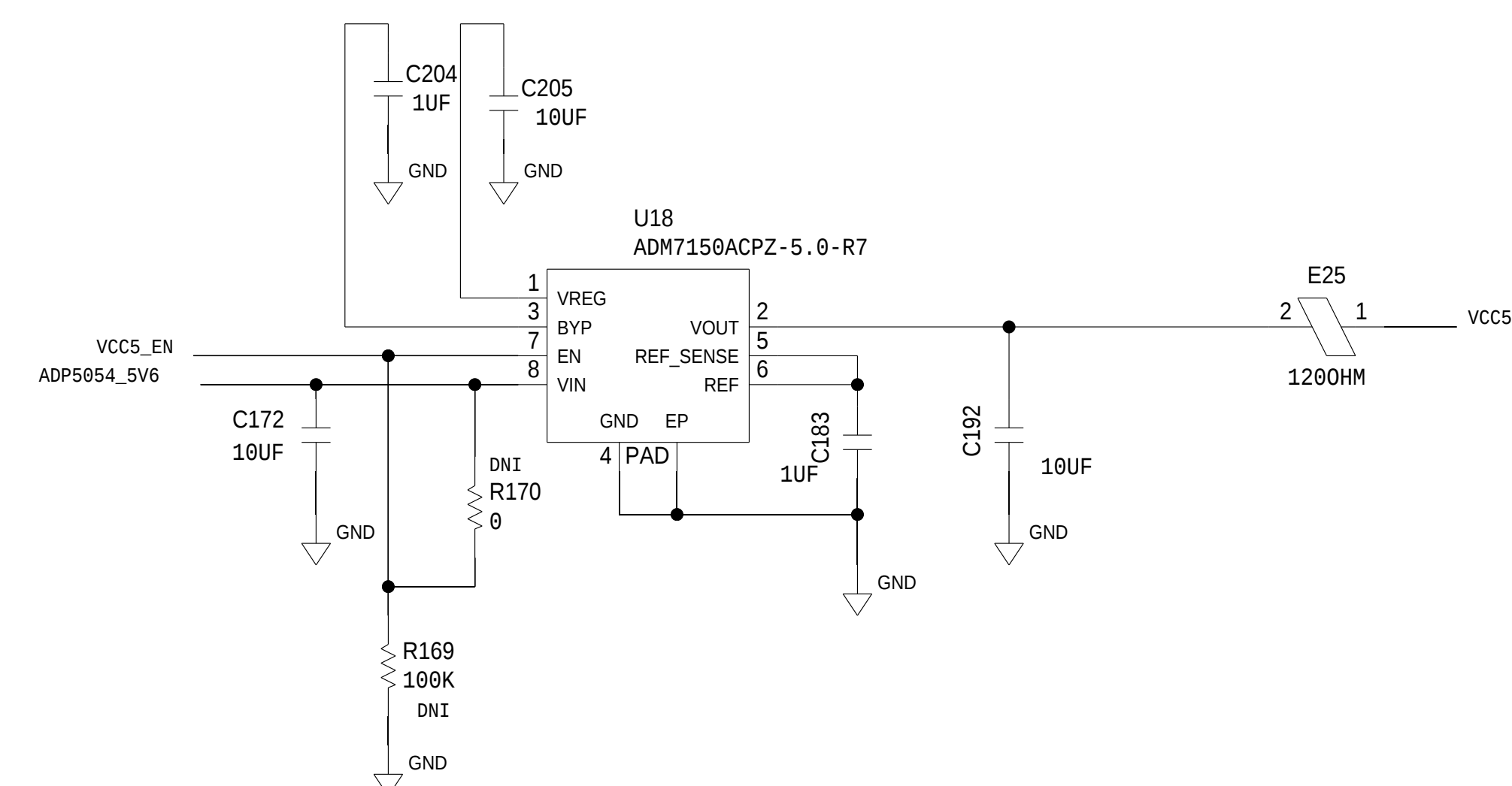
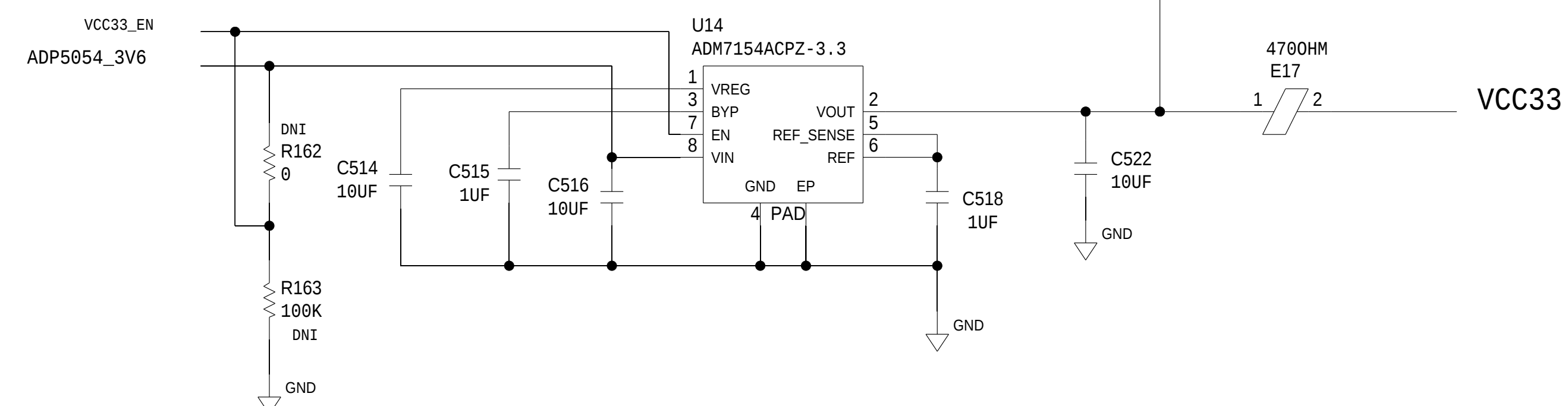


DAC
AMP ANALOG 2.5V



IOVDD
SPI & DAC 3.3V
20 MA MAX

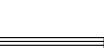
SYNC LOGIC
30 MA MAX

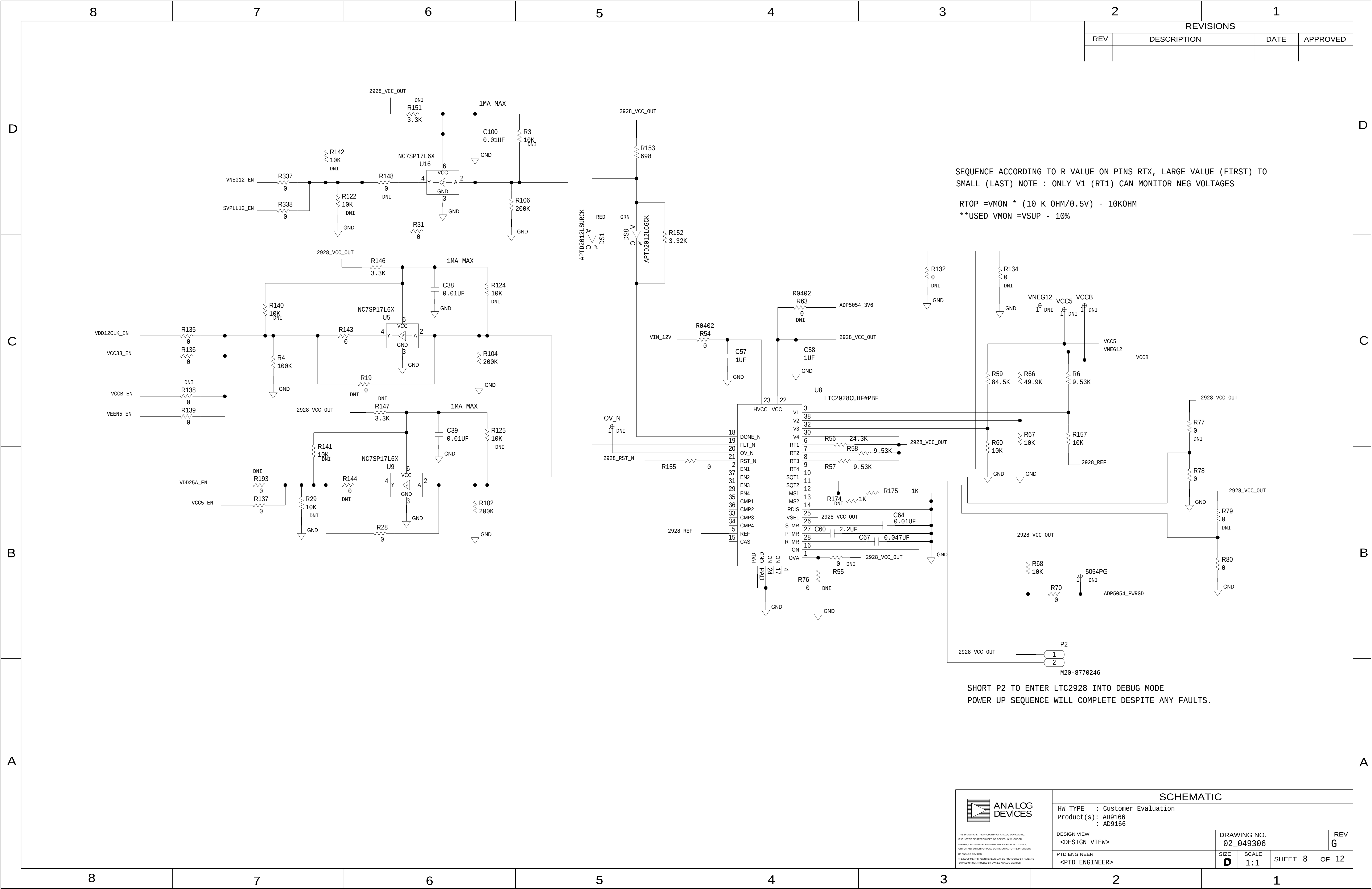


(DAC_IP2_DIG)

DAC
DIGITAL SUPPLY 1.2V

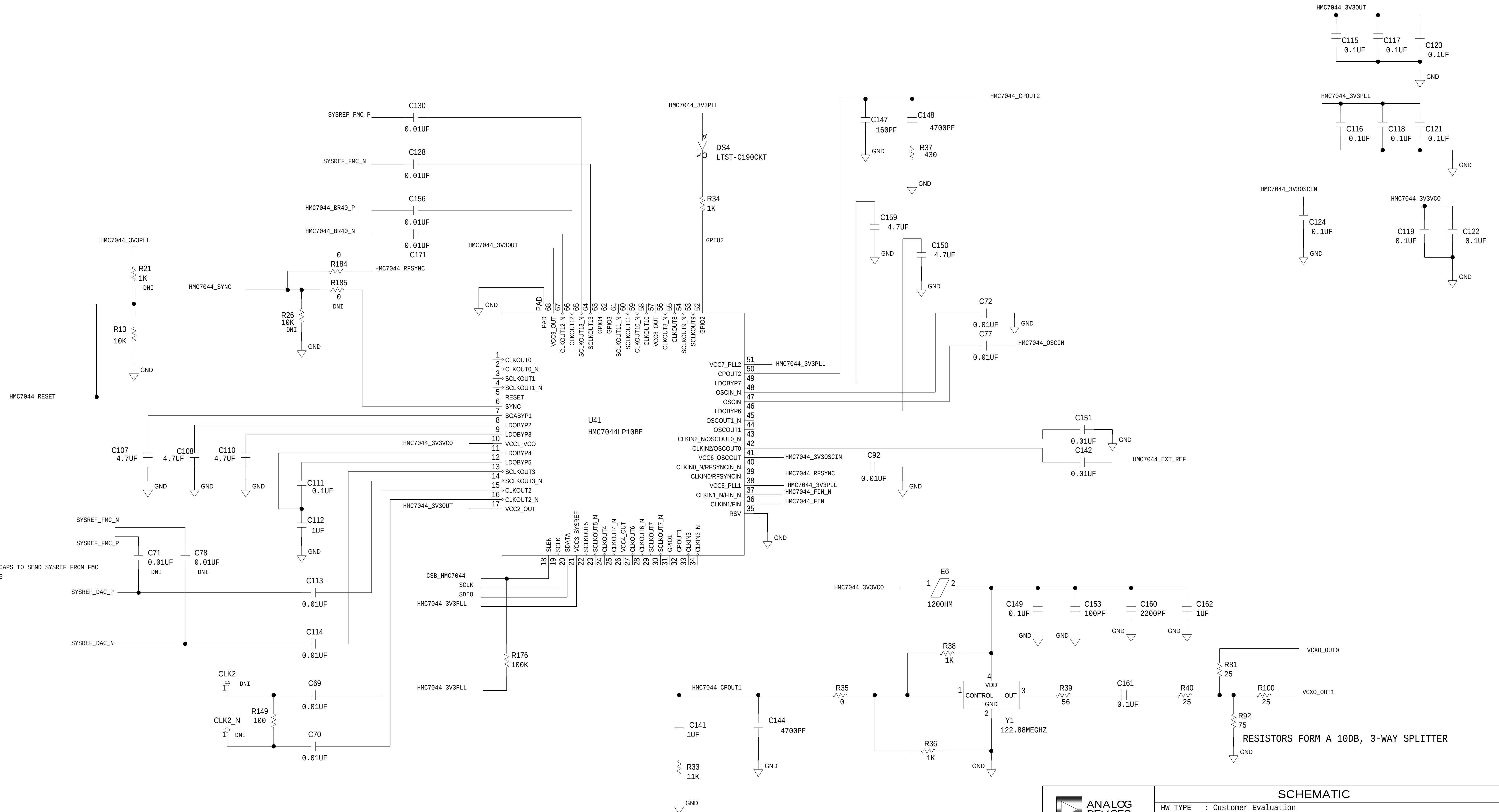


 ANALOG DEVICES	SCHEMATIC				
	HW TYPE : Customer Evaluation Product(s) : AD9166 : AD9166				
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THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES	PTD ENGINEER <PTD_ENGINEER>		SIZE D	SCALE 1 : 1	SHEET 6 OF 12



CLOCK - HMC7044

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



RESISTORS FORM A 10DB, 3-WAY SPLITTER



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OWNED OR CONTROLLED BY OWNED ANALOG DEVICES.

SCHEMATIC

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HW TYPE   : Customer Evaluation
Product(s): AD9166
           : AD9166
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DESIGN VIEW	
<DESIGN_VIEW>	

DRAWING NO.	02_049306
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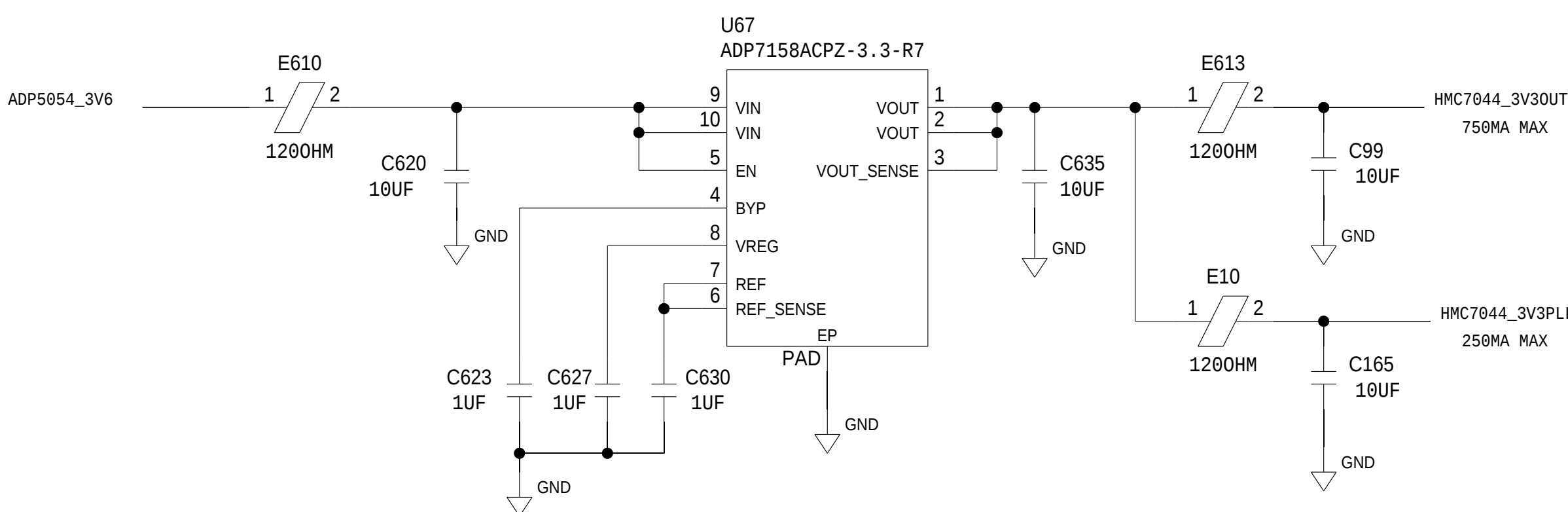
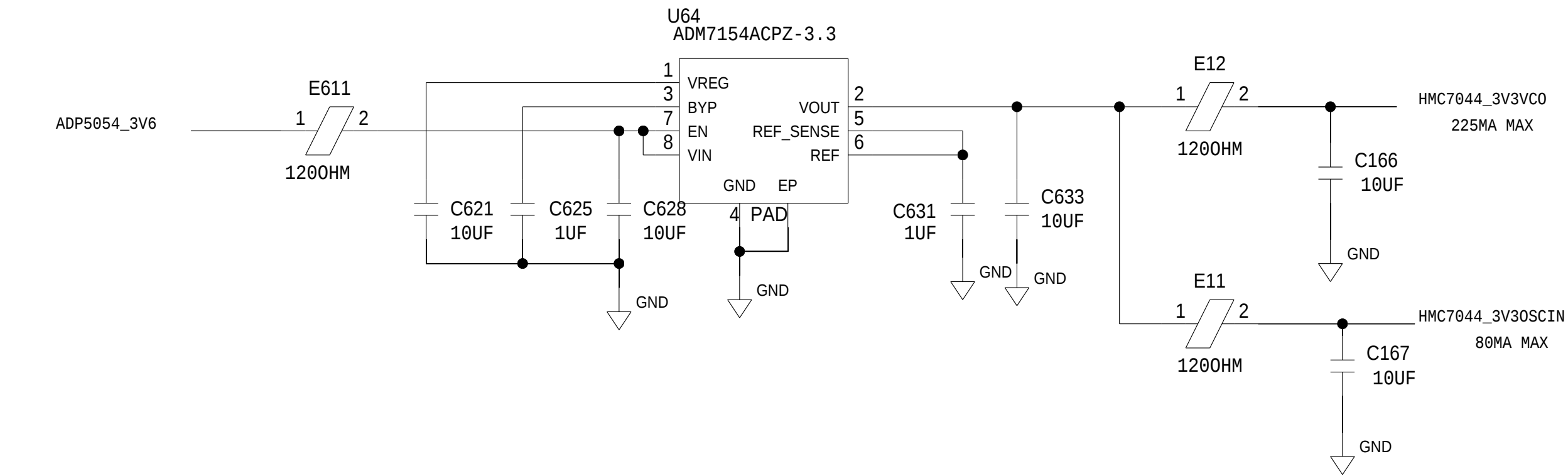
REV
G

PTD ENGINEER
<PTD_ENGINEER>

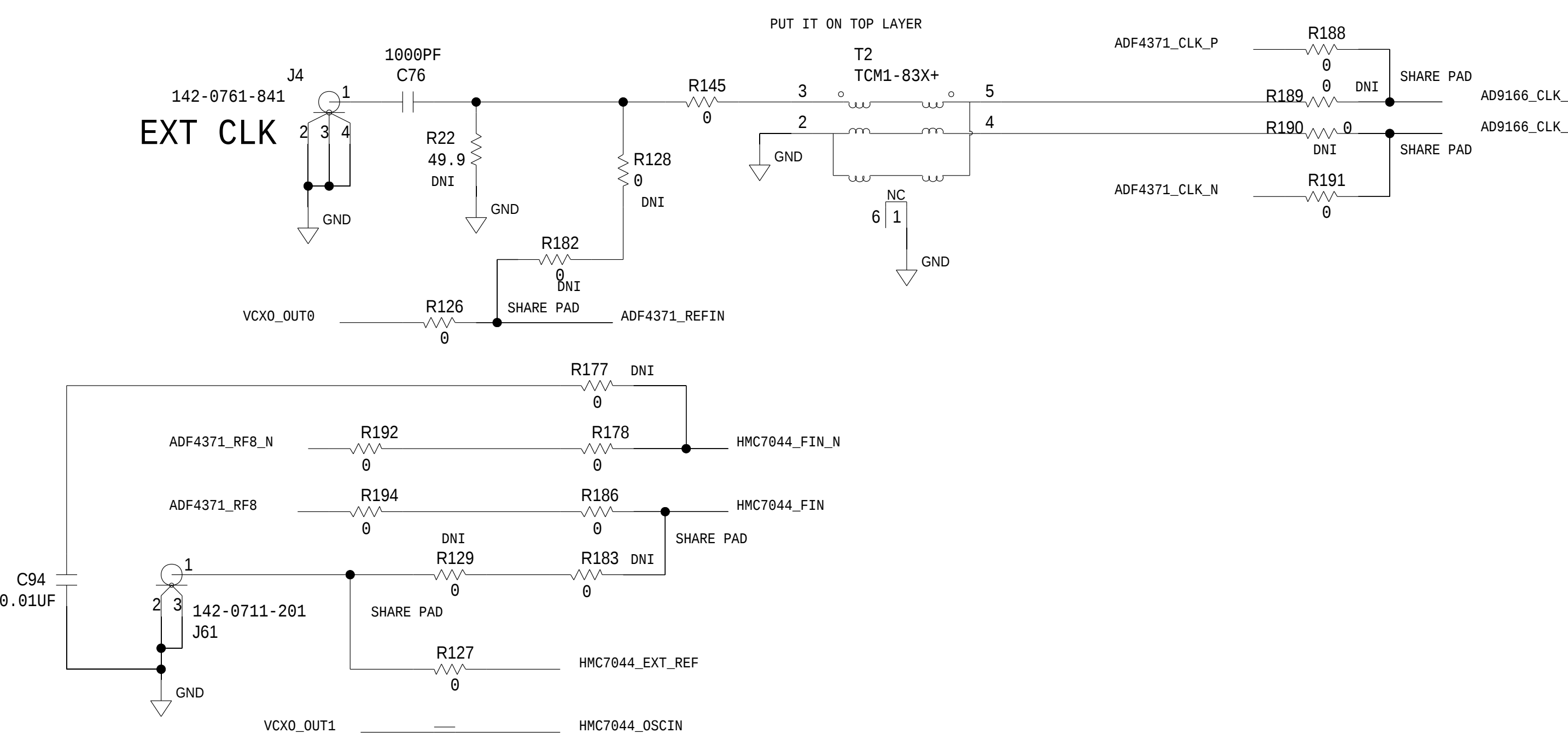
SIZE D	SCALE 1:1	SHEET 10 OF 12
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HMC7044 POWER

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

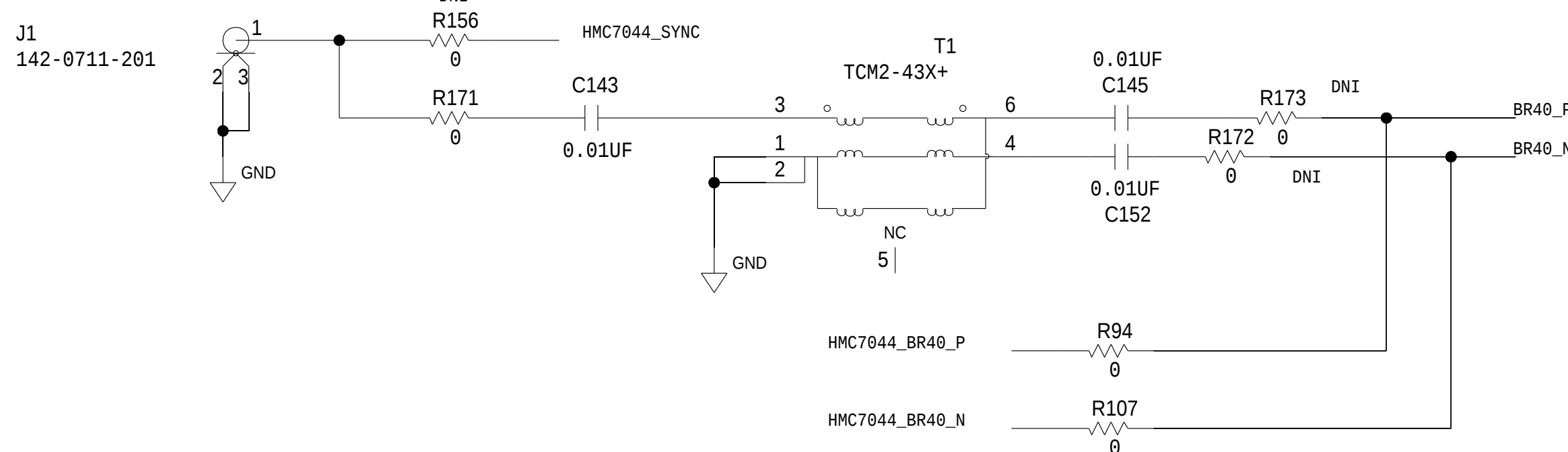


FOR WIDER BANDWIDTH
USE TCM1-83X1



HMC7044 REFIN

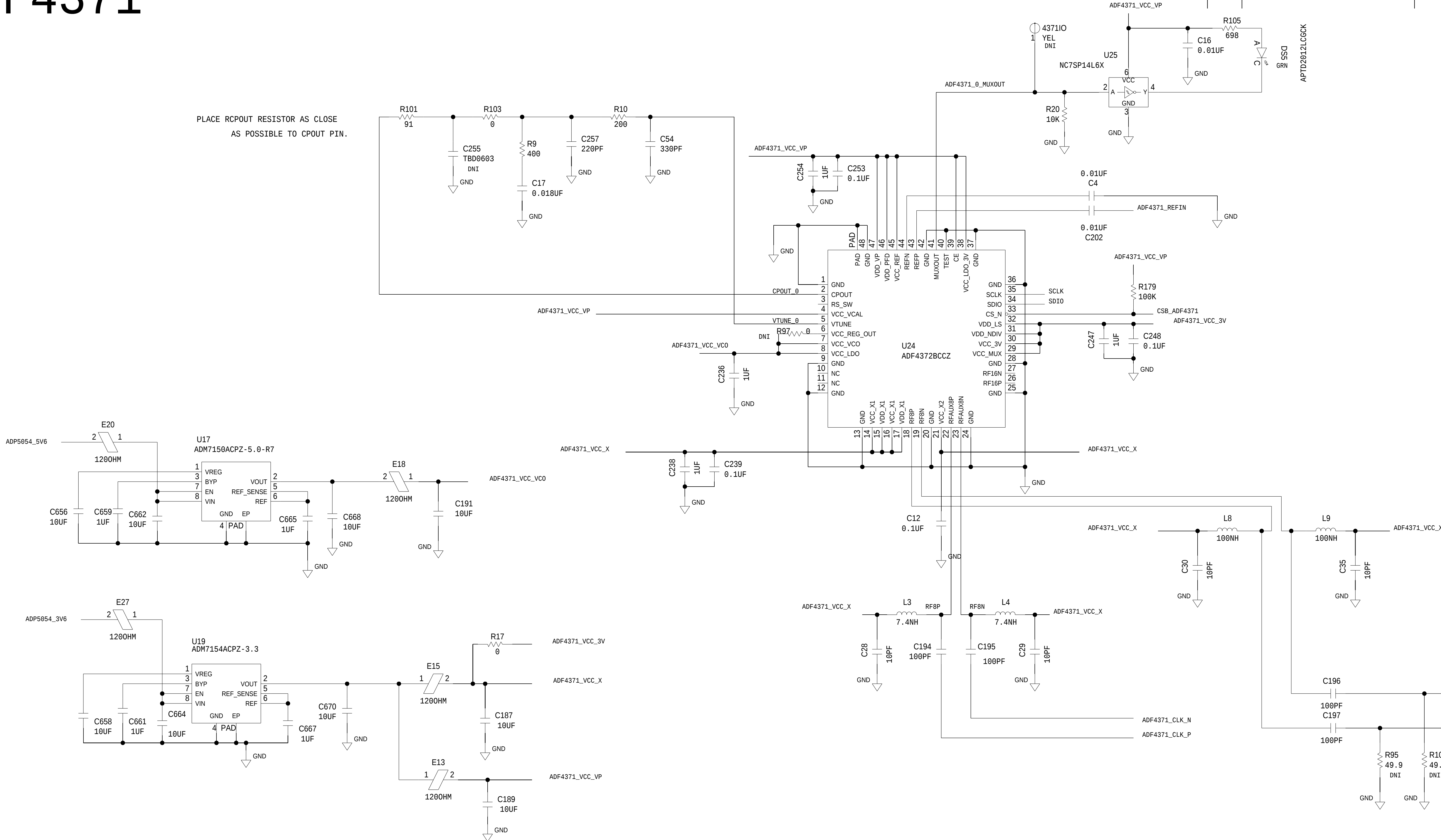
HMC7044 SYNC / EXT BR40



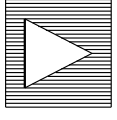
SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : AD9166 : AD9166			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_049306	REV G	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 11 OF 12

ADF4371

PLACE RCPOUT RESISTOR AS CLOSE
AS POSSIBLE TO CPOUT PIN.



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

 ANALOG DEVICES	SCHEMATIC		
	HW TYPE : Customer Evaluation Product(s): AD9166 : AD9166		
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_049306	REV G
	PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1

SHEET 12	OF 12
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