

OUTPUT

INPUT

Data

GPIO #1

GPIO #2

GPIO #3

GPIO #4

GPIO #5

GPIO #6

GPIO Configuration

GPIO #1

INS1

GPIO #2

Margin Up

GPIO #3

Margin Down

GPIO #4

INS4

GPIO #5

Fault 1 OD

GPIO #6

Fault 2 OD

Pull Down

PG Threshold

95%

95%

95%

95%

RESET Pin Configuration

Assert Pin On:

Primary Undervoltage

MON:

1

2

3

4

5

6

7

8

9

10

11

12

Timeout:

25 us

Active High

Active Low

Push - pull

Open Drain

Register and EEPROM lock

Non-volatile EEPROM lock

Software Enable

FAULT1

FAULT2

FAULT Pin Dependencies

Primary Over Voltage

Primary Under Voltage

Early Warning

MON1

MON2

MON3

MON4

MON5

MON6

MON7

MON8

MON9

MON10

MON11

MON12

Active High

Active Low

DAC Configuration	Enabled	Range	Output Code	Margin UP	Margin DN
DAC Control #1		Off	0	0	0
DAC Control #2		0.8 V to 1.6 V	0	69	192
DAC Control #3		0.8 V to 1.6 V	0	68	194
DAC Control #4		Off	0	0	0
DAC Control #5		Off	0	0	0
DAC Control #6		Off	0	0	0
DAC Control #7		Off	0	0	0
DAC Control #8		Off	0	0	0
DAC Control #9		Off	0	0	0
DAC Control #10		Off	0	0	0
DAC Control #11		Off	0	0	0
DAC Control #12		Off	0	0	0

Watchdog Timer

Enable

Startup 25.6 s

Timeout 1600.0 ms

Startup Timer

Reset Output

Mode Dependent

Fault

Fault Control

Save failed lines and ADC values

Deglitch Cycle 2 Cycle

Fault Up Timer 25 ms

Fault Down Timer 100 ms

Fault Behavior

Latch-Off

Auto-Retry: 1600.0 ms

Slew Rate 800 V/s