MAXIM

ERRATA SHEET
DS26514

_____ *Revision B1 Errata*

The errata listed below describe situations where DS26514 revision B1 components perform differently than expected or differently than described in the data sheet. Maxim Integrated Products, Inc., intends to correct these errata when the opportunity to redesign the product presents itself.

*This errata sheet only applies to DS26514 revision B1 components. Revision B1 components are branded on the topside of the package with a six-digit code in the form yywwB1, where yy and ww are two-digit numbers representing the year and work week of manufacture, respectively. To obtain an errata sheet on another DS26514 die revision, visit our website at **www.maxim-ic.com/errata**.*

1) TTIP/TRING OUTPUTS NOT HIGH IMPEDANCE WITH TXENABLE LOW DURING POWER-UP

Description:
The TTIPn/TRINGn line drivers should be high impedance whenever TXENABLE is low. On rev B1 devices the TTIPn/TRINGn line drivers are briefly enabled during power-up, even if TXENABLE is low. This may cause the inactive DS26514 in a Y-cable application to attenuate the transmit waveform of the active DS26514 and create a bit error, as seen in the figures below.
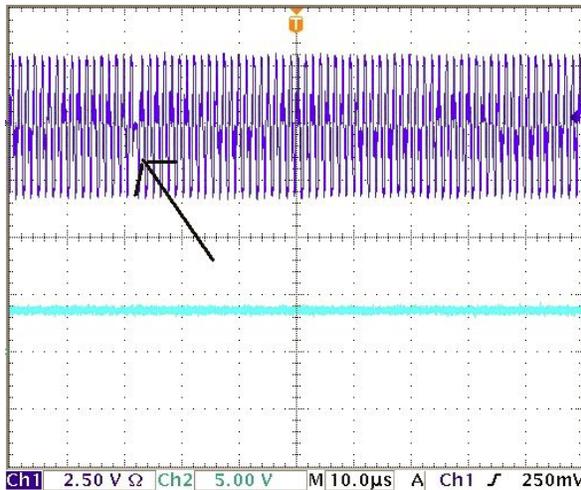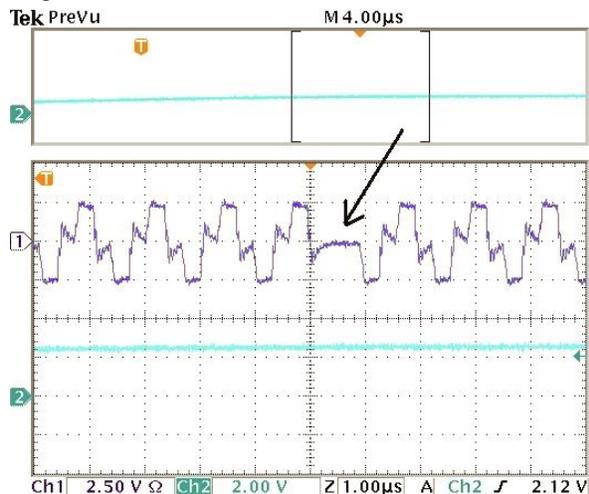
Figure 1

Figure 2

Workaround:
None.