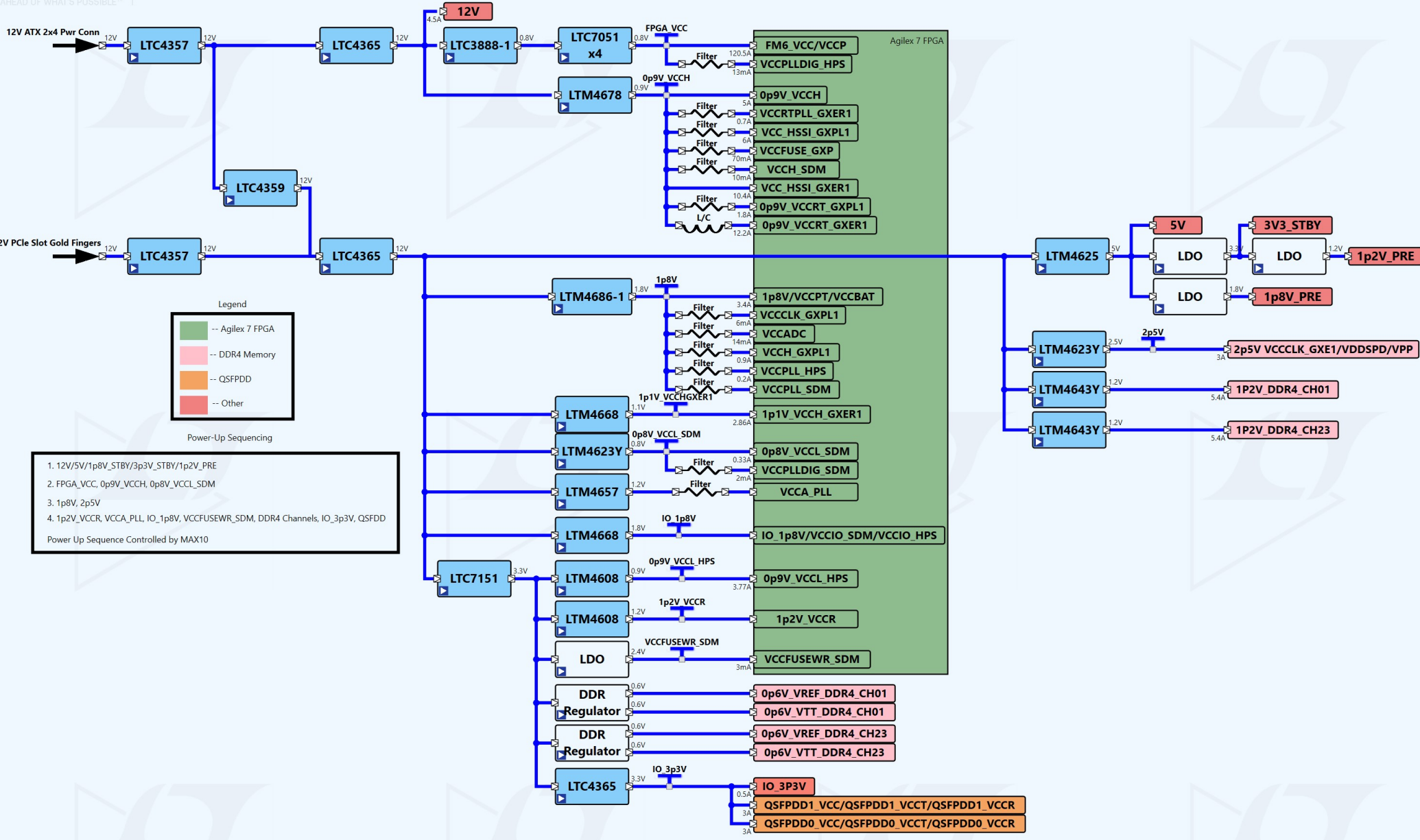




Agilex 7 F-Series PCIE Dev Kit (P-Tile and E-Tile)

FAE: Matt Howard



Legend

- Agilex 7 FPGA
- DDR4 Memory
- QSFDD
- Other

Power-Up Sequencing

- 12V/5V/1p8V_STBY/3p3V_STBY/1p2V_PRE
 - FPGA_VCC, 0p9V_VCCCH, 0p8V_VCCCL_SDM
 - 1p8V, 2p5V
 - 1p2V_VCCR, VCCA_PLL, IO_1p8V, VCCFUSEWR_SDM, DDR4 Channels, IO_3p3V, QSFDD
- Power Up Sequence Controlled by MAX10