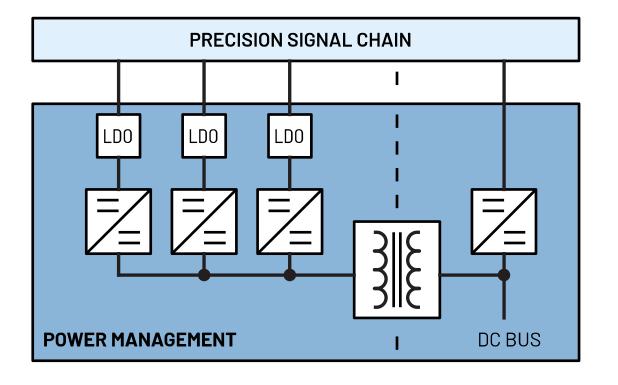


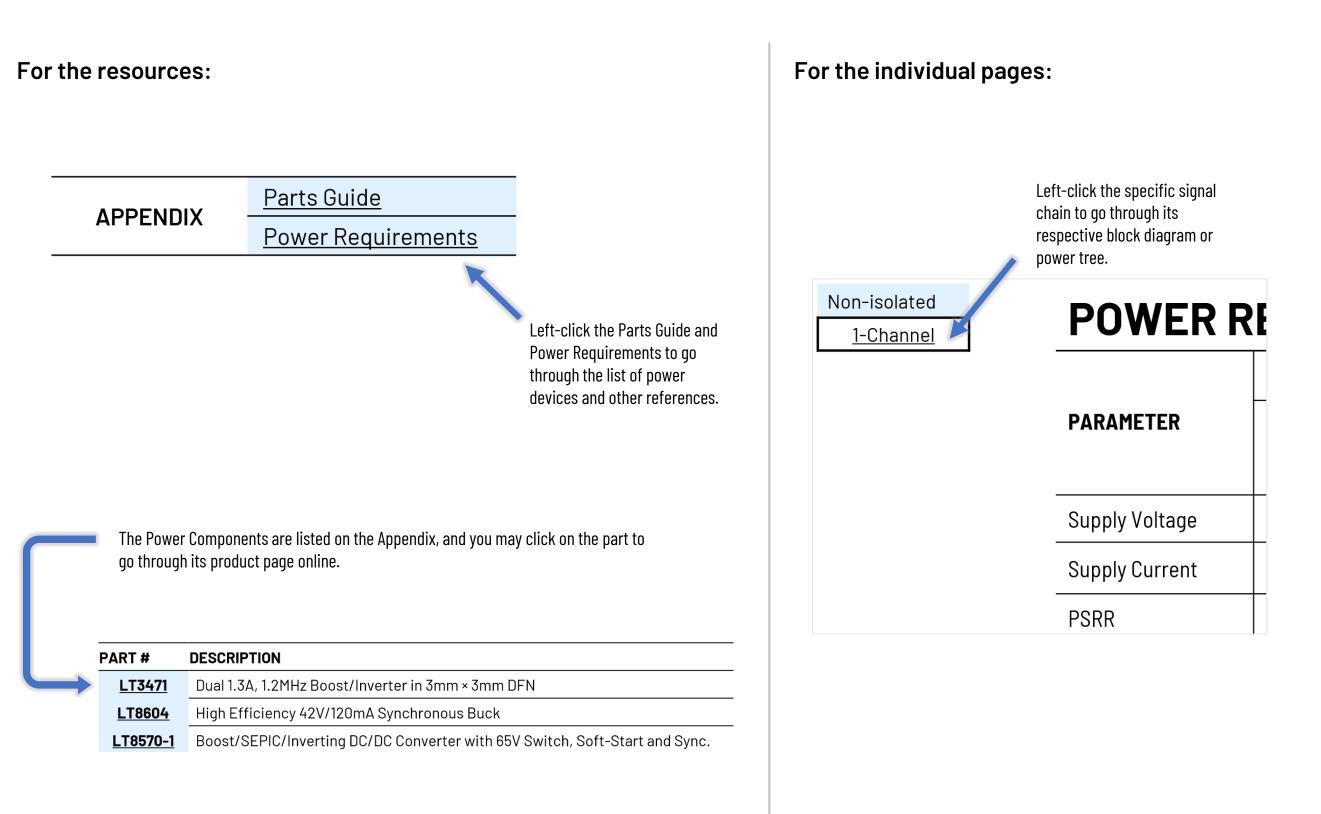
POWER SOLUTIONS FOR PRECISION TECHNOLOGY SIGNAL CHAINS

PRECISION MEDIUM BANDWIDTH Multiple Channel Data Acquisition Low Latency Simultaneous Sampling

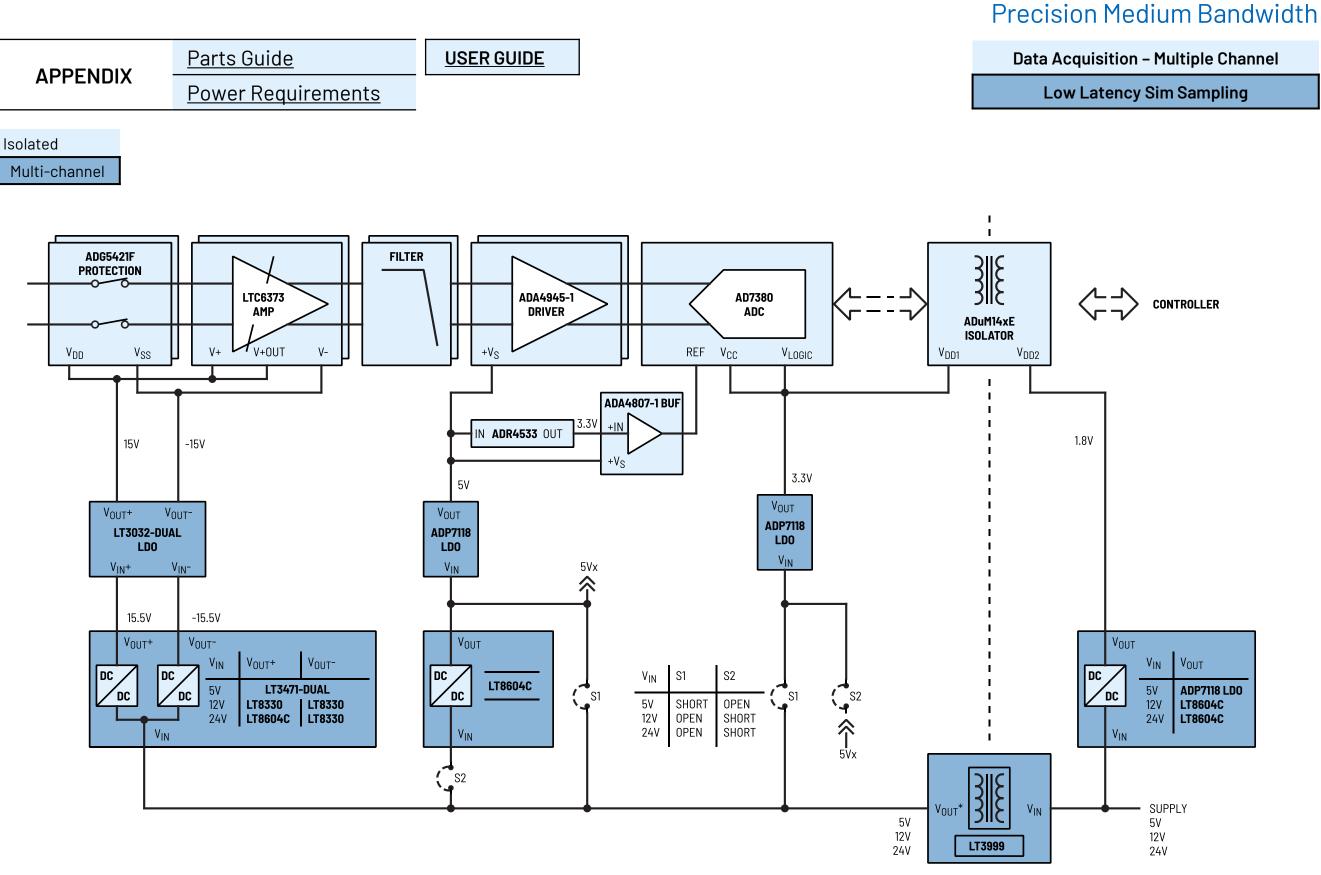
Rev. 0 | Aug. 2022



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*The actual output voltage of LT3999 isolated converter depends primarily on the turns ratio of the transformer used. See LT3999 datasheet for details.

Precision Medium Bandwidth

Data Acquisition – Multiple Channel

Low Latency Sim Sampling

lsolated <u>Multi-channel</u>	PART #	DESCRIPTION
	<u>LT8604</u>	High Efficiency 42V/120mA Synchronous Buck
	<u>LT3471</u>	Dual 1.3A, 1.2MHz Boost/Inverter in 3mm ×3mm DFN
	<u>LT8330</u>	Low I ₀ Boost/SEPIC/Inverting Converter with 1A, 60V Switch
	<u>LT3999</u>	Low Noise, 1A, 1MHz Push-Pull DC/DC Driver with Duty Cycle Control
	<u>ADP7118</u>	20V, 200mA, Low Noise, CMOS LDO Linear Regulator
	LT3032	Dual 150mA Positive/Negative Low Noise Low Dropout Linear Regulator

Precision Medium Bandwidth

Data Acquisition - Multiple Channel

Low Latency Sim Sampling

Isolated

Multi-channel

POWER REQUIREMENTS

	STAGES	Protection		Gain			Filter	ADC D	river	ADC			Reference	Ref. Buffer		Isolation	
PARAMETER	Part #	ADG5421F		LTC6373			-	ADA49	945-1	<u>AD7380</u>			<u>ADR4533</u>	ADA4807-1		ADuM14xE	
	Pin	V _{DD}	V _{SS}	V+	V-	V_{+OUT}		+V _S	-V _S	V _{CC}	V _{LOGIC}	-	IN	+V _S	-V _S	V _{DD1}	V _{DD2}
Supply Voltage	V	15	-15	15	-15	15	-	5	-	3.3	3.3	-	5	5	-	3.3	1.8
Supply Current	mA	0.205	-0.115	20	-20	-	-	4.2	-	26	3.7	-	1.2	3.75	-	17	10
PSRR	dB	90 (1MHz)		67 (1MHz; G=1)	57 (1MHz; G=1)	-	-	106 (1MHz)		75 (1MHz)			68 (1MHz)	73 (1MHz)		-	

Note 1: The supply currents indicated are the maximum quiescent current of the supply rails. For overall full load or short circuit current specifications, refer to the datasheets of the signal chain components.

Note 2: The supply voltages indicated are the values for typical applications.

Note 3: Consult the corresponding datasheets for details on: (1) power supply rejection ratio (PSRR) and (2) power dissipation.

Note 4: The actual supply current requirement shall be multiplied depending on the number of channels on the signal chain.