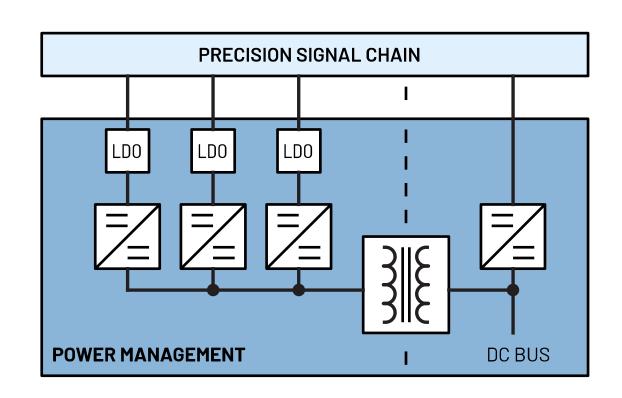


POWER SOLUTIONS FOR PRECISION TECHNOLOGY SIGNAL CHAINS

PRECISION MEDIUM BANDWIDTH Single Channel Data Acquisition High Density

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For the resources:

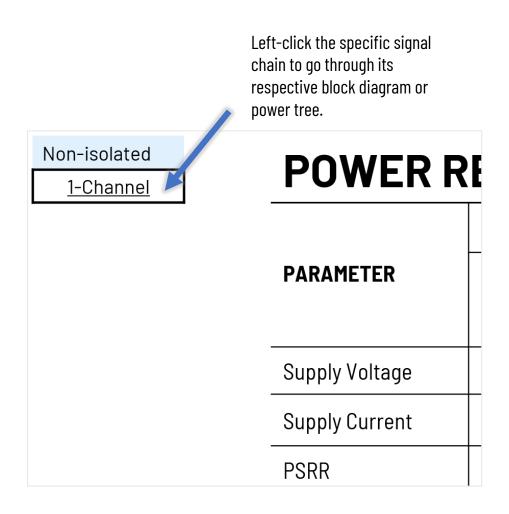
APPENDIX Power Requirements

Left-click the Parts Guide and Power Requirements to go through the list of power devices and other references.

The Power Components are listed on the Appendix, and you may click on the part to go through its product page online.

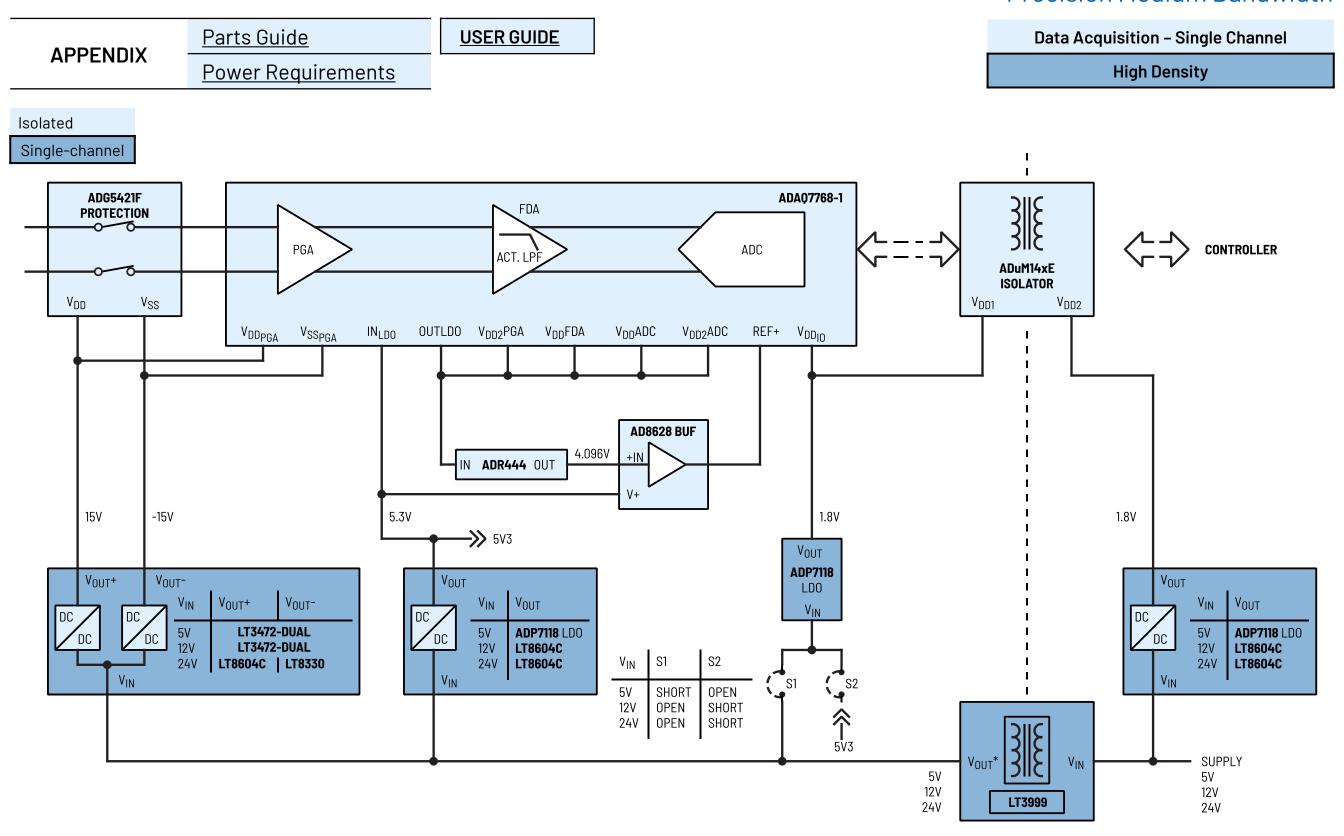
PART#		DESCRIPTION						
	<u>LT3471</u>	Dual 1.3A, 1.2MHz Boost/Inverter in 3mm × 3mm DFN						
	LT8604	High Efficiency 42V/120mA Synchronous Buck						
	LT8570-1	Boost/SEPIC/Inverting DC/DC Converter with 65V Switch, Soft-Start and Sync.						

For the individual pages:





Precision Medium Bandwidth



^{*}The actual output voltage of LT3999 isolated converter depends primarily on the turns ratio of the transformer used. See LT3999 datasheet for details.

Precision Medium Bandwidth

Data Acquisition – Single Channel

High Density

Isolated	
Single-channel	

PART#	DESCRIPTION						
LT8604	High Efficiency 42V/120mA Synchronous Buck						
<u>LT3472</u>	Boost and Inverting DC/DC Converter for CCD Bias						
<u>LT8330</u>	Low I _Q Boost/SEPIC/Inverting Converter with 1A, 60V Switch						
<u>LT3999</u>	Low Noise, 1A, 1MHz Push-Pull DC/DC Driver with Duty Cycle Control						
<u>ADP7118</u>	20V, 200mA, Low Noise, CMOS LDO Linear Regulator						

Precision Medium Bandwidth

Data Acquisition - Single Channel

High Density

Isolated

Single-channel

POWER REQUIREMENTS

	STAGES	Protect	ion	Gain ADC Driver ADC				Ref Buffer	Reference	Isolation	
PARAMETER	Part #	ADG5421F		ADA07768-1				AD8628	<u>ADR444</u>	ADuM141E	
	Pin	V _{DD}	V _{SS}	V _{DD_PGA}	V _{SS_PGA}	IN _{LDO}	V _{DD_IO}	V+	V _{IN}	V _{DD1}	V _{DD2}
Supply Voltage	V	15	-15	15	-15	5.3	1.8	5.3	5	1.8	1.8
Supply Current	mA	0.205	-0.205	8.5	-14.24	19.43	9.1	1.2	3.75	17	10
PSRR	dB	90 (1MHz)		121 (1MHz; G=0.65)	121 (1MHz; G=0.65)	121 (1MHz; G=0.65)	125 (1MHz)	15 (1MHz)	20 (1MHz)	-	-

Note 1: The supply currents indicated are the maximum quiescent current of the supply rails. For overall full load or short circuit current specifications, refer to the datasheets of the signal chain components.

Note 2: The supply voltages indicated are the values for typical applications.

Note 3: Consult the corresponding datasheets for details on: (1) power supply rejection ratio (PSRR) and (2) power dissipation.

Note 4: The actual supply current requirement shall be multiplied depending on the number of channels on the signal chain.