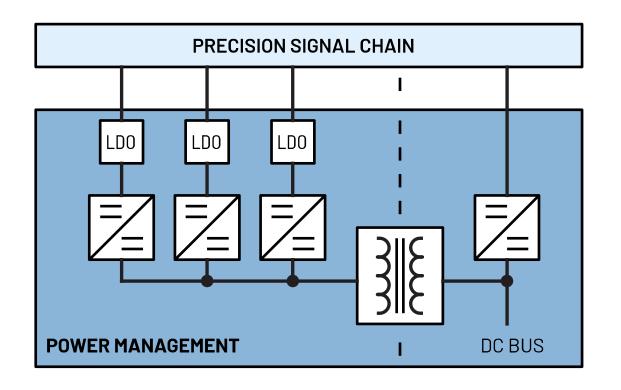


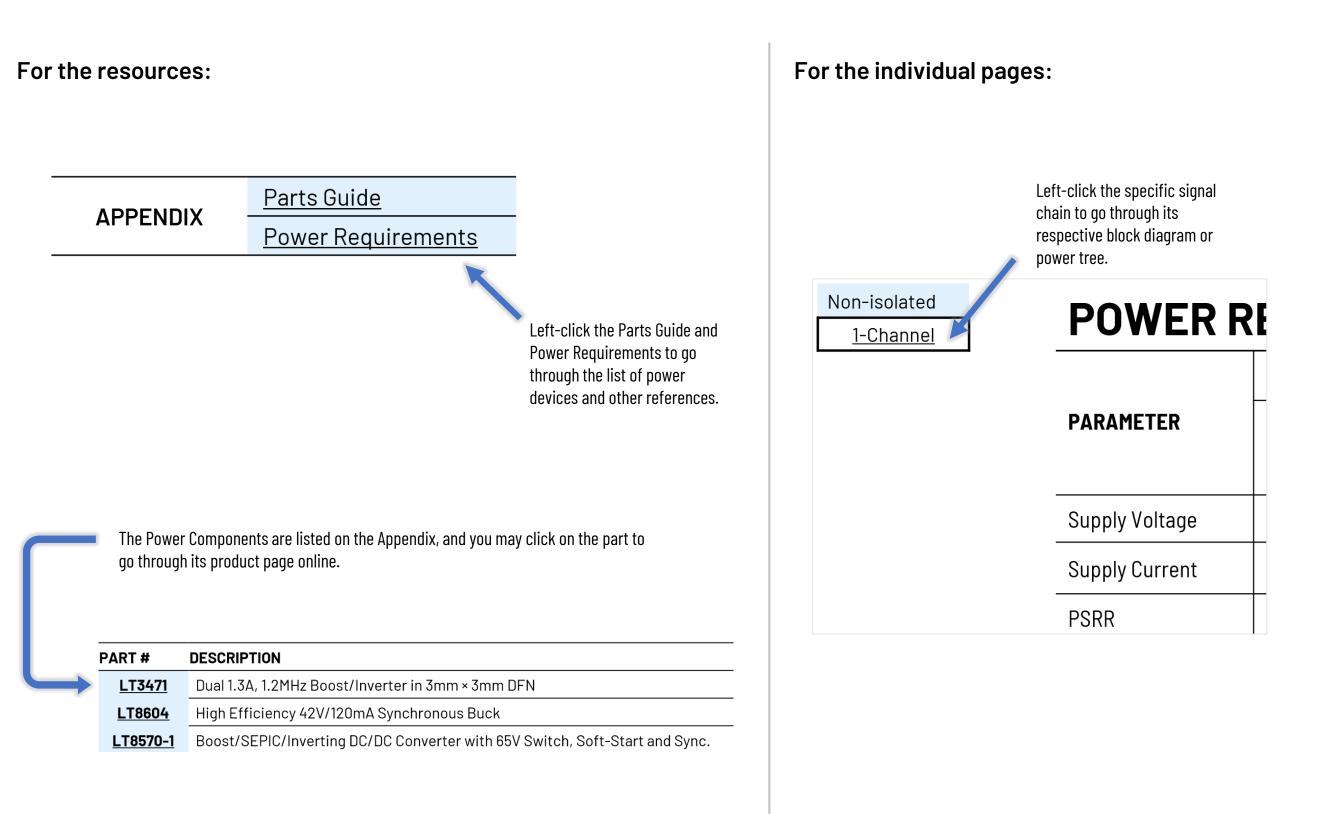
POWER SOLUTIONS FOR PRECISION TECHNOLOGY SIGNAL CHAINS

PRECISION NARROW BANDWIDTH Adaptable Voltage Drive Noise and Stability Optimized

Rev. 0 | Jan. 2022

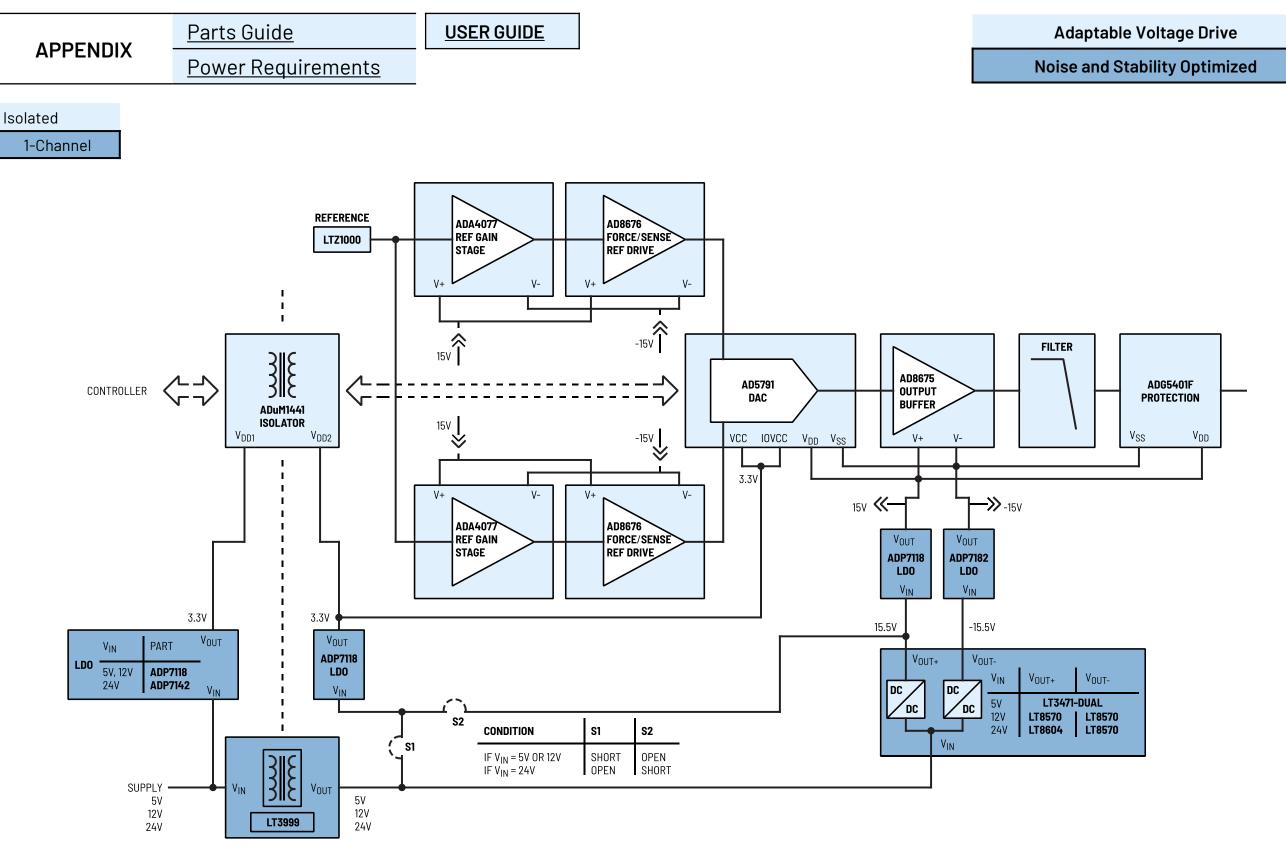


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Precision Narrow Bandwidth



Precision Narrow Bandwidth

Adaptable Voltage Drive

Noise and Stability Optimized

lsolated <u>1-Channel</u>	PART #	DESCRIPTION
<u>r onumer</u>	<u>LT3471</u>	Dual 1.3A, 1.2MHz Boost/Inverter in 3mm × 3mm DFN
	<u>LT8604</u>	High Efficiency 42V/120mA Synchronous Buck
	<u>LT8570</u>	Boost/SEPIC/Inverting DC/DC Converter with 65V Switch, Soft-Start and Sync.
	<u>LT3999</u>	Low Noise, 1A, 1MHz Push-Pull DC/DC Driver with Duty Cycle Control
	<u>ADP7118</u>	20V, 200mA, Low Noise, CMOS LDO Linear Regulator
	<u>ADP7142</u>	40V, 200 mA, Low Noise, CMOS LDO Linear Regulator
	<u>ADP7182</u>	–28V, –200mA, Low Noise, Linear Regulator

Isolated

1-Channel

Precision Narrow Bandwidth

Adaptable Voltage Drive

Noise and Stability Optimized

	STAGES	Buffer		Buffer		Isolation		DAC				Amp		Protection	
PARAMETER	Part #	ADA4077		<u>AD8676</u>		<u>ADuM1441</u>		AD5791				<u>AD8675</u>		ADG5401F	
	Pin	V+	V-	V+	V-	V _{DD1}	V_{DD2}	V _{CC}	IOV _{CC}	V_{DD}	V_{SS}	V+	V-	V _{DD}	V_{SS}
Supply Voltage	V	15	-15	15	-15	3.3	3.3	3.3	3.3	15	-15	15	-15	15	-15
Supply Current	mA	0.65	-0.65	4.2	-4.2	0.9	-0.75	0.9	0.14	5.2	-4.9	3.8	-3.8	0.22	0.09
PSRR	dB	12 (1MHz)	24 (1MHz)	22 (1MHz)	68 (1MHz)	-	_	_				48 (1MHz)	52 (1MHz)	82 (1MHz)	

POWER REQUIREMENTS

Note 1: The supply currents indicated are the maximum quiescent current of the supply rails. For overall full load or short circuit current specifications, refer to the datasheets of the signal chain components.

Note 2: The supply voltages indicated are the values for typical applications.

Note 3: Consult the corresponding datasheets for details on power dissipation if needed.

Note 4: The actual supply current requirement shall be multiplied depending on the number of channels on the signal chain.