

IMPORTANT NOTE

DO NOT CONNECT PIN

R12 IS CRITICAL COMPONENT AND MUST BE 12.4K OHM, 1%, 100PPM
PIN 1 SEPARATELY FILTERED FROM OTHER VDD18 PINS.

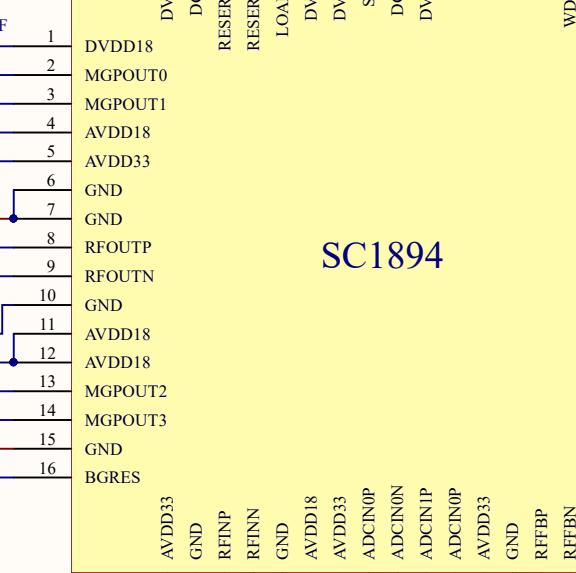
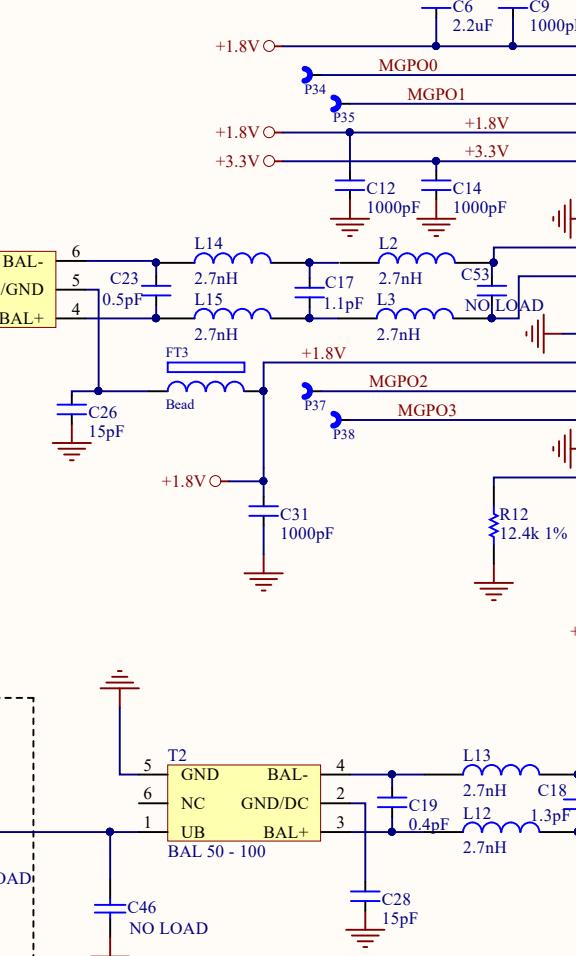
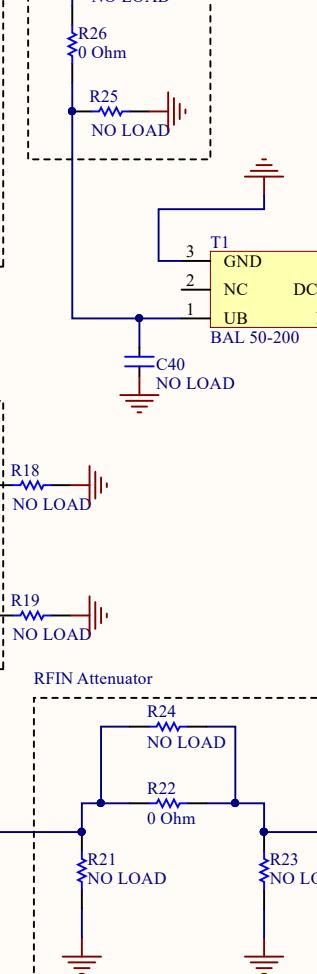
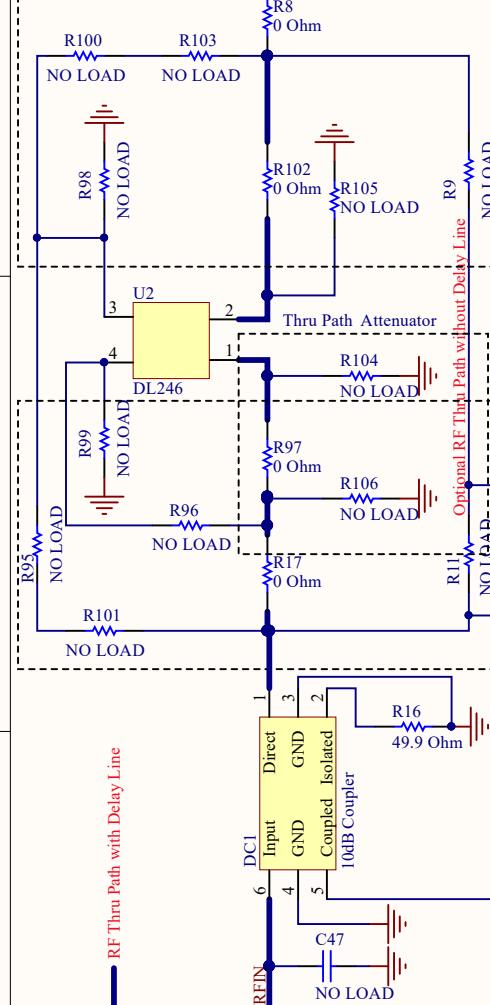
NO LOAD (OR NL): DO NOT INSTALL COMPONENT (PCB CONTAINS LAYOUT FOR COMPONENT)

NO LOAD COMPONENTS ARE RECOMMENDED FOR PROTOTYPE DEVELOPMENT AND MAY NOT BE NEEDED FOR FINAL DESIGN.

DEFAULT CONFIGURATION FOR DELAY COMPONENT (DL246) IS 4NS

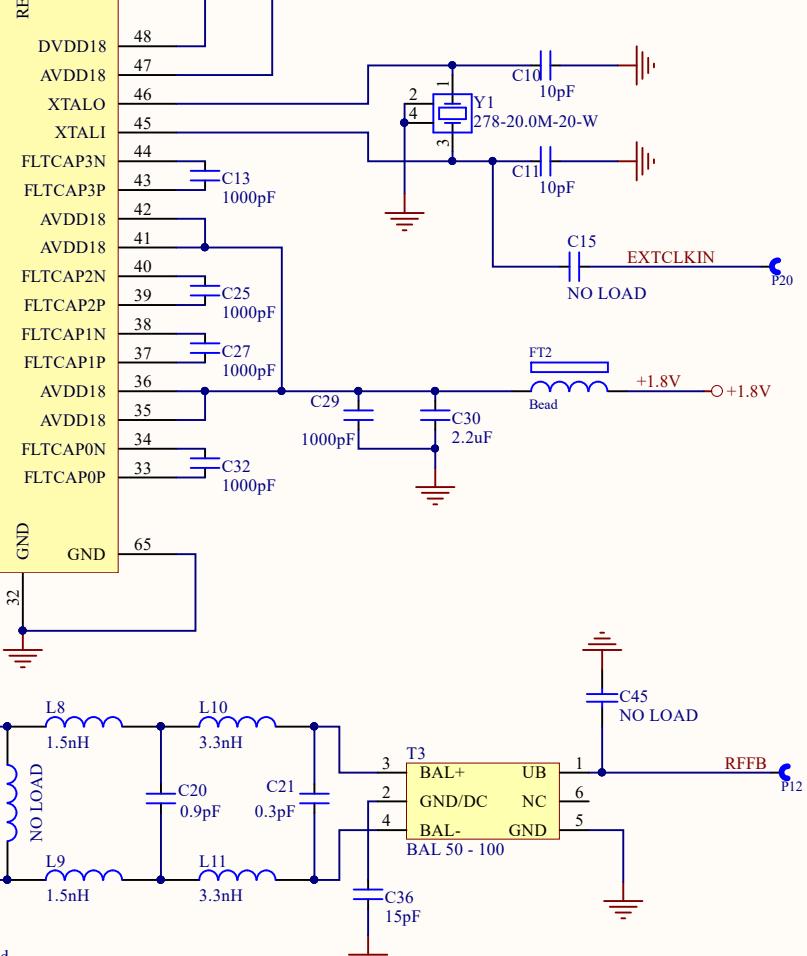


| | | | | |
|---------|----|--|----|-------|
| WDTEN | 1 | | 2 | N/C |
| LOADENB | 3 | | 4 | STATO |
| DGPIO1 | 5 | | 6 | RESET |
| DGPIO0 | 7 | | 8 | SSN |
| GND | 9 | | 10 | SDI |
| GND | 11 | | 12 | SDO |
| GND | 13 | | 14 | SCLK |



SC1894

P6 P9 ADC Inputs: Leave unconnected if not used



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