

**IMPORTANT NOTES:**

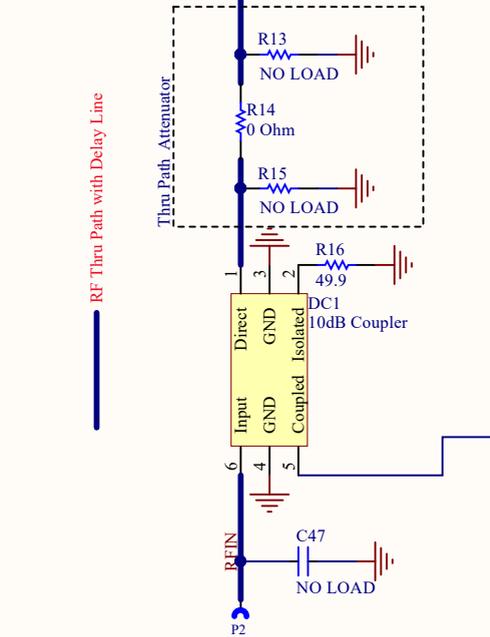
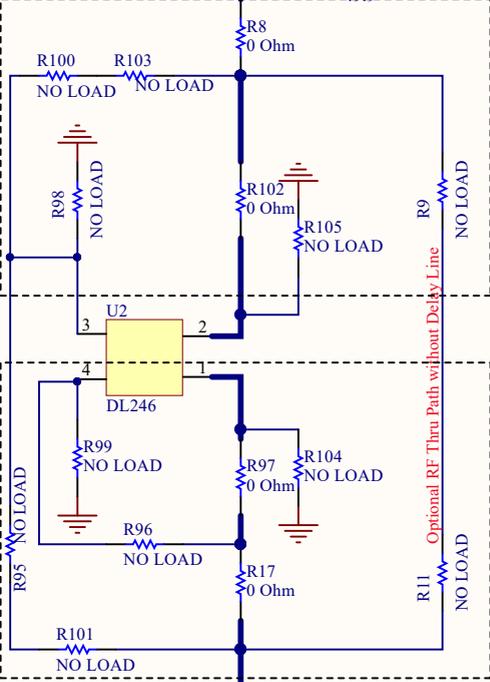
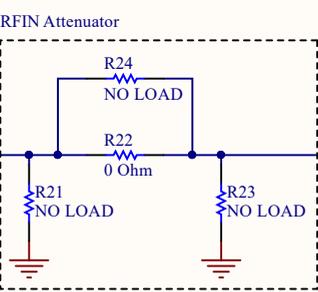
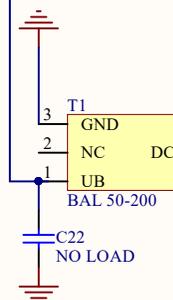
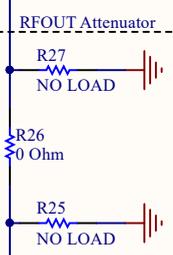
DO NOT CONNECT PINS 61, 62.  
 R12 IS CRITICAL COMPONENT AND MUST BE 12.4K OHM, 1%, 100PPM/c.  
 PIN 1 SEPARATELY FILTERED FROM OTHER VDD18 PINS.

NO LOAD (OR NL): DO NOT INSTALL COMPONENT (PCB CONTAINS LAYOUT FOR COMPONENT)  
 NO LOAD COMPONENTS ARE FOR SCINTERA VALIDATION PURPOSES, NOT NEEDED FOR FINAL DESIGN.

DEFAULT CONFIGURATION FOR DELAY COMPONENT (DL246) IS 4NS.

**INTERFACE CONNECTOR RECOMMENDED FOR DEVELOPMENT**

WDTEN	1	2	N/C
LOADENB	3	4	STATO
DGPIO1	5	6	RESETN
DGPIO0	7	8	SSN
GND	9	10	SDI
GND	11	12	SDO
GND	13	14	SCLK



"This document contains confidential information proprietary to Maxim Integrated Products, Inc., and is provided under a non-disclosure agreement. Unauthorized copying or distribution is prohibited. Maxim Integrated and Maxim Integrated logo are trademarks of Maxim Integrated Products, Inc. All other trademarks are the property of their respective owners. No circuit patent licenses are implied. Maxim Integrated Products, Inc. reserves the right to change its product specifications at any time. © 2014, Maxim Integrated Products, Inc. All Rights Reserved."

Title <b>SC1894-DB1900A (1800 - 2200 MHz)</b>			Maxim Integrated 160 Rio Robles San Jose, CA. 95134 U.S.A. 408-601-1000	
Size: B	Number:*	Revision:2.0		
Date: 6/5/2020	Time: *	Sheet 1 of 1		
File: SC1894-DB1900A.SchDoc				

PCB1  
RDB3.6\_R2