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100 percent verifiable, extensive on-chip functionality, and other features not be reproduced until they are approved for release in future silicon quantities and/or product generations.

HARDWARE NAME:MAX22707_EVKIT_B

HARDWARE NUMBER:

ENGINEER:DESIGNER:

DATE: 09/1/2021008++/GERBER: ASSEMBLY_TOP

The diagram illustrates the PCB layout for the MAX22707_EVKIT_B, showing two main configuration modes: LPF (Low Pass Filter) and BPF (Band Pass Filter). The layout includes various components such as resistors (R1-R34), capacitors (C1-C31), and integrated circuits (MAX22707, MAX22707). It also features a warning for high voltage AC and a QR code for more information.

LPF CONFIGURATION MODE

BPF CONFIGURATION MODE

JUMPER SETTINGS		
JUM_REF	SHUNT_REF	SHUNT_POSITION
J1	SU1	1:2
J10	SU8	1:2
J11	SU17	1:2
J14	SU13	1:2
J17	SU16	1
J18	SU14	1:2
J19	SU15	2:3
J2	SU2	1:2
J21	SU10	1:2
J23	SU12	1
J24	SU11	2:3
J29	SU9	1:2
J3	SU3	1:2
J4	SU4	1:2
J5	SU5	1:2
J7	SU6	1:2
J8	SU7	1:2