ART FILM - assyl REVISIONS REVDESCRIPTION DATE APPROVED Α INITIAL RELEASE 19AUG20 P. DELCORRO ASSEMBLY NOTES: BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION). REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED. 2. 3. REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD. ROHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND ROHS PCB ASSEMBLY STANDARDS (EU ROHS DIRECTIVE 2002/95/EC). BOARDS TO BE SINGULATED PRIOR SHIPPING TO CUSTOMER. 5. PRIMARY SIDE WWM DIVISION UNLESS OTHERWISE SPECIFIED DATE DIMENSIONS ARE IN INCHES TEMPLATE ENGINEER TOLERANCES 804 WOBURN STREET DECIMALS FRACTIONS ANGLES

.XX +-.010 +-1/32 +- 2

.XXX +-.005

.XXXX +-.0050

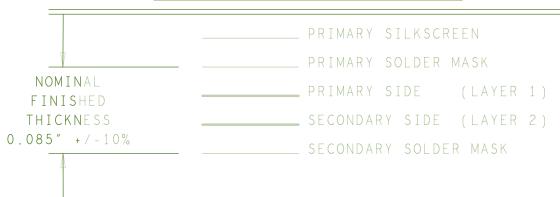
HARDWARE SERVICES

M. VALE

HARDWARE SYSTEMS WILMINGTON, MA 01887 19AUG20 TITLE ASSEMBLY TEST ENGINEER MATERIAL P2 1 COMPONENT ENGINEER M. YAN 19AUG20 EVAL-ADXL317Z TEST PROCESS HARDWARE RELEASE
K. JABATAN 19AUG20 M. VICEDO FINISH SIZE | FSCM NO DRAWING NUMBER REV 19AUG20 PTD ENGINEER
P. DELCORRO 19AUG20 24355 01 - 065660 SHEET 1 OF 1 DO NOT SCALE DWG SCALE 1 / 1 4

ART FILM - assyl

SPECIFICATIONS: ROHS COMPLIANCE NOTE: HOMOGENOUS MATERIALS IN THIS BOARD SHALL BE COMPLIANT THE EU ROHS DIRECTIVE 2002/95/EC MATERIALS: IPC-4101 OR IPC-4103, MINIMUM Tg>170degC, Td>300degC, U.L. RATING OF 94 V-0 MATERIAL FAMILY ISOLA 370HR EXTERNAL LAYERS .5 OZ. COPPER, OVERPLATE TO 1.5 OZ. CLADDING # NOTE: IF THE LAYER STACKUP CONFLICTS WITH THE ABOVE CLADDING SPECIFICATIONS THEN THE LAYER STACKUP SHALL TAKE PRECEDENCE. SOLDER MASK # OVER BARE COPPER OR GOLD AND SHALL MEET IPC-SM-840 (LATEST REV.) CLASS 3. COLOR GREEN. SILK SCREEN # SHALL BE PERMANENT NON-CONDUCTIVE EPOXY INK, COLOR: WHITE SYNTHETIC INKJET PRINTING ALLOWED FOR DENSE BOARDS, SURFACE FINISH; ENIG (ELECTROLESS NICKEL/IMMERSION GOLD) INTENTIONAL SHORTS: IF SUPPLIED DATA INCLUDES A FILE "READ ME.2", THEN INTENTIONAL NET SHORTS EXIST. CUSTOMER REVIEW AND APPROVAL IS REQUIRED IF SUPPLIED DATA REPORTS ANY CONDITION THAT TEST REQUIREMENTS # 100% NETLIST ELECTRICAL VERIFICATION USING CUSTOMER SUPPLIED IPC-D-356 NETLIST FOR OPENS AND SHORTS WHEN "GERBER DATA" IS PROVIDED. THIS VERIFICATION ALSO REQUIRED FOR "ODB++" DATA PER EMBEDDED NETLIST. 2 LAYER STACKUP PRIMARY SILKSCREEN



R.213 —

1.200

-.200

200

4

#

PLATED: +/-3MILS NON PLATED: +/-2MILS

DRILL CHART: TOP to BOTTOM									
ALL UNITS ARE IN MILS									
FIGURE	SIZE	PLATED	QTY	TOLERANCE/NOTES					
+	10.0	PLATED	109	DIA MAX					
Δ	15.0	PLATED	8						
♦	30.0	PLATED	10						
	40.0	PLATED	1						
А	140.0	NON-PLATED	4						

REV 19AUG20 P. DELCORRO

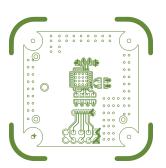
- REFER TO IPC-6010 SERIES (LATEST REV.), CLASS 2 FOR
- ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00115, (LATEST REVISION.)
- MODIFICATIONS TO THE ARTWORK ARE NOT ALLOWED WITHOUT
- A DIAMETER OF 0.005 INCHES FROM THEIR TRUE POSITION.
- PLATED HOLE WALL THICKNESS SHALL NOT BE LESS THAN 0.001 INCH MINIMUM AVERAGE, WITH NO READING LESS THAN .0008 BY
- HOLE DIAMETERS APPLY AFTER PLATING.
- FINISHED CONDUCTOR WIDTHS SHALL NOT BE REDUCED FROM THE NOMINAL INDICATED ON THE MASTER PATTERN, BY MORE THAN THE CONDUCTOR THICKNESS.
- MINIMUM DESIGN LINE WIDTH IS .008 INCH.
- NON-FUNCTIONAL PAD REMOVAL FROM INNER SIGNAL LAYERS MAY BE PERFORMED AFTER CUSTOMER APPROVAL.
- ANNULAR RING REQUIREMENT, MFGR. MAY REQUEST APPROVAL TO TEAR DROP PADS TO MAINTAIN ANNULAR RING. (AT PAD TO TRACE INTERSECTION ONLY AND ELECTRICAL INTEGRITY MUST BE MAINTAINED.)
- THIEVING MAY BE ADDED TO COMPENSATE FOR LOW COPPER DENSITY AREAS ON THIS DESIGN ONLY AFTER REVIEW AND APPROVAL FROM THE CUSTOMER:
 - A. THIEVING TO CARD EDGE, FIDUCIALS, NON-PLATED THROUGH HOLES,
 - MASK OR INTERNAL COPPER PLANES.
- MFGR. TO LEGIBLY ETCH OR STAMP/SCREEN WITH PERMANENT NON-CONDUCTIVE INK ON SECONDARY SIDE IN A CLEAR AREA UNLESS OTHERWISE INDICATED;
 - A. U.L. CODE-FLAMMABILITY RATING D. MFGR LOGO
 - B. DATE CODE (STAMP).

- REPAIRS PER IPC-7711/21 (LATEST REV.) ARE ALLOWED.

FINISH	DESIGNER M. VICEDO PTD ENGINEER	19AUG20	SIZE	FSCM NO	DRAWING NUMBER		REV		
	HARDWARE RELEASE K. JABATAN	19AUG20	EVAL-ADXL317Z						
	COMPONENT ENGINEER M. YAN TEST PROCESS	19AUG20							
MATERIAL	TEST ENGINEER		FABRICATION						
.XXX +005 .XXXX +0050	HARDWARE SYSTEMS		TITLE						
TOLERANCES DECIMALS FRACTIONS ANGLES .XX +010 +-1/32 +- 2	HARDWARE SERVICES M. VALE	19AUG20			SES 804 WILMI	WOBURN STREET NGTON, MA 01887			
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	APPROVAL TEMPLATE ENGINEER	DATE		TANAL		WWM DIVISION			

ART FILM - 11 primary

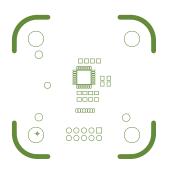
L1 PRIMARY 08-065660-01 REV A



ART FILM - 11_primary

ART FILM - mask_prm

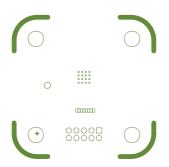
SOLDERMASK PRIMARY 08-065660-04 RFV A



ART FILM - mask prm

ART FILM - mask sec

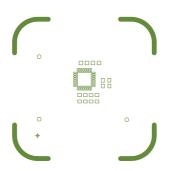
SOLDERMASK SECONDARY 08-065660-05 RFV A



ART FILM - mask sec

ART FILM - paste_prm

PASTEMASK PRIMARY 08-065660-06 RFV A



ART FILM - paste_prm

ART FILM - silk prm

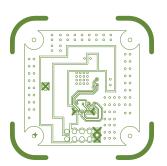
SILKSCREEN PRIMARY 08-065660-03 RFV A



ART FILM - silk_prm

ART FILM - 12 secondary

L2 SECONDARY 08-065660-02 RFV A



ART FILM - 12_secondary