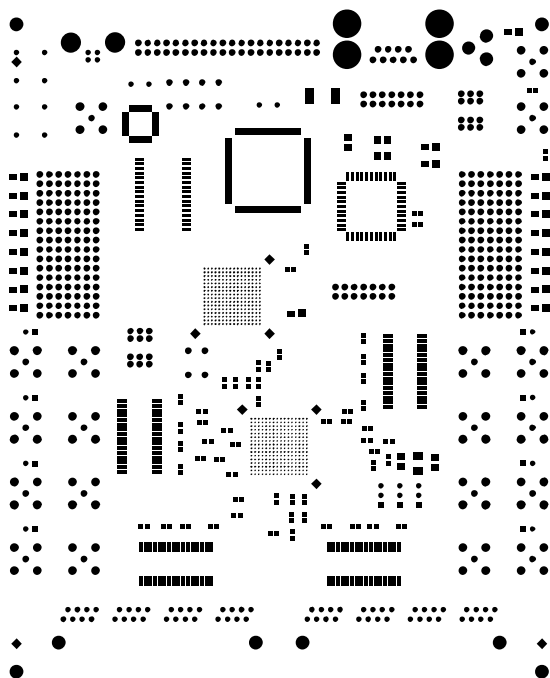


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DALLAS SEMICONDUCTOR / MAXIM
SEP 10, 2007
ROBERT SPENCER
(972) 371-6757

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11. SILKT.ART (TOP SILK SCREEN)
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SEE README.TXT FILES FOR DETAILS



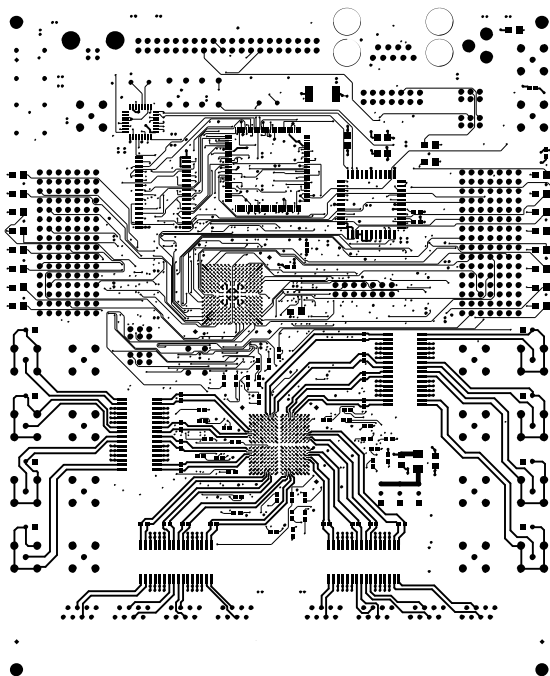
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13. SDRMT.ART (SOLDER MASK TOP)

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SEE README.TXT FILES FOR DETAILS

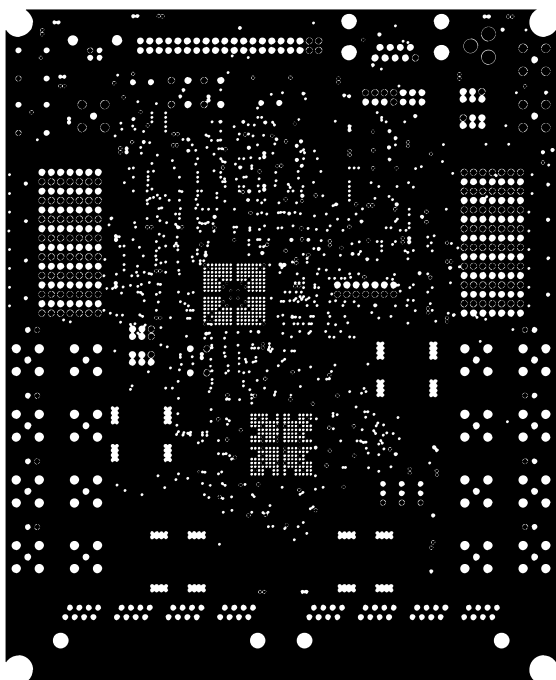


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1. TOP.ART

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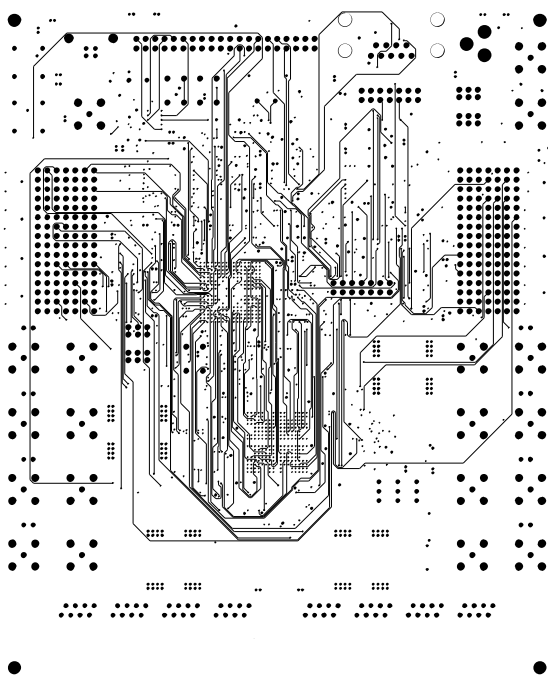
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2. L2GND.ART (GND PLANE)
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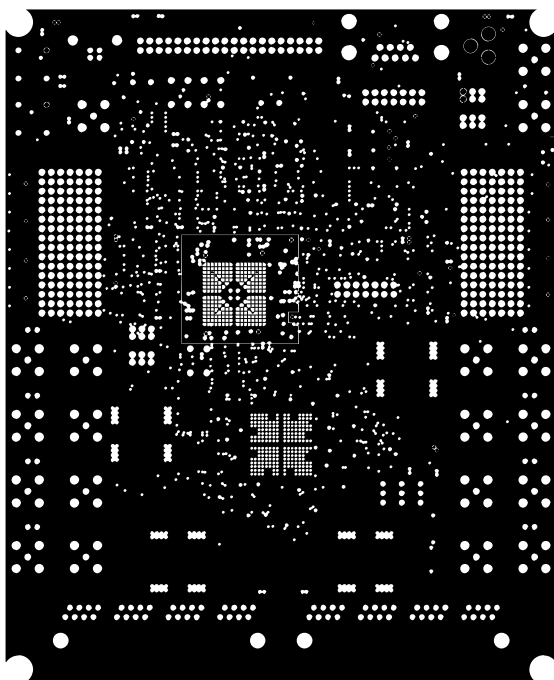
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L3.ART

SEE README.TXT FILES FOR DETAILS



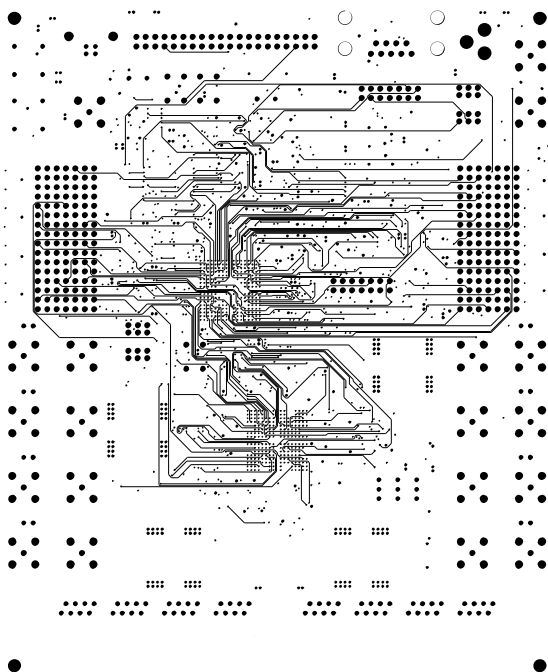


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L4_V5V.ART (5V BOARD PLANE)

SEE README.TXT FILES FOR DETAILS



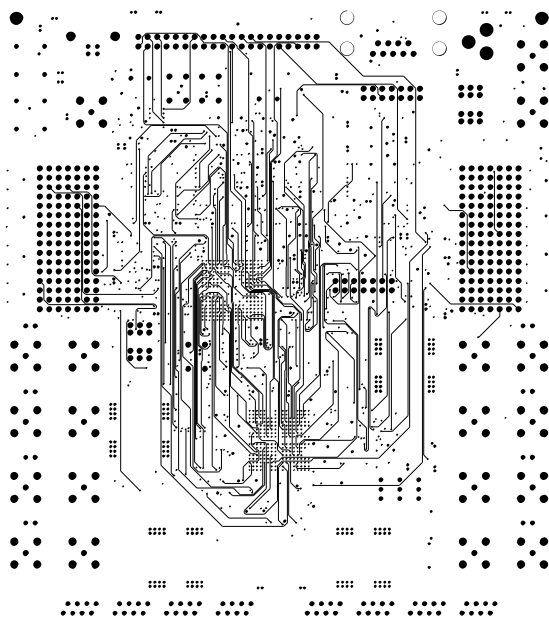
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L5.ART

SEE README.TXT FILES FOR DETAILS



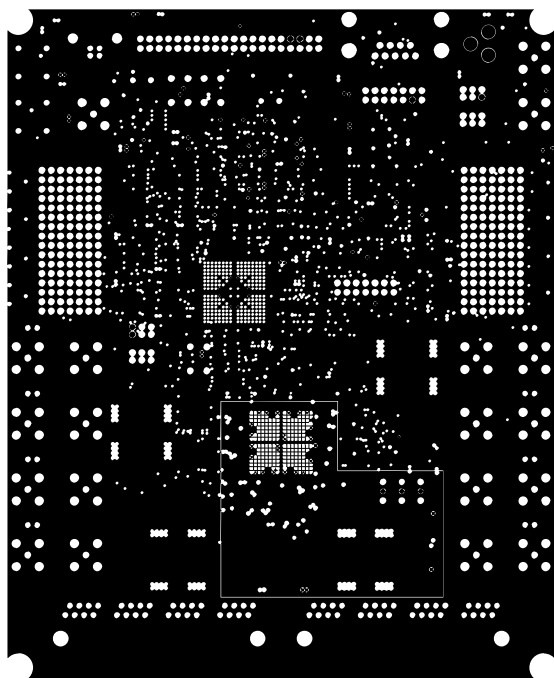


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L6.ART

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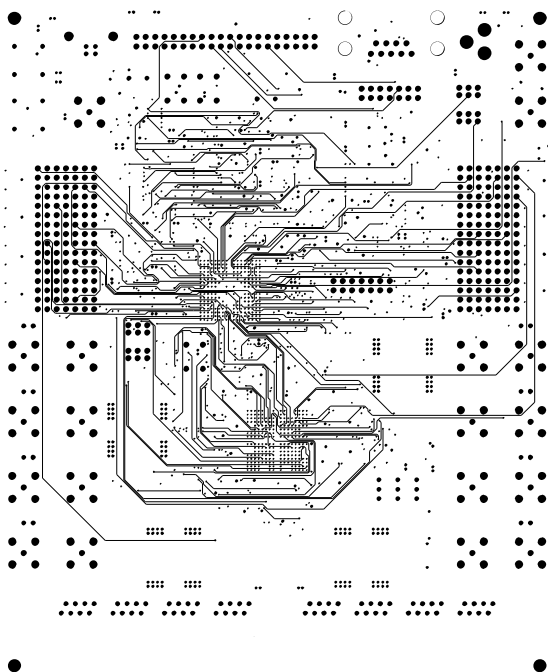


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L7_VCCVDD.ART (3.3 VCC / 3.3 VDD PLANE;

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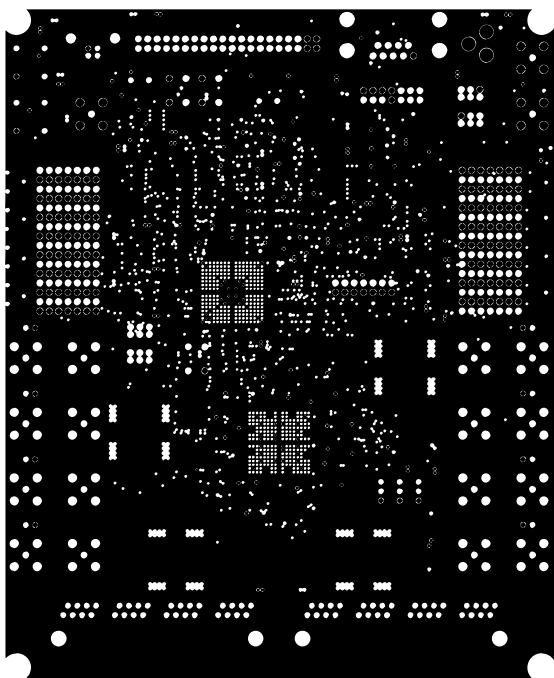


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L8.ART

SEE README.TXT FILES FOR DETAILS

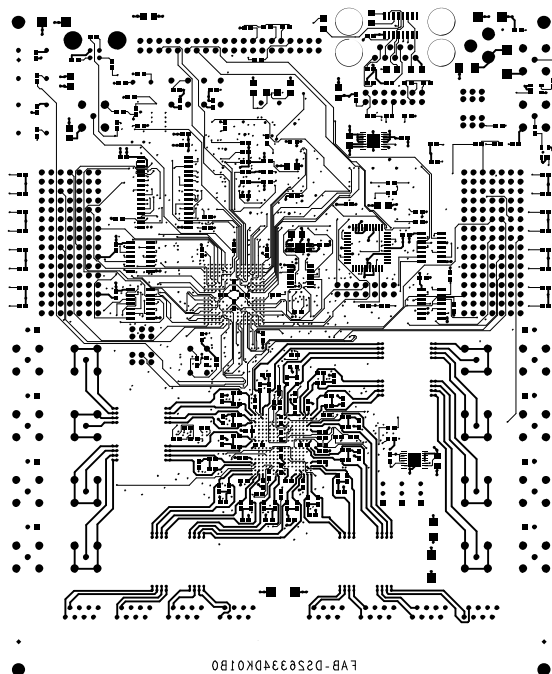


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L9GND.ART (GND PLANE)

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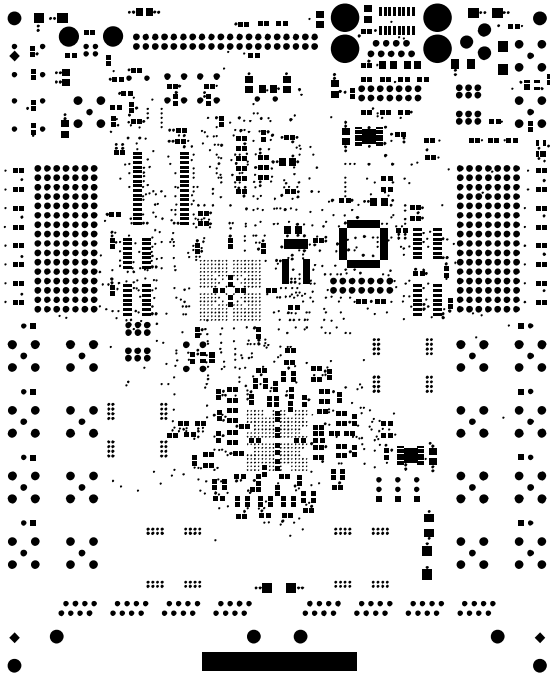
LFB-D2S0334DK01B0

DS26334DK01B0 FAB ART WORK
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SEE README.TXT FILES FOR DETAILS



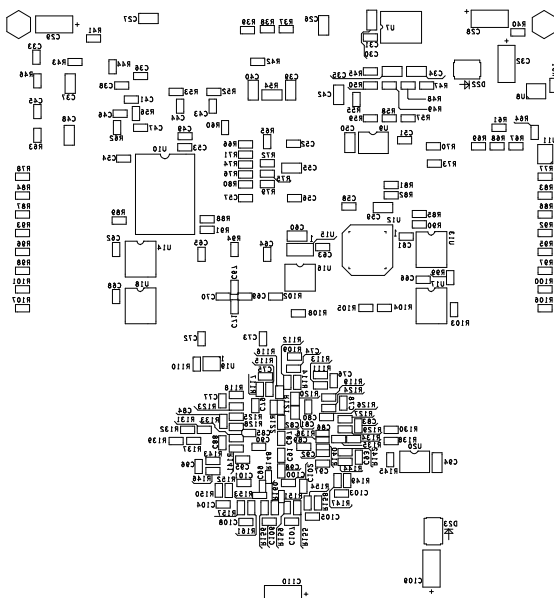


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SDRMB.ART (SOLDER MASK BOTTOM)

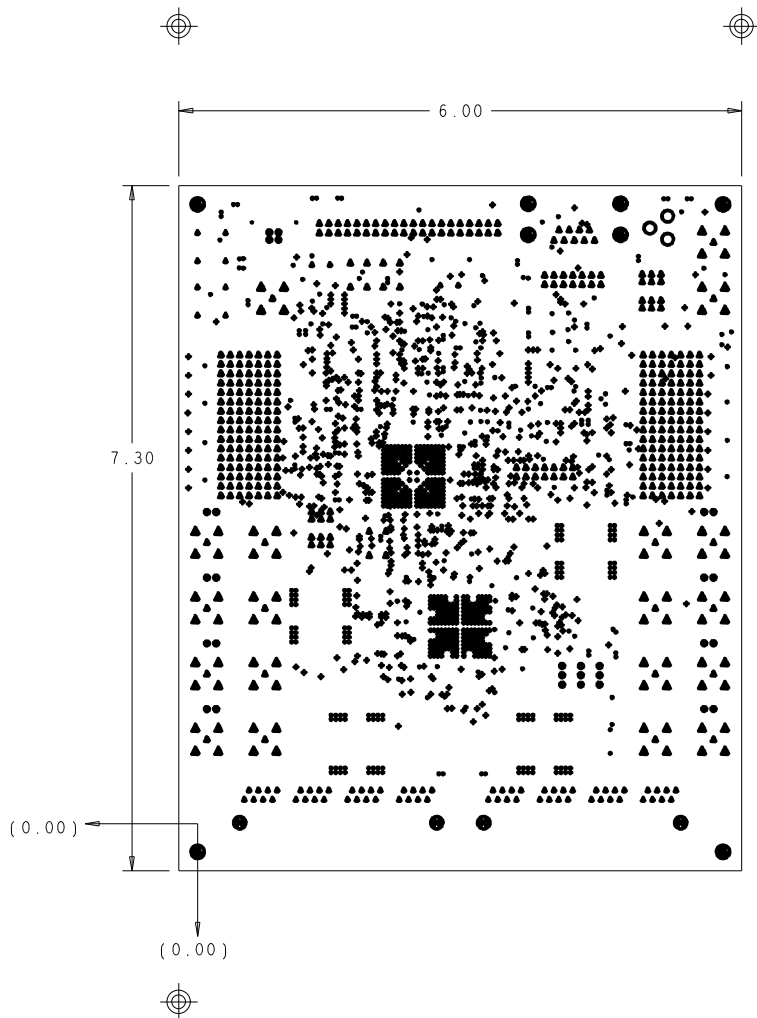
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HOLE CHART: TOP to BOTTOM			
ALL UNITS ARE IN MILS			
FIGURE	SIZE	PLATED	QTY
•	10.0	PLATED	724
•	10.0	PLATED	450
•	15.0	PLATED	362
•	36.0	PLATED	76
•	41.0	PLATED	18
•	42.0	PLATED	11
•	48.0	PLATED	356
•	71.0	PLATED	76
•	105.0	PLATED	2
○	120.0	PLATED	3
●	140.0	PLATED	4
●	130.0	NON-PLATED	4
●	140.0	NON-PLATED	4

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DRILL.ART

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