







Drafted by: Peep Narusberg  
Checked by: ---  
Approved by: Stephan Kubisch

1		2		3		4		
A	<div>Changelog: TMC8462-BOB-ETH V0.91 -&gt; TMC8462A-BOB-ETH V1.0  1. Created project with PCB from TMC8462-BOB-ETH V0.91 2. Redraw schematic with components from Hedgehog database 3. Used TMC8462A-BA 4. Changed clock to 25 MHz 5. Connected pin TSTCLK_SELECT to GND 6. Added optional pull-up to pin PDI_EMULATION 7. Used components with smaller footprint (0402) 8. Added TVS and 10uF cap to +3.3V net 9. New route 10. Moved LINK/ACT LEDs behind RJ-45 connector and added IN/OUT comments to connector 11. Added Trinamic logo to top silk  TMC8462-BOB-ETH V0.9 -&gt; V0.91 1. Adjusted power connect J1 location 2. Added for signal TSTCLK_SELECT 2 solderable resistors. Default is connection to +3.3V 3. Renumbered resistors</div>		<div>ToDo:</div>				A	
B								