

Design Rules Verification Report

Filename : C:\Users\Peep\Documents\SVN\Trinamic-TMC846x\TMC-8462A-BOB-ETH\TM

Warnings 0
Rule Violations 7

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=0.1mm) ((InDifferentialPairClass('All Differential Pairs'))),(All)	0
Clearance Constraint (Gap=0.1mm) (All),(All)	0
Clearance Constraint (Gap=0.3mm) ((ObjectKind = 'Pad') And (HoleDiameter = AsMM(3.25))),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint (All)	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.14mm) (Max=1.5mm) (Preferred=0.2mm) (All)	0
Routing Via (MinHoleWidth=0.45mm) (MaxHoleWidth=0.9mm) (PreferredHoleWidth=0.5mm) (MinWidth=0.9mm)	0
Routing Via (MinHoleWidth=0.15mm) (MaxHoleWidth=0.3mm) (PreferredHoleWidth=0.2mm) (MinWidth=0.4mm)	0
Power Plane Connect Rule(Relief Connect)(Expansion=0.4mm) (Conductor Width=0.2mm) (Air Gap=0.2mm)	0
Minimum Annular Ring (Minimum=0.125mm) (((ObjectKind = 'Pad') OR (ObjectKind = 'Via')) And (Layer = 'InnerLayer'))	0
Minimum Annular Ring (Minimum=0.225mm) (((ObjectKind = 'Pad') OR (ObjectKind = 'Via')) And (Layer = 'MultiLayer'))	0
Minimum Annular Ring (Minimum=0.1mm) (((ObjectKind = 'Pad') OR (ObjectKind = 'Via')) And (Layer = 'OuterLayer'))	0
Hole Size Constraint (Min=0.15mm) (Max=3.25mm) (All)	0
Net Antennae (Tolerance=0mm) (All)	0
Board Clearance Constraint (Gap=0mm) (All)	7
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm) (HasFootprint('TEST4L')),(All)	0
Component Clearance Constraint (Horizontal Gap = 0mm, Vertical Gap = 0mm) (HasFootprint('FID10X20')),(All)	0
Component Clearance Constraint (Horizontal Gap = 0.25mm, Vertical Gap = 0.25mm) (All),(All)	0
Height Constraint (Min=0mm) (Max=25mm) (Preferred=12.5mm) (All)	0
Total	7

Board Clearance Constraint (Gap=0mm) (All)	
Board Outline Clearance(Outline Edge): (Collision < 0.5mm) Between Arc (17.042mm,36.099mm) on Top Overlay And Board Edge	
Board Outline Clearance(Outline Edge): (Collision < 0.5mm) Between Arc (17.367mm,35.574mm) on Top Overlay And Board Edge	
Board Outline Clearance(Outline Edge): (Collision < 0.5mm) Between Arc (33.216mm,35.574mm) on Top Overlay And Board Edge	
Board Outline Clearance(Outline Edge): (Collision < 0.5mm) Between Arc (33.542mm,36.099mm) on Top Overlay And Board Edge	
Board Outline Clearance(Outline Edge): (Collision < 0.5mm) Between Board Edge And Track (40.992mm,27.764mm)(40.992mm,40.304mm) on Top	
Board Outline Clearance(Outline Edge): (Collision < 0.5mm) Between Board Edge And Track (9.592mm,27.764mm)(9.592mm,40.304mm) on Top	
Board Outline Clearance(Outline Edge): (Collision < 0.5mm) Between Board Edge And Track (9.592mm,40.304mm)(40.992mm,40.304mm) on Top	