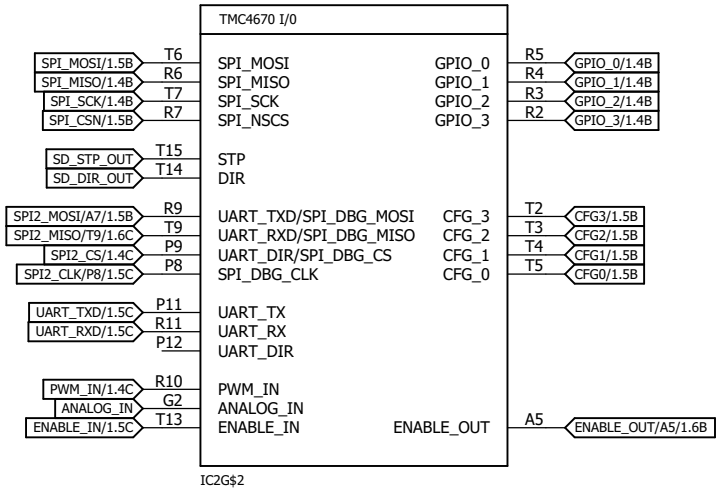
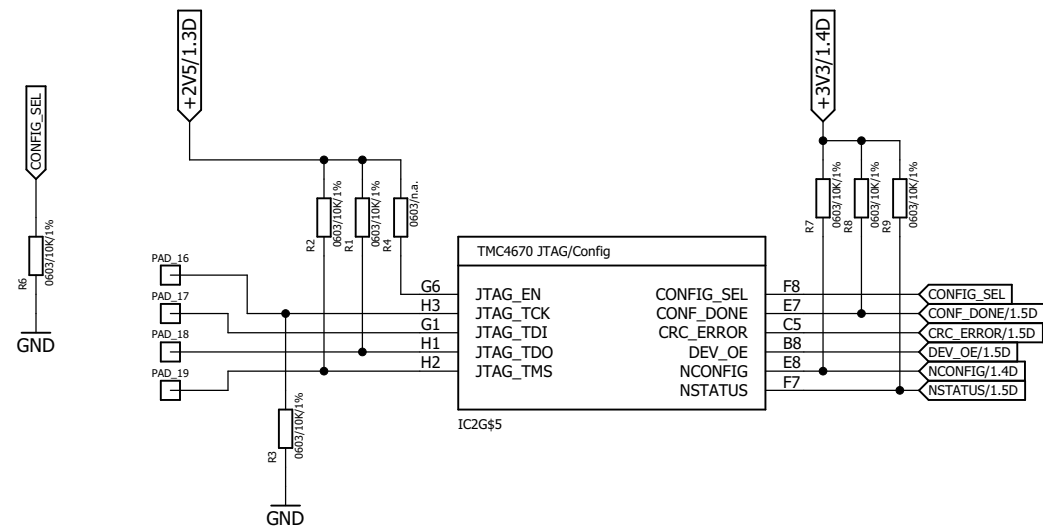
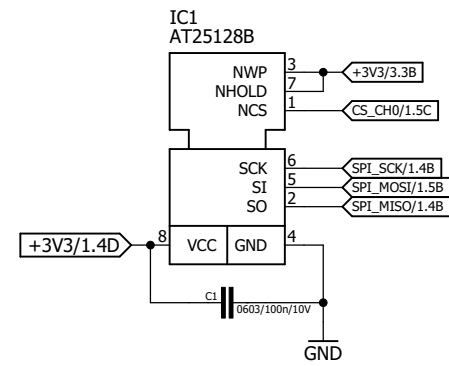
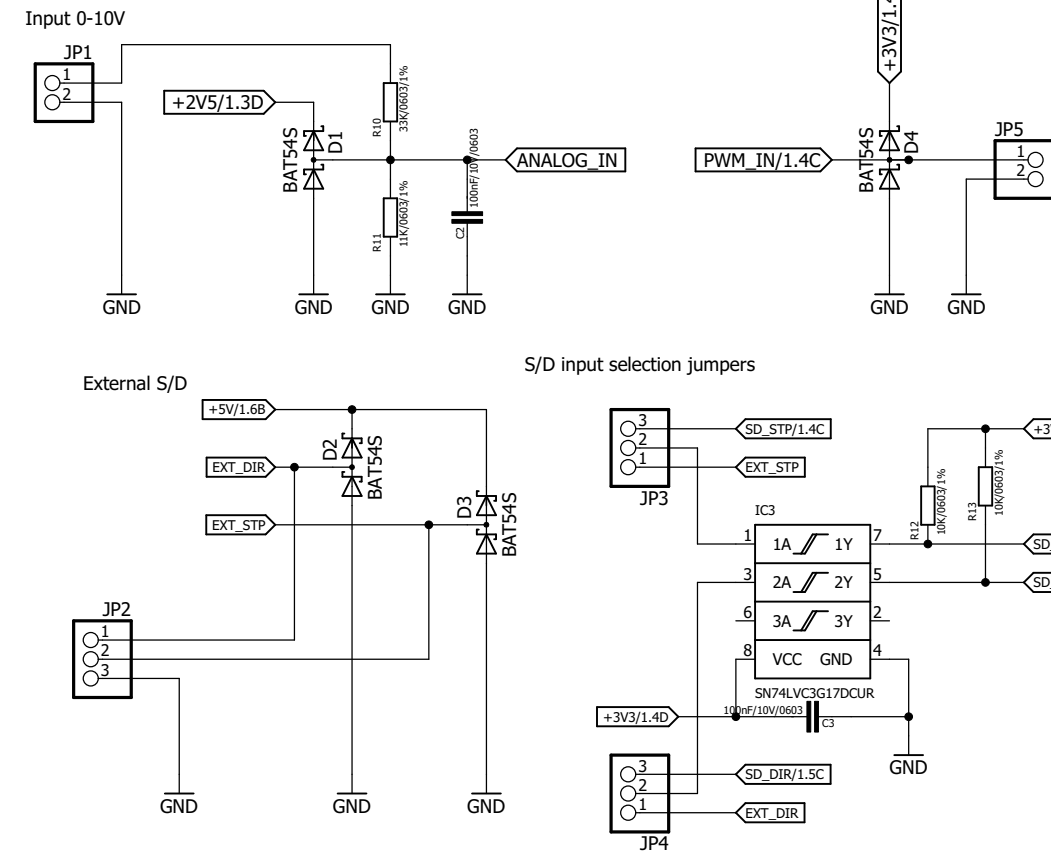


Module identification EEPROM



Motor control inputs



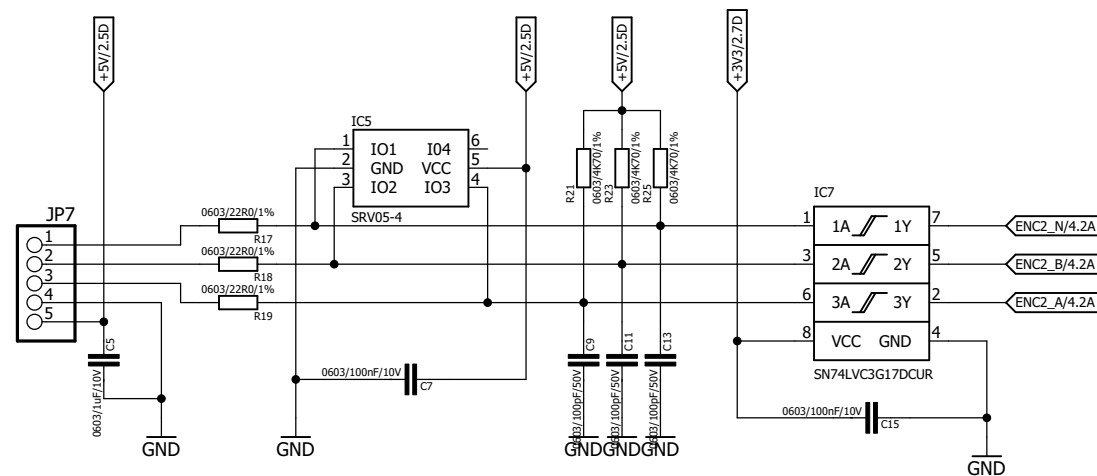
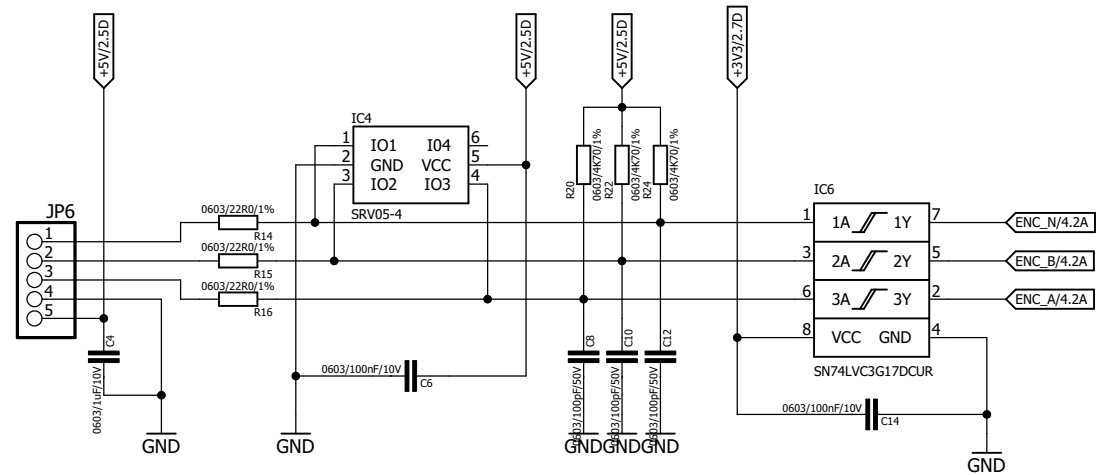
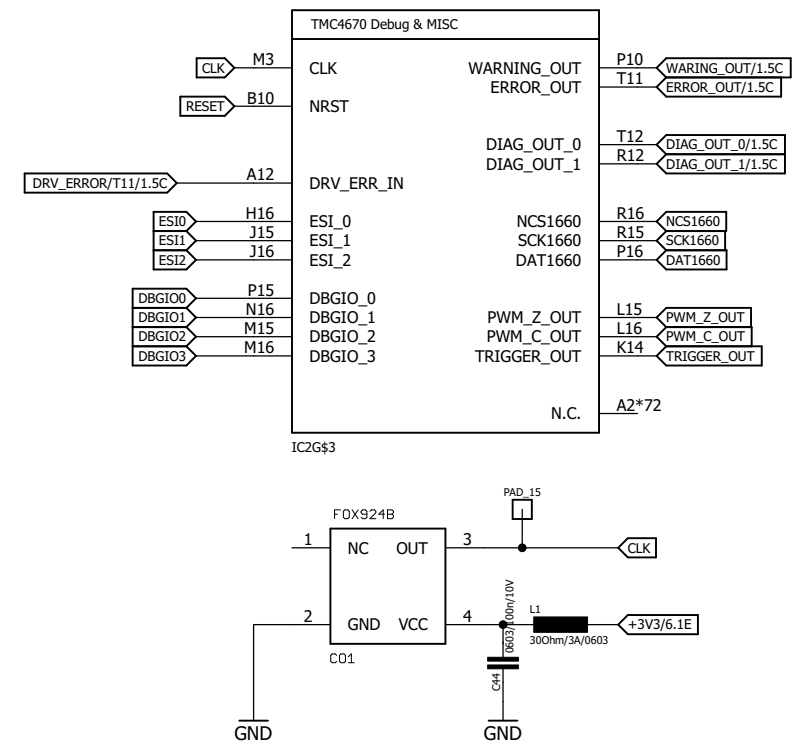


Figure 1 shows four schematic diagrams of the AENC circuit, each corresponding to a different input signal: AENC_WY, AENC_V, AENC_UX, and AENC_N. Each circuit consists of a resistor (R43, R44, R45, R46) connected to the input signal, a 0603/3K3/1% resistor, a 0603/3K3/1% resistor, and a 0603/70PF/10V capacitor. The output is connected to GND.

The schematic diagram illustrates the electrical connections for two test boards: ADC_T_MOSFETS and ADC_T_MOTOR. Both boards feature a 4.2C output connected to a 100nF capacitor (C28 for MOSFETS, C23 for MOTOR) and a 100k resistor (R4 for MOSFETS, R49 for MOTOR) to ground. The input side of each board is connected to a 33k resistor (R47 for MOSFETS, R42 for MOTOR) and a 1.5C (for MOSFETS) or 1.6C (for MOTOR) input. The output of the 33k resistor is connected to the input of the ADC_T_MOSFETS or ADC_T_MOTOR block.

TMC4670 Debug & Clock



Reset

