

Rarely Asked Questions—Issue 135 Amplifier R_F: Think Before You Choose!

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Question:

My system design requires a large amplifier gain, can I just use a large feedback resistor?

Answer:

When selecting the feedback resistor (R_F) for single-ended voltage feedback and a fully differential amplifier, consideration needs to be given to the system requirements. There are trade-offs in the selection of R_F , which include power dissipation, bandwidth, and stability. If speed is critical, as discussed in RAQ Issue 122, "The Truth About Voltage Feedback Resistors," the recommended data sheet R_F value is advised. If power dissipation is critical, and the system requires a higher gain, a larger R_F can be the right choice.

The choices for R_F increase as gain increases. The destabilizing effect between the internal capacitance of the amplifier and the feedback resistor is reduced with larger gains. As gain increases, the amplifier is less sensitive to gain peaking.

The example of Figure 1 shows the lab results of the normalized frequency response for the ADA4807-1, low noise, rail-to-rail input and output, voltage feedback amplifier in a noninverting configuration with an R_F of 10 k Ω for gains of 11 V/V, 21 V/V, and 31 V/V.

The degree of peaking in the small signal frequency response indicates instability. Increasing the gain from 11 V/V to 31 V/V results in less than 1 dB of peaking. This would imply that the amplifier has sufficient phase margin with an $R_{\rm F}$ of 10 $k\Omega$ and is stable at high gains.



Figure 1. Lab results for different gains with $R_F = 10 \ k\Omega$. $V_s = \pm 5 \ V$, $R_{LOAD} = 1 \ k\Omega$ for gains 11 V/V, 21 V/V, and 31 V/V.



Figure 2. Simulation results using the ADA4807 SPICE model. $R_{\rm F} = 10 \ k\Omega, V_{\rm S} = \pm 5 \ V, R_{\rm LOAD} = 1 \ k\Omega$ for gain = 2, and 31 V/V.

Validating a circuit in the lab is not a mandatory step for verifying potential instabilities. Figure 2 shows the simulation results using the SPICE model with gain of 2 V/V and 31 V/V. The instability with using a large gain resistor such as 10 k Ω in a gain of 2 V/V is shown in comparison with the same R_F in a gain of 31 V/V. Figure 3 shows results for gains 11 V/V, 21 V/V, and 31 V/V in the time domain.

There are system trade-offs in the selection of $R_{\scriptscriptstyle F}.$ To achieve full performance from a system, the appropriate $R_{\scriptscriptstyle F}$ choice will depend on the system requirements with respect to stability, bandwidth, and power.



Figure 3. Pulse response simulation results using the ADA4807 SPICE model. $V_s = \pm 5 V$, $R_F = 10 k\Omega$; G = 11 V/V, 21 V/V, and 31 V/V and $R_{L0A0} = 1 k\Omega$.

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