



Product/Process Change Notice - PCN 21_0074 Rev. -

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This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. **Any inquiries or requests with this PCN (additional data or samples) must be sent to ADI within 30 days of publication date.** ADI contact information is listed below.

PCN Title: LTC4366 Datasheet Limit Change

Publication Date: 07-Oct-2021

Effectivity Date: 09-Jan-2022 *(the earliest date that a customer could expect to receive changed material)*

Revision Description:

Initial Release

Description Of Change:

Please be advised that Analog Devices has made a minor changes to the LTC4366 product datasheet to facilitate improvement in manufacturing capability. The changes are shown on the attached pages of the marked-up datasheet.

Electrical Characteristics table changes (page 4 of datasheet):

VDD Shunt Regulator Voltage minimum spec limit changed from 11.5V to 11.0V. Typical value was changed from 12 to 11.7V.

VDD Shunt Regulator Load Regulation maximum spec limit for C, I and H-Grade changed from 90mV to 130mV. Typical value was changed from 30 to 60mV.

VDD Pin Current – Start-Up, Gate High typical value changed from 9 to 8uA.

OUT Shunt Regulator Voltage typical value changed from 5.7 to 5.5V.

OUT Undervoltage Lockout 1 maximum spec limit for C, I and H-Grade changed from 2.75 to 2.80V. Typical value was changed from 2.55 to 2.60V.

BASE Shunt Regulator Voltage (OUT – BASE) typical value changed from 6.2 to 6.1V.

External N-Channel Gate Drive (GATE – OUT) typical value changed from 12 to 11.75V.

GATE Pin Current – Fault minimum spec limit changed from 0.3 to 0.25mA and maximum spec limit changed from 1.20 to 1.25mA.

Electrical Characteristics table changes (page 5 of datasheet):

SD Pin Hysteresis minimum spec limit for C, I and H-Grade changed from 147mV to 129mV. Typical value changed from 280 to 260mV.

SD Pin Input Pull-up Current minimum spec limit for C, I and H-Grade changed from -0.7 to -0.5uA.

TIMER Pin Threshold minimum spec limit changed from 2.6V to 2.5V. Typical value changed from 2.8 to 2.7V.

Timer Pin Pull-Down Current minimum spec limit for C, I and H-Grade changed from 0.9 to 0.7uA.

Cool-Down Timer (Internal) maximum spec limit for C, I and H-Grade changed from 16 to 19 seconds. Typical value was changed from 9 to 10 seconds.

Reason For Change:

To facilitate improvement in manufacturing capability.

Impact of the change (positive or negative) on fit, form, function & reliability:

This datasheet change does not impact the fit, form, function, or reliability of the LTC4366.

Product Identification *(this section will describe how to identify the changed material)*

Product shipped after effectivity date will be tested to the new limits.

The new silicon can be identified with date code and lot traceability identification.

Summary of Supporting Information:

Changes will be reflected on the new product datasheet. See changes on Electrical Characteristics table on pages 4 and 5.

Supporting Documents

Attachment 1: Type: Datasheet Specification Comparison

ADI_PCN_21_0074_Rev_-_LTC4366_Marked-up_Datasheet.pdf

For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.

Americas:

PCN_Americas@analog.com

Europe:

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Japan:

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Rest of Asia:

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Appendix A - Affected ADI Models

Added Parts On This Revision - Product Family / Model Number (48)

LTC4366 / LTC4366CDDDB-1#PBF	LTC4366 / LTC4366CDDDB-1#TRMPBF	LTC4366 / LTC4366CDDDB-1#TRPBF	LTC4366 / LTC4366CDDDB-2#PBF	LTC4366 / LTC4366CDDDB-2#TRMPBF
LTC4366 / LTC4366CDDDB-2#TRPBF	LTC4366 / LTC4366CTS8-1#PBF	LTC4366 / LTC4366CTS8-1#TRMPBF	LTC4366 / LTC4366CTS8-1#TRPBF	LTC4366 / LTC4366CTS8-2#PBF
LTC4366 / LTC4366CTS8-2#TRMPBF	LTC4366 / LTC4366CTS8-2#TRPBF	LTC4366 / LTC4366HDDDB-1#PBF	LTC4366 / LTC4366HDDDB-1#TRMPBF	LTC4366 / LTC4366HDDDB-1#TRPBF
LTC4366 / LTC4366HDDDB-2#PBF	LTC4366 / LTC4366HDDDB-2#TRMPBF	LTC4366 / LTC4366HDDDB-2#TRPBF	LTC4366 / LTC4366HTS8-1#PBF	LTC4366 / LTC4366HTS8-1#TRMPBF
LTC4366 / LTC4366HTS8-1#TRPBF	LTC4366 / LTC4366HTS8-2#PBF	LTC4366 / LTC4366HTS8-2#TRMPBF	LTC4366 / LTC4366HTS8-2#TRPBF	LTC4366 / LTC4366IDDB-1#PBF
LTC4366 / LTC4366IDDB-1#TRMPBF	LTC4366 / LTC4366IDDB-1#TRPBF	LTC4366 / LTC4366IDDB-2#PBF	LTC4366 / LTC4366IDDB-2#TRMPBF	LTC4366 / LTC4366IDDB-2#TRPBF
LTC4366 / LTC4366ITS8-1#PBF	LTC4366 / LTC4366ITS8-1#TRMPBF	LTC4366 / LTC4366ITS8-1#TRPBF	LTC4366 / LTC4366ITS8-2#PBF	LTC4366 / LTC4366ITS8-2#TRMPBF
LTC4366 / LTC4366ITS8-2#TRPBF	LTC4366 / LTC4366MPDDB-1#PBF	LTC4366 / LTC4366MPDDB-1#TRMPBF	LTC4366 / LTC4366MPDDB-1#TRPBF	LTC4366 / LTC4366MPDDB-2#PBF
LTC4366 / LTC4366MPDDB-2#TRMPBF	LTC4366 / LTC4366MPDDB-2#TRPBF	LTC4366 / LTC4366MPTS8-1#PBF	LTC4366 / LTC4366MPTS8-1#TRMPBF	LTC4366 / LTC4366MPTS8-1#TRPBF
LTC4366 / LTC4366MPTS8-2#PBF	LTC4366 / LTC4366MPTS8-2#TRMPBF	LTC4366 / LTC4366MPTS8-2#TRPBF		

Appendix B - Revision History			
Rev	Publish Date	Effectivity Date	Rev Description
Rev. -	07-Oct-2021	09-Jan-2022	Initial Release

Analog Devices, Inc.

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